# UTC UNISONIC TECHNOLOGIES CO., LTD

# 2SA1020

## PNP SILICON TRANSISTOR

## SILICON PNP EPITAXIAL TRANSISTOR

#### **DESCRIPTION**

The UTC 2SA1020 is designed for power amplifier and power switching applications.

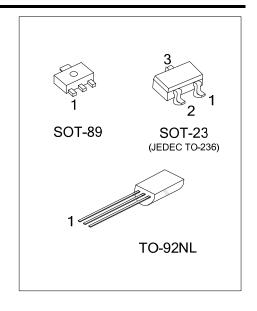
#### **FEATURES**

\*Low collector saturation voltage:

 $V_{CE(SAT)}$ =-0.5 $V_{(MAX)}$  (I<sub>C</sub>= -1A)

\*High speed switching time: t<sub>STG</sub>=1.0µs(TYP)

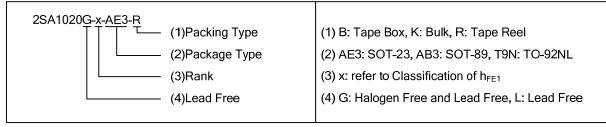
\*Complement to UTC 2SC2655



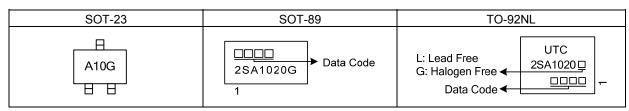
#### **ORDERING INFORMATION**

Ordering Number		Doolsons	Pin Assignment			Dealine	
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	2SA1020G-x-AE3-R	SOT-23	Е	В	С	Tape Reel	
-	2SA1020G-x-AB3-R	SOT-89	В	С	Е	Tape Reel	
2SA1020L-x-T9N-B	2SA1020G-x-T9N-B	TO-92NL	Е	C	В	Tape Box	
2SA1020L-x-T9N-K	2SA1020G-x-T9N-K	TO-92NL	Е	C	В	Bulk	

Note: Pin Assignment: B: Base C: Collector E: Emitter



#### **MARKING**



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#### ■ **ABSOLUTE MAXIMUM RATINGS** (T<sub>A</sub>=25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Collector-Base Voltage		$V_{CBO}$	-50	V
Collector-Emitter Voltage		$V_{CEO}$	-50	V
Emitter-Base Voltage		$V_{EBO}$	-5	V
Collector Current		lc	-2	Α
Collector Power Dissipation	SOT-23	P <sub>C</sub>	300	mW
	SOT-89		500	mW
	TO-92NL		900	mW
Junction Temperature		$T_J$	150	°C
Storage Temperature		T <sub>STG</sub>	-55 ~ <b>+</b> 150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

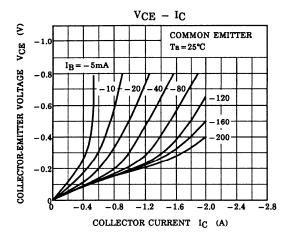
#### ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, unless otherwise specified)

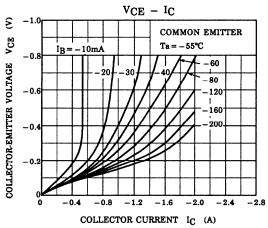
			•			
ARAMETER	SYMBOL	TEST CONDITIONS		TYP	MAX	UNIT
Collector to Emitter Breakdown Voltage		I <sub>C</sub> =-10mA, I <sub>B</sub> =0				V
Collector Cut-off Current		V <sub>CB</sub> =-50V, I <sub>E</sub> =0			-1.0	μΑ
Emitter Cut-off Current		V <sub>EB</sub> =-5V, I <sub>C</sub> =0			-1.0	μΑ
DC Current Gain		V <sub>CE</sub> =-2V, I <sub>C</sub> =-0.5A			240	
		V <sub>CE</sub> =-2V, I <sub>C</sub> =-1.5A				
ter Saturation Voltage	V <sub>CE(SAT)</sub>	I <sub>C</sub> =-1A, I <sub>B</sub> =-0.05A			-0.5	V
Base to Emitter Saturation Voltage		I <sub>C</sub> =-1A, I <sub>B</sub> =-0.05A			-1.2	V
ency	f <sub>T</sub>	V <sub>CE</sub> =-2V, I <sub>c</sub> =-0.5A		100		MHz
Capacitance	Сов	V <sub>CB</sub> =-10V, I <sub>E</sub> =0, f=1MHz		40		pF
Turn-on Time	t <sub>ON</sub>	INPUT IB2 OUTPUT		0.1		μs
Storage Time	t <sub>STG</sub>	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		1.0		μs
Fall Time	t <sub>F</sub>			0.1		μs
	Current Current  Iter Saturation Voltage Saturation Voltage ency Capacitance Turn-on Time Storage Time	Turn-on Time   Turn	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	ter Breakdown Voltage $BV_{CEO}$ $I_{C}$ =-10mA, $I_{B}$ =0 $-50$ $Current$ $I_{CBO}$ $V_{CB}$ =-50V, $I_{E}$ =0 $V_{CB}$ -50V, $I_{C}$ =0 $V_{CB}$ -5V, $I_{C}$ =0 $V_{CE}$ -2V, $I_{C}$ -0.5A $V_{CE}$ -2V, $I_{C}$ -1.5A $V_{CE}$ -2V, $I_{C}$ -10.5A $V_{CE}$ -2V, $I_{C}$ -1A, $I_{B}$ -0.05A $V_{CE}$ -2V, $I_{C}$ -0.5A $V_{CE}$ -10V, $I_{C}$ -1B1 $I_{C}$ -30V $V_{CC}$ -30V $V_{CC}$ -30V	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

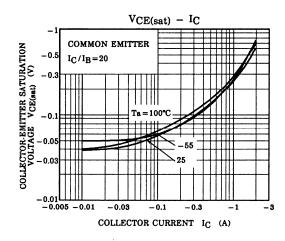
### ■ CLASSIFICATION OF h<sub>FE1</sub>

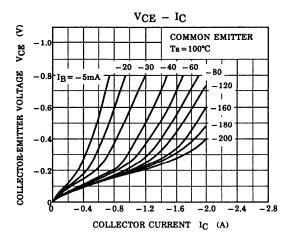
RANK	0	Y
RANGF	70 - 140	120 - 240

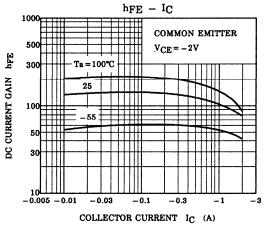
#### **■ TYPICAL CHARACTERISTICS**

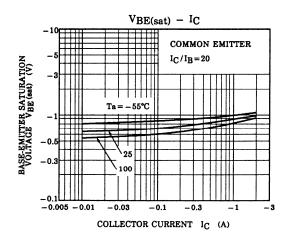




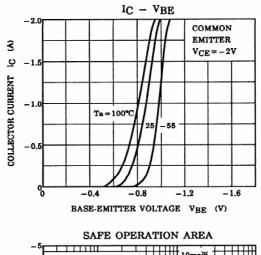


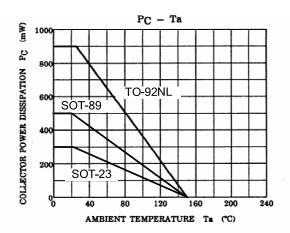


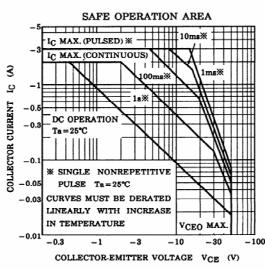




#### **■ TYPICAL CHARACTERISTICS(Cont.)**







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