UNISONIC TECHNOLOGIES CO., LTD

2SB772S

PNP SILICON TRANSISTOR

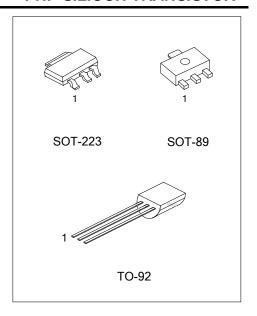
MEDIUM POWER LOW **VOLTAGE TRANSISTOR**

DESCRIPTION

The UTC 2SB772S is a medium power low voltage transistor, designed for audio power amplifier, DC-DC converter and voltage regulator.

■ FEATURES

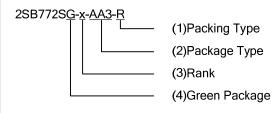
- * High current output up to 3A
- * Low saturation voltage
- * Complement to 2SD882S



ORDERING INFORMATION

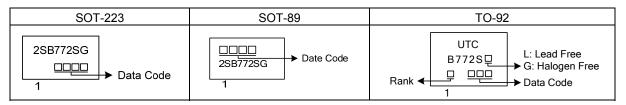
Ordering	Dackago	Pin Assignment			Dooking		
Lead Free	Halogen Free	Package	1	2	3	Packing	
-	2SB772SG-x-AA3-R	SOT-223	В	С	Е	Tape Reel	
-	2SB772SG-x-AB3-R	SOT-89	В	С	Е	Tape Reel	
2SB772SL-x-T92-B	2SB772SG-x-T92-B	TO-92	Е	O	В	Tape Box	
2SB772SL-x-T92-K	2SB772SG-x-T92-K	TO-92	Е	С	В	Bulk	

Pin Assignment: C: Collector B: Base E: Emitter



- (1) B: Tape Box, K: Bulk, R: Tape Reel
- (2) AA3: SOT-223, AB3: SOT-89, T92: TO-92
- (3) x: refer to Classification of hFE2
- (4) G: Halogen Free and Lead Free, L: Lead Free

MARKING



www.unisonic.com.tw 1 of 3

■ ABSOLUTE MAXIMUM RATINGS (T_A= 25°C, unless otherwise specified)

PARAMETEI	R	SYMBOL	RATINGS	UNIT
Collector-Base Voltage		V_{CBO}	-40	V
Collector-Emitter Voltage		$V_{\sf CEO}$	-30	V
Emitter-Base Voltage		V_{EBO}	-5	V
Peak Collector Current		I _{CP}	-7	Α
DC Collector Current		Ic	-3	Α
Base Current		I _B	-0.6	Α
	SOT-89		0.5	W
Power Dissipation	SOT-223	P_{D}	1	W
	TO-92		0.5	W
Junction Temperature		T_J	+150	°C
Storage Temperature	•	T _{STG}	-55 ~ +150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ ELECTRICAL CHARACTERISTICS (T_A= 25°C, unless otherwise specified)

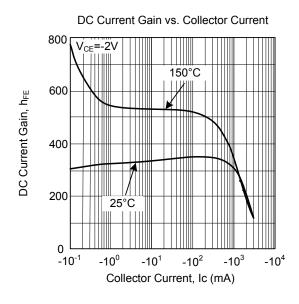
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV_CBO	I _C =-100μA, I _E =0	-40			V
Collector-Emitter Breakdown Voltage	BV_CEO	I _C =-1mA, I _B =0	-30			V
Emitter-Base Breakdown Voltage	BV_{EBO}	I _E =-100μA, I _C =0	-5			V
Collector Cut-Off Current	I _{CBO}	V_{CB} =-30V, I_E =0			-1000	nA
Collector Cut-Off Current	I _{CEO}	V _{CE} =-30V, I _B =0			-1000	nA
Emitter Cut-Off Current	I _{EBO}	V_{EB} =-3 V , I_{C} =0			-1000	nA
DC Current Coin(Note 1)	h _{FE1}	V _{CE} =-2V, I _C =-20mA	30	200		
DC Current Gain(Note 1)	h _{FE2}	V_{CE} =-2V, I_{C} =-1A	100	150	400	
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	I _C =-2A, I _B =-0.2A		-0.3	-0.5	V
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	I _C =-2A, I _B =-0.2A		-1.0	-2.0	V
Current Gain Bandwidth Product	f_T	V _{CE} =-5V, I _C =-0.1A		80		MHz
Output Capacitance	Сов	V_{CB} =-10V, I_E =0, f=1MHz		45		pF

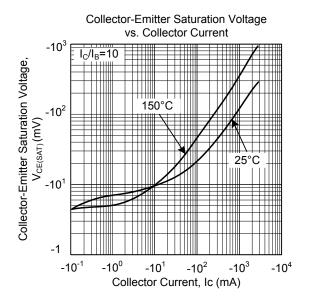
Note 1: Pulse test: P_W<300µs, Duty Cycle<2%

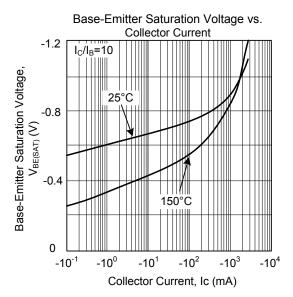
■ CLASSIFICATION OF h_{FE2}

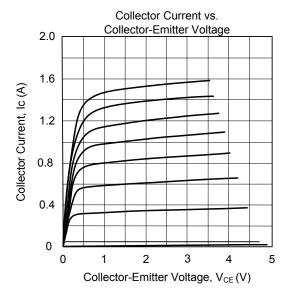
RANK	Q	Р	Е	
RANGE	100 ~ 200	160 ~ 320	200 ~ 400	

■ TYPICAL CHARACTERICS









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