

## 4N65K-TC 4A, 650V N-CHANNEL POWER MOSFET

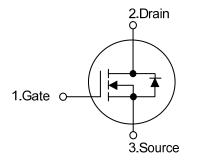
### DESCRIPTION

The UTC **4N65K-TC** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristic. This power MOSFET is usually used in high speed switching applications including power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

#### FEATURES

- \*  $R_{DS(ON)}$  < 2.5 $\Omega$  @  $V_{GS}$  = 10 V,  $I_D$  = 2.0 A
- \* Fast Switching Capability
- \* Avalanche Energy Specified
- \* Improved dv/dt Capability, High Ruggedness

#### SYMBOL



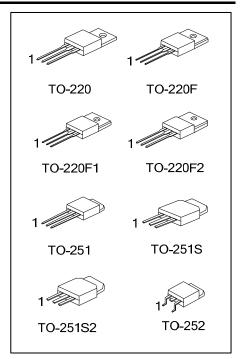
#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Packing
Lead Free	Halogen Free	Package	1	2	3	Facking
4N65KL-TA3-T	4N65KG-TA3-T	TO-220	G	D	S	Tube
4N65KL-TF1-T	4N65KG-TF1-T	TO-220F1	G	D	S	Tube
4N65KL-TF2-T	4N65KG-TF2-T	TO-220F2	G	D	S	Tube
4N65KL-TF3-T	4N65KG-TF3-T	TO-220F	G	D	S	Tube
4N65KL-TM3-T	4N65KG-TM3-T	TO-251	G	D	S	Tube
4N65KL-TMS2-T	4N65KG-TMS2-T	TO-251S2	G	D	S	Tube
4N65KL-TN3-R	4N65KG-TN3-R	TO-252	G	D	S	Tape Reel

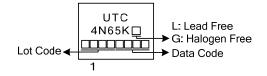
Note: Pin Assignment: G: Gate D: Drain S: Source

4N65K <u>Ģ-TA3-Ţ</u>		(1) T: Tube, R: Tape Reel
(1)	Packing Type	(2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1,
(2)	Package Type	TF2: TO-220F2, TM3: TO-251, TMS: TO-251S
(3)	Green Package	TMS2: TO-251S2, TN3: TO-252
	Ŭ	(3) G: Halogen Free and Lead Free, L: Lead Free

## **Power MOSFET**



### MARKING





PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V <sub>DSS</sub>	650	V	
Gate-Source Voltage		V <sub>GSS</sub>	±30	V	
Drain Current	Continuous	I <sub>D</sub>	4.0	А	
	Pulsed (Note2)	I <sub>DM</sub>	16	А	
Avalanche Energy	Single Pulsed (Note3)	E <sub>AS</sub>	113	mJ	
Peak Diode Recovery d	lv/dt (Note4)	dv/dt	3.79	V/ns	
*	TO-220		106	W	
	TO-220F/TO-220F1		36	W	
Power Dissipation	TO-220F2		30	VV	
	TO-251/TO-251S		50	W	
	TO-251S2/TO-252	P <sub>D</sub>	50	vv	
	TO-220		0.84	W/°C	
	TO-220F/TO-220F1		0.29	W/°C	
rain Current C valanche Energy Si eak Diode Recovery dv/dt (I ru power Dissipation Tu tru tru tru tru tru tru tru tru tru tr	TO-220F2		0.29	VV/ C	
	TO-251/TO-251S		0.40	W/°C	
	TO-251S2/TO-252			VV/ C	
Junction Temperature		TJ	+150	°C	
Operating Temperature		T <sub>OPR</sub>	-55 ~ +150	°C	
Storage Temperature		T <sub>STG</sub>	-55 ~ +150	°C	

#### ■ ABSOLUTE MAXIMUM RATINGS (T<sub>c</sub> = 25°C, unless otherwise specified)

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

- 3. L=25mH, I<sub>AS</sub>=3.0A, V<sub>DD</sub>=50V, R<sub>G</sub>=25  $\Omega$ , Starting T<sub>J</sub> = 25°C
- 4.  $I_{SD}\leq4.0A$ , di/dt $\leq200A/\mu s$ ,  $V_{DD}\leq BV_{DSS}$ , Starting  $T_{J}$  = 25°C

#### THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction-to-Ambient	TO-220/TO-220F TO-220F1/TO-220F2	0	62.5	°C/W
	TO-251/TO-251S TO-251S2/TO-252	$\theta_{JA}$	83	°C/W
Junction-to-Case	TO-220		1.18	°C/W
	TO-220F/TO-220F1		3.4	°C/W
	TO-220F2	θ <sub>JC</sub>	3.57	°C/W
	TO-251/TO-251S TO-251S2/TO-252		2.5	°C/W



DADAMETED	SYMBOL	TEST CONDITIONS	MIN	TVD	MAX	
PARAMETER OFF CHARACTERISTICS	STINBUL	TEST CONDITIONS	IVIIIN	רזין	IVIAX	
Drain-Source Breakdown Voltage	D\/	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250µA	650			V
	BV <sub>DSS</sub>		000		10	-
Drain-Source Leakage Current	I <sub>DSS</sub>	$V_{DS} = 650 \text{ V}, V_{GS} = 0 \text{ V}$			100	μA
Gate-Source Leakage Current	GSS	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$				nA nA
Reverse Breakdown Voltage Temperature Coefficient		$V_{GS}$ = -30 V, $V_{DS}$ = 0 V I <sub>D</sub> =250µA, Referenced to 25°C	°C 0.6		-100	V/°C
ON CHARACTERISTICS				0.0		V/ C
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250µA	2.0		4.0	V
Static Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 2.0 \text{ A}$	2.0		2.5	Ω
	TUS(ON)				2.0	32
Input Capacitance C <sub>ISS</sub>				490		pF
Output Capacitance	C <sub>OSS</sub>	$V_{DS} = 25 V, V_{GS} = 0V,$		54		pF
Reverse Transfer Capacitance		f = 1MHz		5.8		pF
SWITCHING CHARACTERISTICS	-100	I				- F
Total Gate Charge	Q <sub>G</sub>	$V_{DS}$ =50V, $V_{GS}$ =10V, $I_{D}$ =1.3A		32		nC
Gate-Source Charge	Q <sub>GS</sub>			4.2		nC
Gate-Drain Charge	Q <sub>GD</sub>	I <sub>G</sub> = 100μΑ (Note1, 2)		5.6		nC
Turn-On Delay Time	t <sub>D(ON)</sub>			44		ns
Turn-On Rise Time	t <sub>R</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =10V, I <sub>D</sub> =0.5A,		28		ns
Turn-Off Delay Time	t <sub>D(OFF)</sub>	R <sub>G</sub> =25Ω (Note1, 2)		116		ns
Turn-Off Fall Time	t <sub>F</sub>			33		ns
SOURCE- DRAIN DIODE RATINGS AND	CHARACTERIS	STICS	_	_	_	
Maximum Continuous Drain-Source Diode					4.0	۸
Forward Current	IS	Is			4.0	A
Maximum Pulsed Drain-Source Diode	la				16	А
Forward Current	I <sub>SM</sub>				10	А
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 4.0A	1		1.4	V
Reverse Recovery Time	trr	$V_{GS} = 0V, I_S = 4.0A,$		360		nS
Reverse Recovery Charge	Q <sub>rr</sub>	dI <sub>F</sub> / dt =100A/µs (Note 1)		1.42		μC

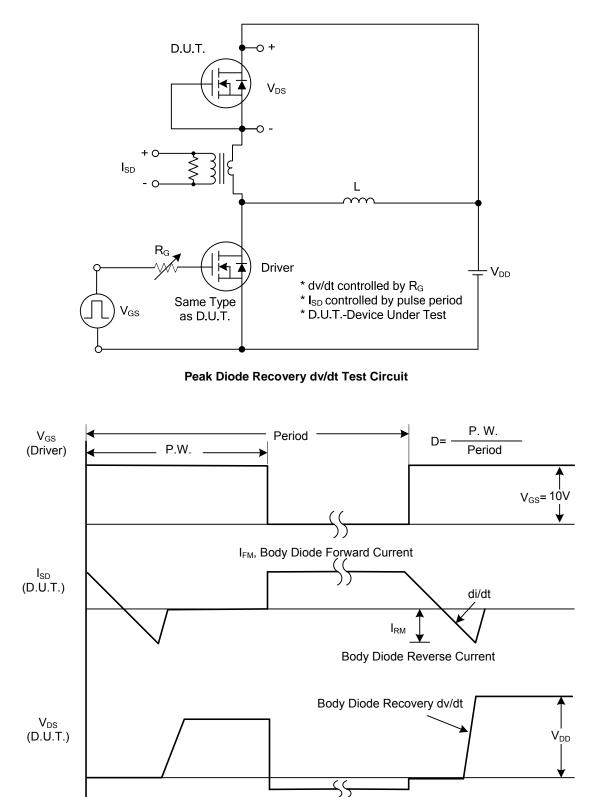
#### ■ ELECTRICAL CHARACTERISTICS (T<sub>C</sub> =25°C, unless otherwise specified)

Notes: 1. Pulse Test: Pulse width  $\leq$  300µs, Duty cycle $\leq$ 2%.

2. Essentially independent of operating temperature.



### TEST CIRCUITS AND WAVEFORMS



Peak Diode Recovery dv/dt Waveforms

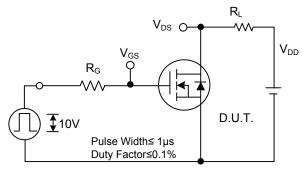
Body Diode

Forward Voltage Drop

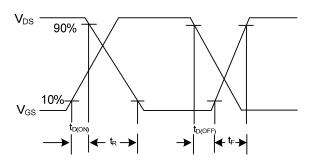


## **Power MOSFET**

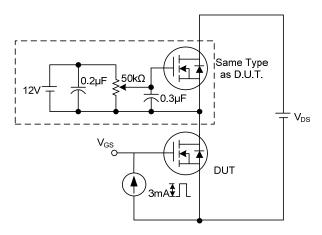
### ■ TEST CIRCUITS AND WAVEFORMS (Cont.)



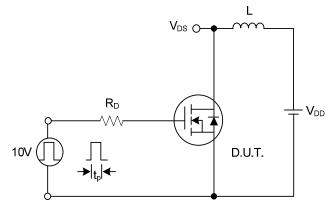
Switching Test Circuit



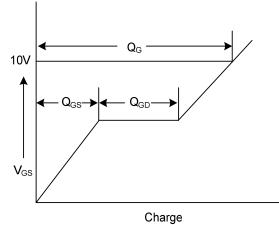
Switching Waveforms



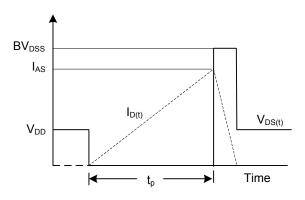
Gate Charge Test Circuit



**Unclamped Inductive Switching Test Circuit** 



Gate Charge Waveform

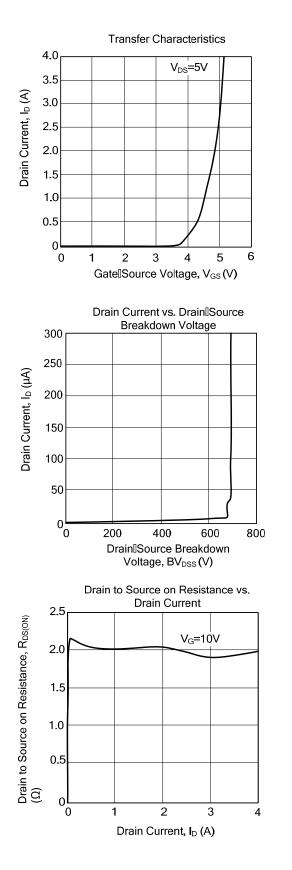


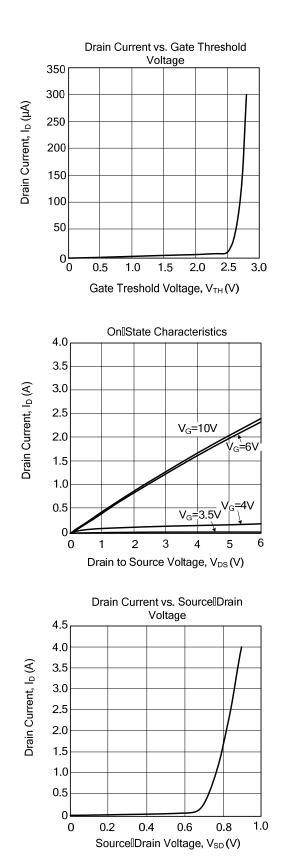
**Unclamped Inductive Switching Waveforms** 



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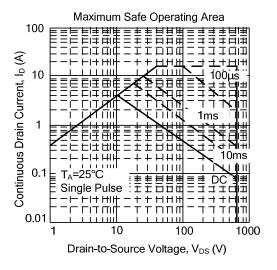
### TYPICAL CHARACTERISTICS







### TYPICAL CHARACTERISTICS (Cont.)



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