

5A, 650V N-CHANNEL POWER MOSFET

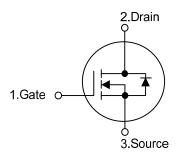
DESCRIPTION

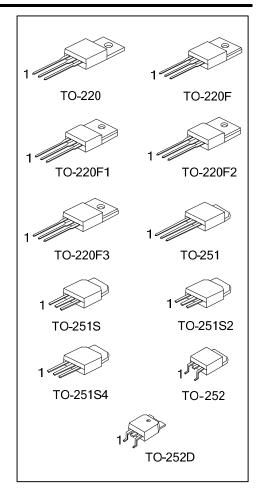
The UTC **5N65K-MT** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications at power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits.

■ FEATURES

- * $R_{DS(ON)}$ < 2.40 @ V_{GS} = 10 V, I_{D} = 2.5 A
- * Fast Switching Capability
- * Improved dv/dt Capability, High Ruggedness

■ SYMBOL

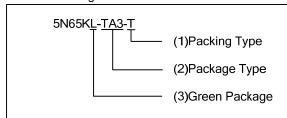




■ ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
5N65KL-TA3-T	5N65KG-TA3-T	TO-220	G	D	S	Tube	
5N65KL-TF3-T	5N65KG-TF3-T	TO-220F	G	D	S	Tube	
5N65KL-TF1-T	5N65KG-TF1-T	TO-220F1	G	D	S	Tube	
5N65KL-TF2-T	5N65KG-TF2-T	TO-220F2	G	D	S	Tube	
5N65KL-TF3-T	5N65KG-TF3-T	TO-220F3	G	D	S	Tube	
5N65KL-TM3-T	5N65KG-TM3-T	TO-251	G	D	S	Tube	
5N65KL-TMS-T	5N65KG-TMS-T	TO-251S	G	D	S	Tube	
5N65KL-TMS2-T	5N65KG-TMS2-T	TO-251S2	G	D	S	Tube	
5N65KL-TMS4-T	5N65KG-TMS4-T	TO-251S4	G	D	S	Tube	
5N65KL-TN3-R	5N65KG-TN3-R	TO-252	G	D	S	Tape Reel	
5N65KL-TND-R	5N65KG-TND-R	TO-252D	G	D	S	Tape Reel	

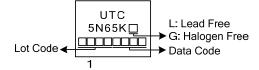
Note: Pin Assignment: G: Gate D: Drain S: Source



- (1) T: Tube, R: Tape Reel
- (2) TA3: TO-220, TF3: TO-220F, TF1: TO-220F1, TF2: TO-220F2, TF3: TO-220F3, TM3: TO-251 TMS: TO-251S, TMS2: TO-251S2,

TMS4: TO-251S4, TN3: TO-252, TND: TO-252D (3) L: Lead Free, G: Halogen Free and Lead Free

■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (T_C = 25°C, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	650	V	
Gate-Source Voltage		V_{GSS}	±30	V	
Avalanche Current (Not	e 2)	I_{AR}	5	Α	
Continuous Drain Current		I_{D}	5	Α	
Pulsed Drain Current (N	ote 2)	I_{DM}	20	Α	
Avalanaha Enargy	Single Pulsed (Note 3)	E _{AS}	160	m l	
Avalanche Energy	Repetitive (Note 2)	E _{AR}	10	mJ	
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns	
	TO-220		108		
Power Dissipation	TO-220F/TO-220F1 TO-220F3			36	
	TO-220F2	P_{D}	38	W	
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D		54		
Junction Temperature		ΤJ	+150	°C	
Operation Temperature		T _{OPR}	-55 ~ +150	°C	
Storage Temperature		T _{STG}	-55 ~ +150	°C	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

- 2. Pulse width limited by $T_{J(\mbox{\scriptsize MAX})}$
- 3. L=12.8mH, I_{AS} =5A, V_{DD} =50V, R_{G} =25 Ω , Starting T_{J} = 25°C
- 4. $I_{SD} \le 5A$, di/dt $\le 200A/\mu s$, $V_{DD} \le BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F TO-220F1/TO-220F2 TO-220F3	0	62.5	°C/W
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D	$ heta_{ m JA}$	110	°C/W
Junction to Case	TO-220		1.15	°C/W
	TO-220F/TO-220F1 TO-220F3		3.47	°C/W
	TO-220F2	θ_{JC}	3.28	°C/W
	TO-251/TO-251S TO-251S2/TO-251S4 TO-252/TO-252D		2.3	°C/W

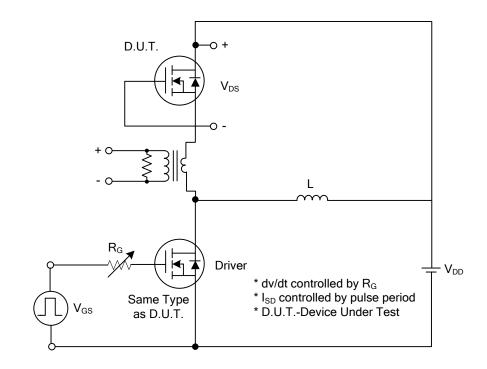
■ ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS								
Drain-Source Breakdown Voltage		BV _{DSS}	$V_{GS} = 0V, I_D = 250\mu A$	650			V	
Drain-Source Leakage Current		I _{DSS}	V _{DS} =650V, V _{GS} = 0V			1	μΑ	
Coto Source Leakage Current For	rward	- I _{GSS}	$V_{GS} = 30V, V_{DS} = 0V$			100	nA	
Gate-Source Leakage Current Re	verse		V_{GS} =-30V, V_{DS} = 0V			-100		
Breakdown Voltage Temperature Coefficient		$\triangle BV_{DSS}/\triangle T_J$	I_D =250 μ A, Referenced to 25°C		0.6		V/°C	
ON CHARACTERISTICS	ON CHARACTERISTICS							
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	2.0		4.0	V	
Static Drain-Source On-State Resistance		R _{DS(ON)}	$V_{GS} = 10V, I_D = 2.5A$		1.47	2.4	Ω	
DYNAMIC CHARACTERISTICS								
Input Capacitance	Input Capacitance		$V_{DS} = 25V, V_{GS} = 0V,$		475	670	pF	
Output Capacitance		Coss	f = 1.0MHz		65	72	pF	
Reverse Transfer Capacitance		C_{RSS}	1 - 1.01/11/12		6.5	8.5	pF	
SWITCHING CHARACTERISTICS								
Turn-On Delay Time		$t_{D(ON)}$	V _{DD} = 30V, I _D =0.5A,		50	60	ns	
Turn-On Rise Time		t_R			42	60	ns	
Turn-Off Delay Time		$t_{D(OFF)}$	$R_G = 25\Omega \text{ (Note 1, 2)}$		180	210	ns	
urn-Off Fall Time		t_{F}			56	100	ns	
Total Gate Charge		Q_G	V _{DS} = 50 V, I _D = 1.3A,		18	23	nC	
Gate-Source Charge		Q_GS	$V_{GS} = 30 \text{ V}, I_D = 1.3\text{A},$ $V_{GS} = 10 \text{ V} \text{ (Note 1, 2)}$		6.4		nC	
Gate-Drain Charge		Q_GD	V _{GS} = 10 V (Note 1, 2)		4		nC	
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS								
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS} = 0 \text{ V}, I_{S} = 5\text{A}$			1.4	V	
Maximum Continuous Drain-Source Diode		Is				5	Α	
Forward Current								
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				20	Α	

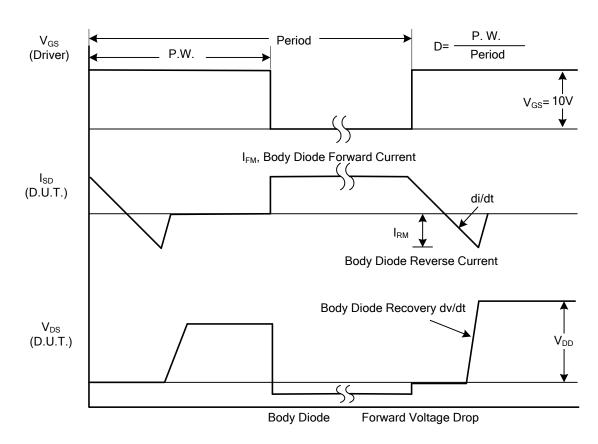
Note: 1. Pulse Test: Pulse width ≤ 300µs, Duty cycle ≤ 2%

^{2.} Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

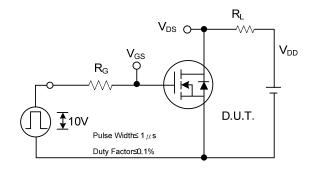


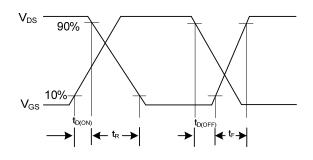
Peak Diode Recovery dv/dt Test Circuit



Peak Diode Recovery dv/dt Waveforms

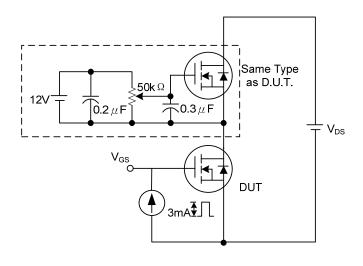
■ TEST CIRCUITS AND WAVEFORMS (Cont.)

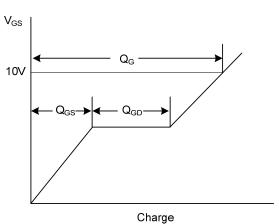




Switching Test Circuit

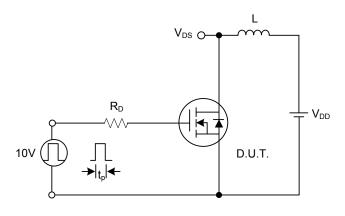
Switching Waveforms





Gate Charge Test Circuit

Gate Charge Waveform



Unclamped Inductive Switching Test Circuit

Unclamped Inductive Switching Waveforms

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