United **SiC**







650V 60A SiC Merged PiN-Schottky Diode

Revision C, January 2019

Description

United Silicon Carbide, Inc. offers the 3rd generation of high performance SiC Merged-PiN-Schottky (MPS) diodes. With zero reverse recovery charge and 175°C maximum junction temperature, these diodes are ideally suited for high frequency and high efficiency power systems with minimum cooling requirements.

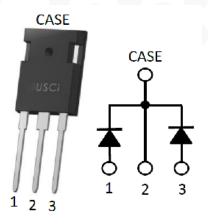
Features

- 175°C maximum operating junction temperature
- Easy paralleling
- Extremely fast switching not dependent on temperature
- No reverse or forward recovery
- Enhanced surge current capability, MPS structure
- Excellent thermal performance, Ag sintered
- 100% UIS tested
- AEC-Q101 qualified

Typical applications

- Power converters
- Industrial motor drives
- Switching-mode power supplies
- Power factor correction modules

JJ3D06560KSD



Part Number	Package	Marking
UJ3D06560KSD	TO-247-3L	UJ3D06560KSD











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Maximum Ratings

Paramater	Symbol	Test Conditions	Value (Leg/ Device)	Units	
DC blocking voltage	V _R		650	V	
Repetitive peak reverse voltage, T _i =25°C	V _{RRM}		650	V	
Surge peak reverse voltage	V _{RSM}		650	V	
Maximum DC forward current	I _F	T _c = 140°C	30/60	А	
Non-repetitive forward surge current sine halfwave	I _{FSM}	$T_{c} = 25^{\circ}C, t_{p} = 10ms$	165/330	A	
		T _c = 110°C, t _p = 10ms	150/300		
Repetitive forward surge current sine halfwave, D=0.1	I _{FRM}	$T_{c} = 25^{\circ}C, t_{p} = 10ms$	107.2/214.4	А	
		T _c = 110°C, t _p = 10ms	66.1/132.2		
Non-repetitive peak forward current	I _{F, max}	T _c = 25°C, t _p = 10μs	1250/2500	A	
		T _c = 110°C, t _p = 10μs	1250/2500		
i²t value	∫i²dt	T _c = 25°C, t _p = 10ms	136/544	A ² s	
		T _c = 110°C, t _p = 10ms	112/448		
Diode dV/dt ruggedness	dV/dt	V _R = 0 - 650V	200	V/ns	
Power dissipation	P _{tot}	T _c = 25°C	288.5/577	W	
		T _c = 140°C	67.3/134.6		
Maximum junction temperature	T _{J, max}		175	°C	
Operating and storage temperature	T _, , T _{stg}		-55 to 175	°C	
Soldering temperatures, wavesoldering only allowed at	T _{sold}	1.6mm from case for 10s	260	°C	
leads					









Electrical Characterists

Parameter		Test Conditions	Value (Leg/Device)			
	Symbol		Min	Тур	Max	Units
Forward voltage	V _F	I _F = 30A/60A, T _J = 25°C	-	1.5	1.7	V
		I _F = 30A/60A, T _J =	-	1.77	2.10	
		150°C				
		I _F = 30A/60A, T _J =	-	1.85	2.25	
		175°C				
Reverse current I _R	I _R	V _R = 650V, T _J = 25°C	-	30/60	370/740	μA
		V _R = 650V, T _J = 175°C	-	390/780	-	
Total capacitive charge (3)	Q _c	V _R = 400V	-	72/144	-	nC
Total capacitance	С	V _R = 1V, f = 1MHz	-	990/	-	PF
				1980		
		V _R = 300V, f = 1MHz	-	117/234	-	
		V _R = 600V, f = 1MHz	-	101/202	-	
Capacitance stored energy	E _c	V _R = 400V	-	10.5/21	-	μJ

(1) QC is independent on TJ, ${\rm di}_{\rm F}/{\rm dt},$ and IF as shown in the application note USCi_AN0011

Thermal characteristics			Value (Leg/Device)			
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
Thermal resistance, junction -	R _{ejc}		-	0.4/0.2	0.52/0.26	°C/W
case						



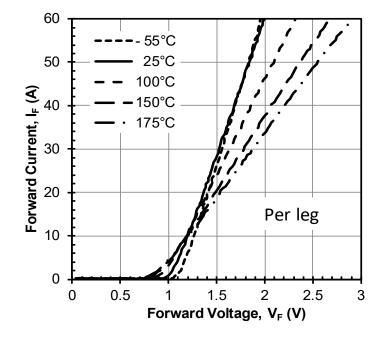
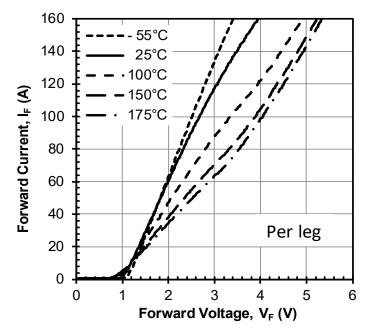


Figure 1. Typical forward characteristics



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Figure 2. Typical forward characteristics in surge current

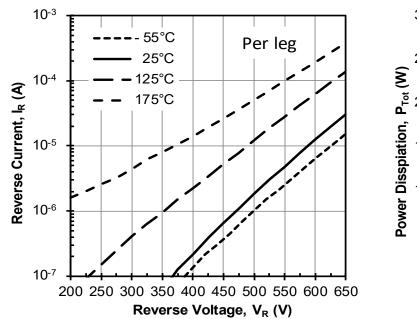


Figure 3. Typical reverse characteristics

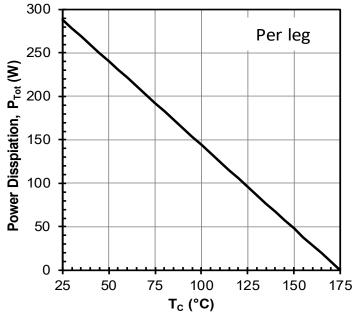


Figure 4. Power dissipation



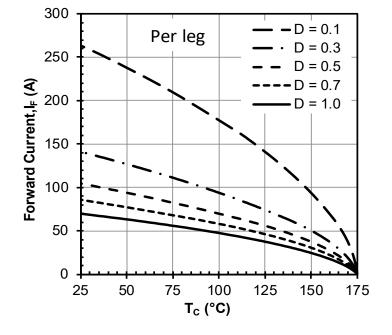
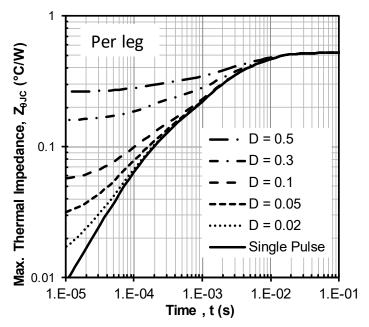


Figure 5. Diode forward current



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Figure 6. Maximum transient thermal impedance

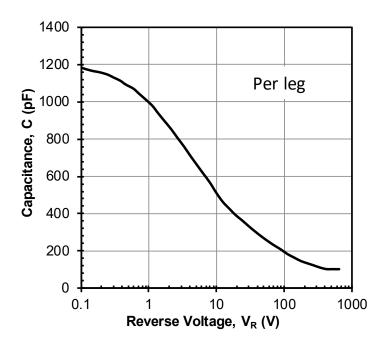


Figure 7. Capacitance vs. reverse voltage at 1MHz

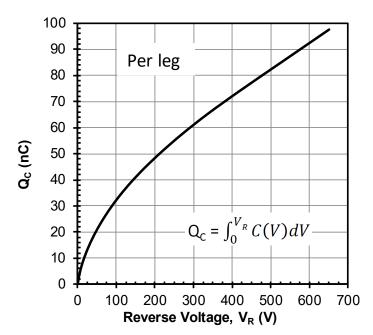


Figure 8. Typical capacitive charge vs. reverse voltage









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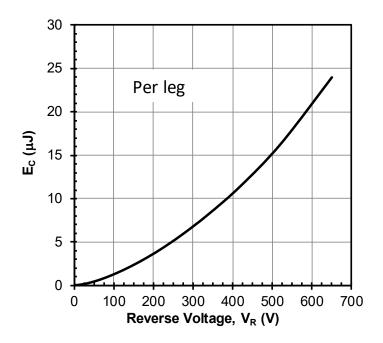


Figure 9. Typical capacitance stored energy vs. reverse voltage

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