

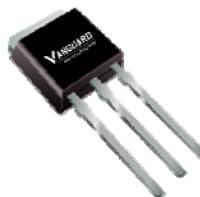
Features

- N-Channel, 5V Logic Level Control
- Enhancement mode
- Very low on-resistance $R_{DS(on)}$ @ $V_{GS}=4.5\text{ V}$
- VitoMOS® II Technology
- 100% Avalanche test
- Pb-free lead plating; RoHS compliant

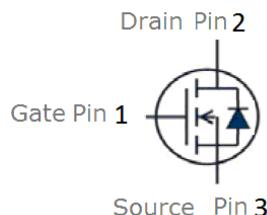


V_{DS}	100	V
$R_{DS(on),TYP}$ @ $V_{GS}=10\text{ V}$	6	$\text{m}\Omega$
$R_{DS(on),TYP}$ @ $V_{GS}=4.5\text{ V}$	7.8	$\text{m}\Omega$
I_D	94	A

TO-251



Part ID	Package Type	Marking	Tape and reel information
VSI008N10MS	TO-251	008N10M	75pcs/Tube



Maximum ratings, at $T_A = 25^\circ\text{C}$, unless otherwise specified

Symbol	Parameter	Rating	Unit
$V_{(BR)DSS}$	Drain-Source breakdown voltage	100	V
V_{GS}	Gate-Source voltage	± 20	V
I_s	Diode continuous forward current	$T_C = 25^\circ\text{C}$	A
I_D	Continuous drain current @ $V_{GS}=10\text{V}$	$T_C = 25^\circ\text{C}$	A
		$T_C = 100^\circ\text{C}$	A
I_{DM}	Pulse drain current tested ①	$T_C = 25^\circ\text{C}$	A
I_{DSM}	Continuous drain current @ $V_{GS}=10\text{V}$	$T_A = 25^\circ\text{C}$	A
		$T_A = 70^\circ\text{C}$	A
EAS	Avalanche energy, single pulsed ②	41	mJ
P_D	Maximum power dissipation	$T_C = 25^\circ\text{C}$	W
P_{DSM}	Maximum power dissipation ③	$T_A = 25^\circ\text{C}$	W
T_{STG}, T_J	Storage and Junction Temperature Range	-55 to 175	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.4	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	100	°C/W

Symbol	Parameter	Condition	Min.	Typ.	Max.	Unit
Static Electrical Characteristics @ $T_j=25^\circ\text{C}$ (unless otherwise stated)						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	--	--	1	μA
	Zero Gate Voltage Drain Current($T_j=125^\circ\text{C}$)	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	--	--	100	μA
I_{GSS}	Gate-Body Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	--	--	± 100	nA
$V_{\text{GS}(\text{TH})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.5	1.7	2.3	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ④	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$	--	6	8.5	$\text{m}\Omega$
$R_{\text{DS}(\text{ON})}$	Drain-Source On-State Resistance ④	$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=15\text{A}$	--	7.8	10.5	$\text{m}\Omega$
Dynamic Electrical Characteristics @ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	2250	2645	3050	pF
C_{oss}	Output Capacitance		980	1155	1305	pF
C_{rss}	Reverse Transfer Capacitance		25	35	45	pF
R_g	Gate Resistance	f=1MHz	--	3.2	--	Ω
Q_g	Total Gate Charge	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=20\text{A}, V_{\text{GS}}=10\text{V}$	--	46	--	nC
Q_{gs}	Gate-Source Charge		--	11	--	nC
Q_{gd}	Gate-Drain Charge		--	9	--	nC
Switching Characteristics						
$t_{\text{d}(\text{on})}$	Turn-on Delay Time	$V_{\text{DD}}=50\text{V}, I_{\text{D}}=20\text{A}, R_{\text{G}}=3\Omega, V_{\text{GS}}=10\text{V}$	--	11.7	--	ns
t_r	Turn-on Rise Time		--	7.2	--	ns
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time		--	34.5	--	ns
t_f	Turn-Off Fall Time		--	12.3	--	ns
Source- Drain Diode Characteristics@ $T_j = 25^\circ\text{C}$ (unless otherwise stated)						
V_{SD}	Forward on voltage	$I_{\text{SD}}=20\text{A}, V_{\text{GS}}=0\text{V}$	--	0.8	1.2	V
t_{rr}	Reverse Recovery Time	$T_j=25^\circ\text{C}, I_{\text{SD}}=20\text{A}, V_{\text{GS}}=0\text{V}$ $dI/dt=500\text{A}/\mu\text{s}$	--	21.6	--	ns
Q_{rr}	Reverse Recovery Charge		--	44.7	--	nC

NOTE:

- ① Repetitive rating; pulse width limited by max junction temperature.
- ② Limited by $T_{j\text{max}}$, starting $T_j = 25^\circ\text{C}$, $L = 0.5\text{mH}$, $R_G = 25\Omega$, $I_{AS} = 10\text{A}$, $V_{GS} = 10\text{V}$. Part not recommended for use above this value
- ③ The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C .
- ④ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.



Vanguard
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VSI008N10MS

100V/94A N-Channel Advanced Power MOSFET

Typical Characteristics

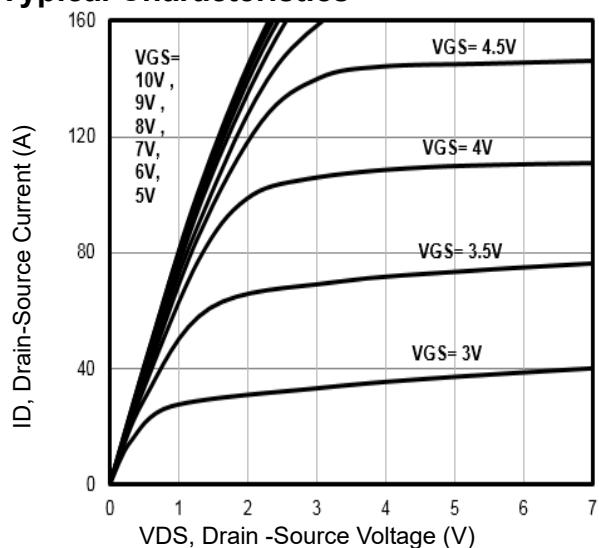


Fig1. Typical Output Characteristics

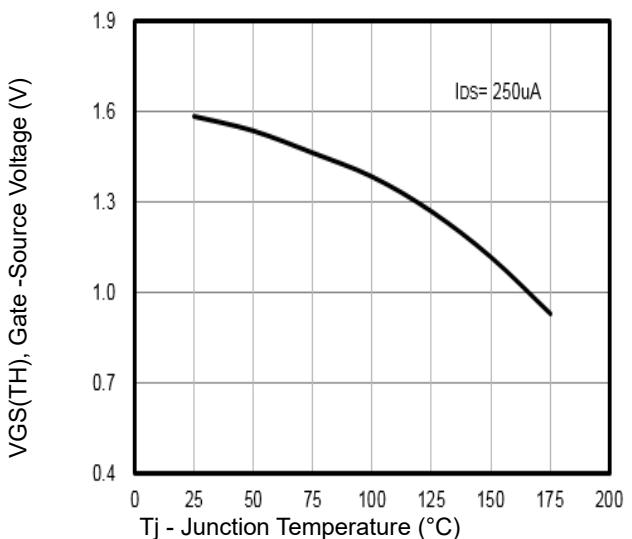


Fig2. $V_{GS(TH)}$ Gate -Source Voltage Vs. T_j

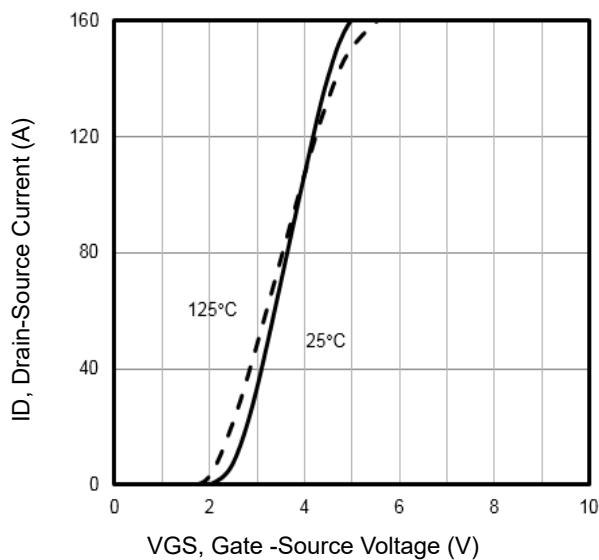


Fig3. Typical Transfer Characteristics

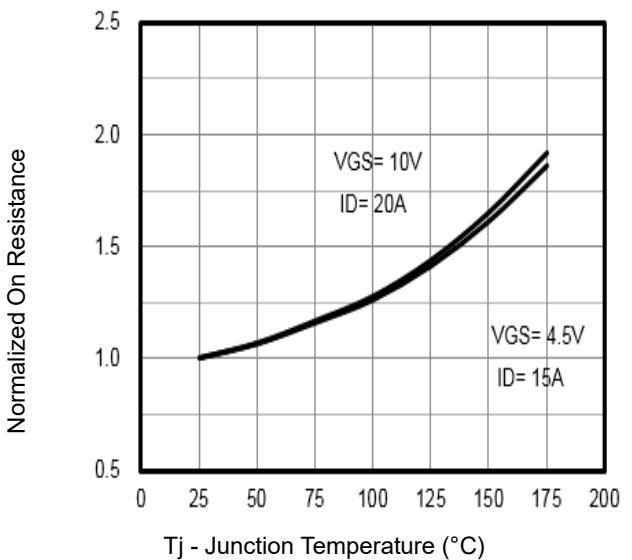


Fig4. Normalized On-Resistance Vs. T_j

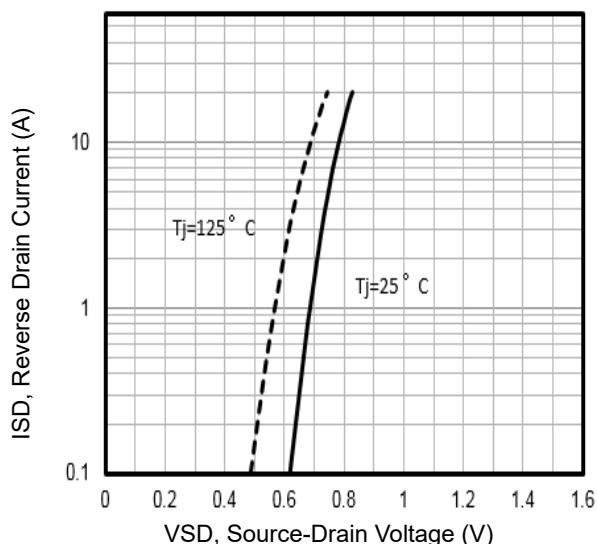


Fig5. Typical Source-Drain Diode Forward Voltage

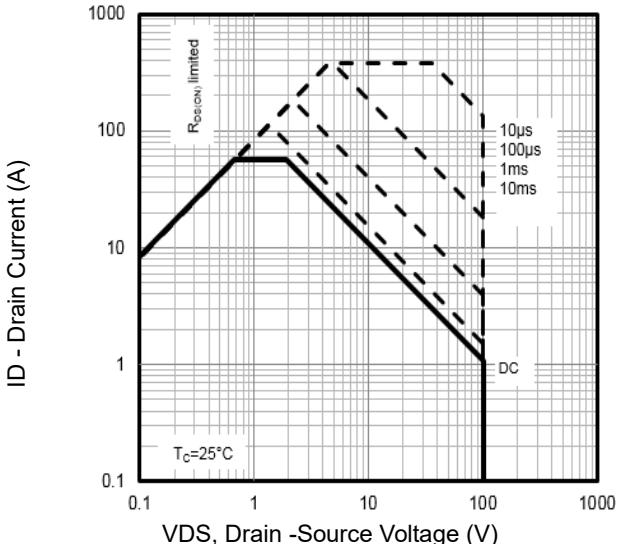


Fig6. Maximum Safe Operating Area



Typical Characteristics

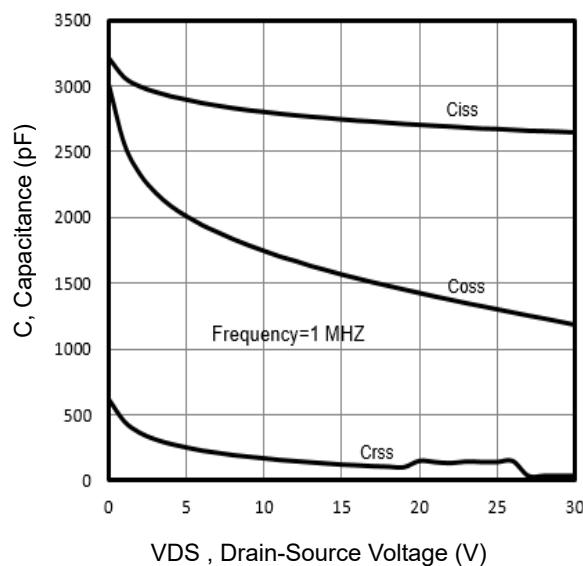


Fig7. Typical Capacitance Vs.Drain-Source Voltage

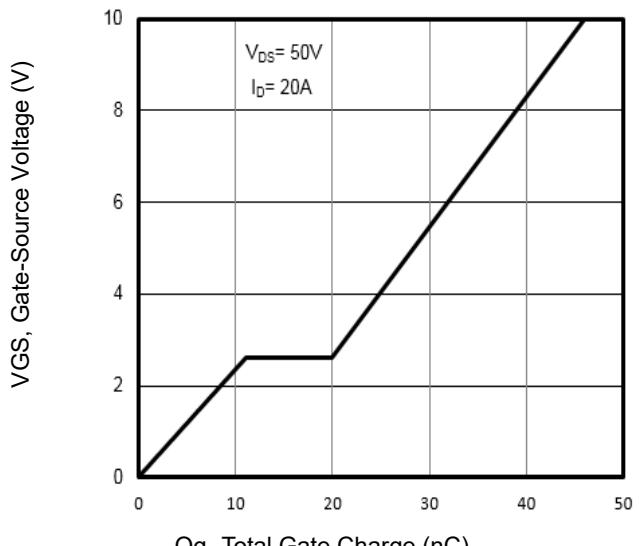


Fig8. Typical Gate Charge Vs.Gate-Source Voltage

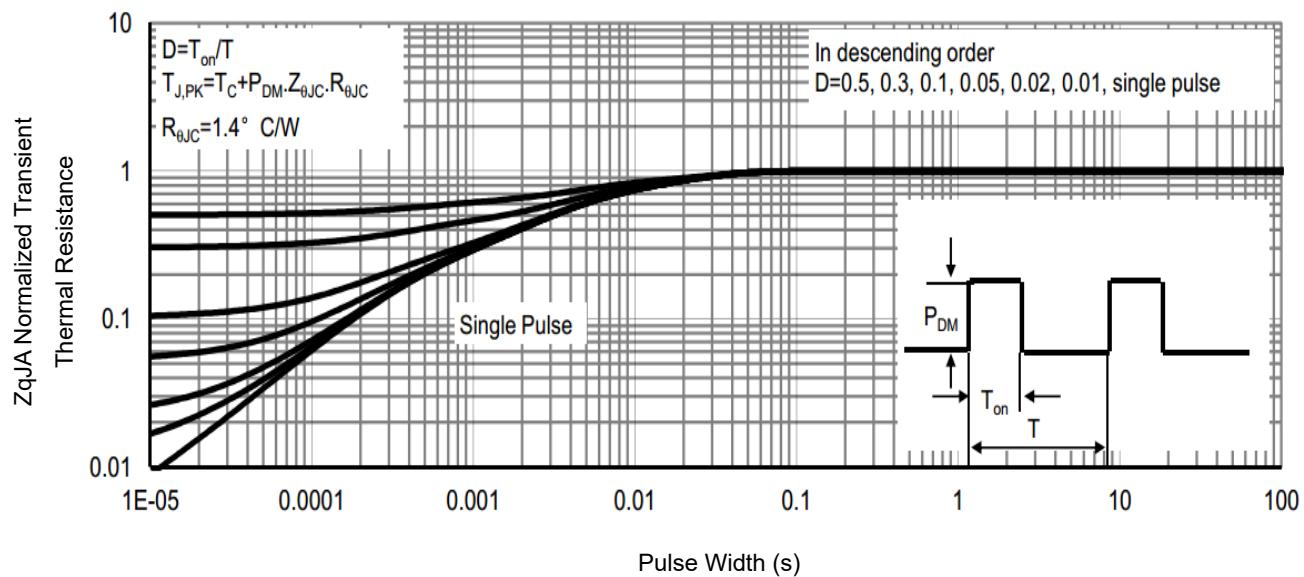


Fig9. Normalized Maximum Transient Thermal Impedance

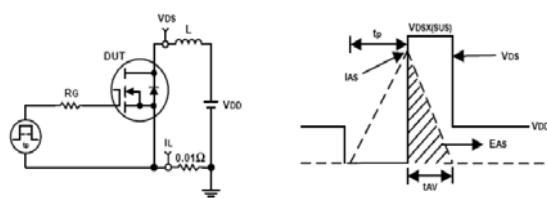


Fig10. Unclamped Inductive Test Circuit and waveforms

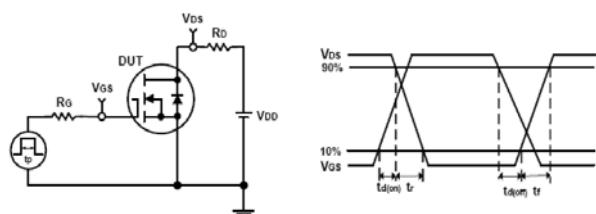
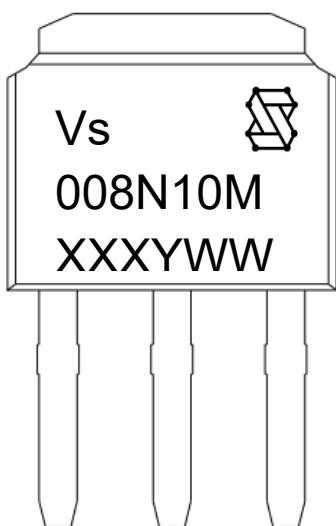


Fig11. Switching Time Test Circuit and waveforms

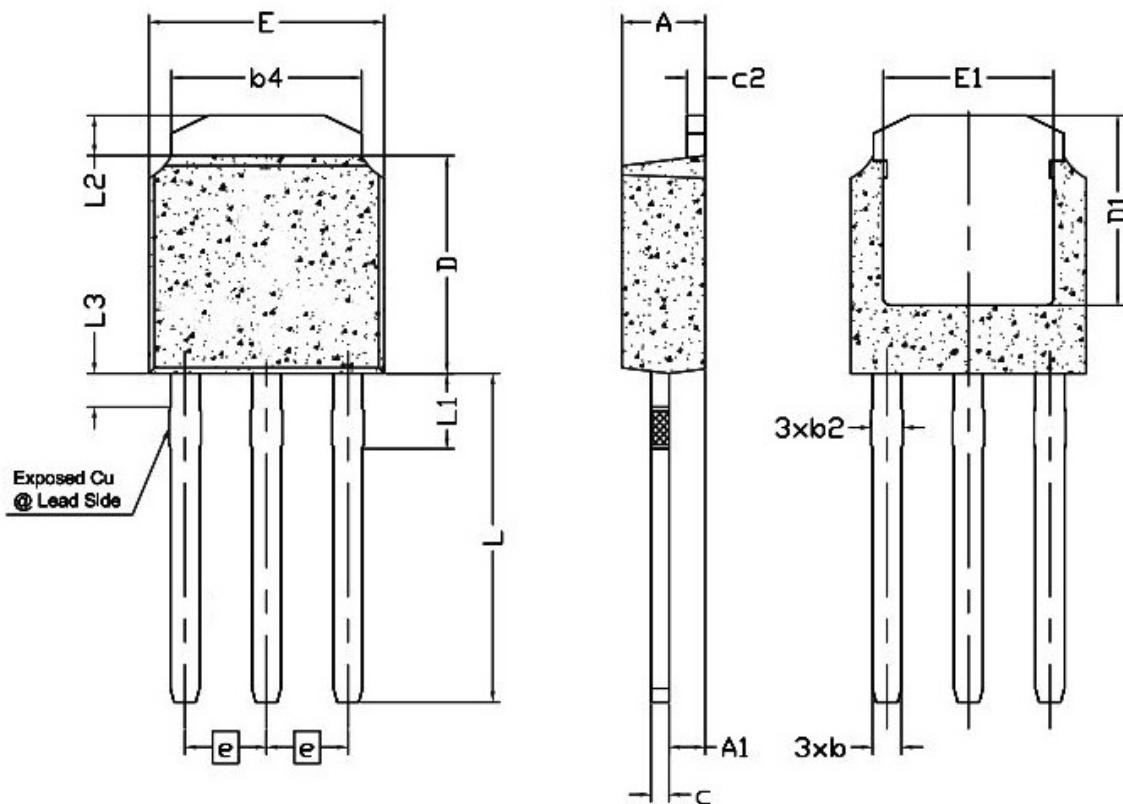
Marking Information



- 1st line: Vanguard Code (Vs), Vanguard Logo
2nd line: Part Number (008N10M)
3rd line: Date code (XXXYWW)
 XXX: Wafer Lot Number
 Y: Year Code, e.g. E means 2017
 WW: Week Code



TO-251 Package Outline Data



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	2.20	2.30	2.39
A1	0.89	1.04	1.15
b	0.64	0.76	0.89
b2	0.77	0.84	1.14
b3	5.21	5.34	5.46
c	0.46	0.50	0.60
c2	0.46	0.50	0.60
D	5.98	6.10	6.223
D1	5.10	--	--
E	6.40	6.60	6.731
E1	4.40	--	--
e	2.286 BSC		
H	11.05	11.25	11.45
L	3.98	4.13	4.35
L3	0.89	--	1.27
L4	0.698 REF		
L5	0.972	1.099	1.226
α	79° REF		

Notes:

- Dimension "D" and "E" do NOT include mold flash, protrusion or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 0.1mm per side.

Customer Service

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