

P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ)		
- 60	0.061 at V _{GS} = - 10 V	- 30	10		
- 60	0.072 at V _{GS} = - 4.5 V	- 25	10		

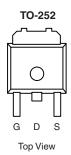
FEATURES

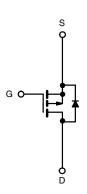
- TrenchFET® Power MOSFET
- 100 % UIS Tested

APPLICATIONS

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted						
Parameter	Symbol	Limit	Unit			
Gate-Source Voltage	V _{GS}	± 20	V			
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 25 °C	I_	- 30			
Continuous Drain Current (1) = 175 C)	T _C = 100 °C	I _D	- 25			
Pulsed Drain Current	I _{DM}	- 30	А			
Continuing Source Current (Diode Conduction)	I _S	- 20				
Avalanche Current	I _{AS}	- 20	1			
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	7.2	mJ		
Maximum Power Dissipation	T _C = 25 °C	P _D	34 ^a	W		
iviaximum rower dissipation	T _A = 25 °C	r D	4 ^b]		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
hunghian ta Ambianto	t ≤ 10 sec	R _{thJA}	20	25	°C/W	
Junction-to-Ambient ^D	Steady State		62	75		
Junction-to-Case		R _{thJC}	5	6		

Notes:

- a. See SOA curve for voltage derating.
- b. Surface Mounted on 1" x 1" FR-4 boad.

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1



Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			· v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0	- 2.0	- 3.0		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = - 60 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ	
		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 175 °C			- 150		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 10			Α	
		V _{GS} = - 10 V, I _D = - 5 A		0.061			
5	_	V _{GS} = - 10 V, I _D = - 5 A, T _J = 125 °C		0.100			
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 5 A, T _J = 175 °C		0.150		Ω	
		V _{GS} = - 4.5 V, I _D = - 2 A		0.072			
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5 A		8		S	
Dynamic	*	'		*			
Input Capacitance	C _{iss}			1000		pF	
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		120			
Reverse Transfer Capacitance	C _{rss}			100			
Total Gate Charge	Q_g			10			
Gate-Source Charge	Q_{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.1		nC	
Gate-Drain Charge	Q_{gd}]		3.2			
Gate Resistance	R_{g}	f = 1 MHz	8.0			Ω	
Turn-On Delay Time ^c	t _{d(on)}			6			
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_{L} = 3.57 \Omega$		15			
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong -8.4 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 2.5 \Omega$		16		ns	
Fall Time ^c	t _f]		8			
Source-Drain Diode Ratings and Cha	racteristics	(T _C = 25 °C) ^b		1			
Pulsed Current	I _{SM}				- 30	Α	
Forward Voltage ^b	V_{SD}	I _F = - 2 A, V _{GS} = 0 V		- 0.9	- 1.3	V	
Reverse Recovery Time	t _{rr}	L _ 9 A di/dt _ 100 A/::s		50		ns	
Reverse Recovery Time	Q _{rr}	I _F = - 8 A, di/dt = 100 A/μs		80		nC	

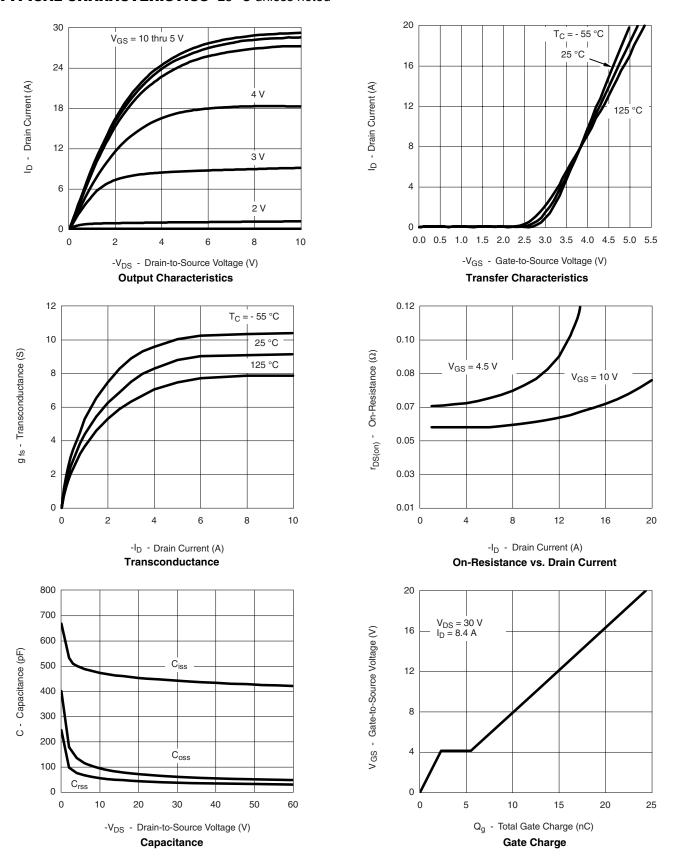
Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

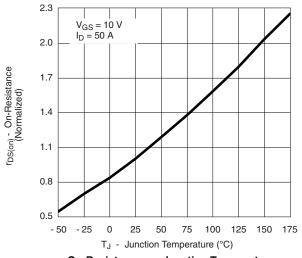


TYPICAL CHARACTERISTICS 25 °C unless noted

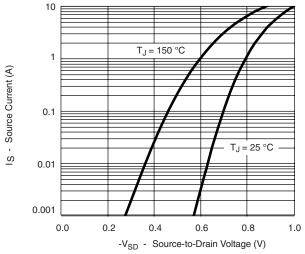




TYPICAL CHARACTERISTICS 25 °C unless noted

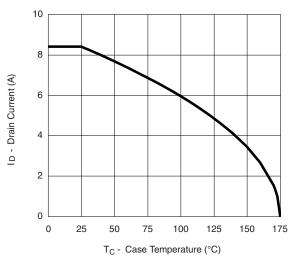




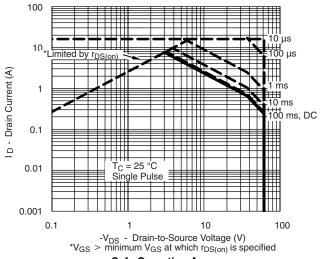


Source-Drain Diode Forward Voltage

THERMAL RATINGS



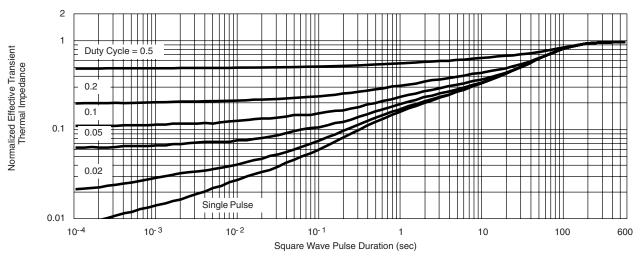
Drain Current vs. Case Temperature



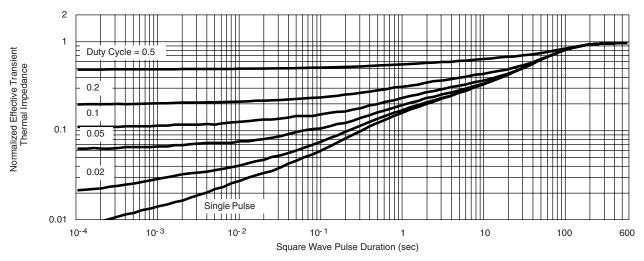
Safe Operating Area



THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Ambient

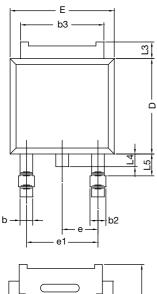


Normalized Thermal Transient Impedance, Junction-to-Case

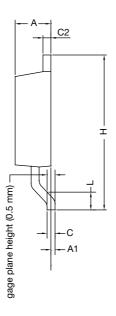
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TO-252AA CASE OUTLINE







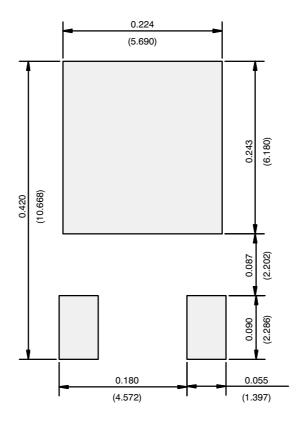
	MILLIMETERS		INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	1	0.170	1	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC 0.090 BSC		0.090 BSC	
e1	4.56	BSC	0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	ı	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347					

Note

• Dimension L3 is for reference only.



RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)



Recommended Minimum Pads Dimensions in Inches/(mm)



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