

# P-Channel 60-V (D-S) MOSFET

PRODUC	T SUMMARY		
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)	Q <sub>g</sub> (Typ)
- 60	0.100 at V <sub>GS</sub> = - 10 V	- 20	12.5
- 00	0.120 at V <sub>GS</sub> = - 4.5 V	- 15	12.5

#### **FEATURES**

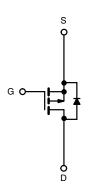
- TrenchFET® Power MOSFET
- 100 % UIS Tested

#### **APPLICATIONS**

Load Switch







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 2$	25 °C, unless othe	rwise noted			
Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
Continuous Drain Current (T <sub>.I</sub> = 175 °C)	T <sub>C</sub> = 25 °C	I.	- 20		
Continuous Drain Current (1) = 175 C)	T <sub>C</sub> = 100 °C	I <sub>D</sub>	- 12		
Pulsed Drain Current		I <sub>DM</sub>	- 60	А	
Continuing Source Current (Diode Conduction)		I <sub>S</sub>	- 12		
Avalanche Current	che Current I <sub>AS</sub>		- 12	-	
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	7.2	mJ	
Maximum Pawar Dissination	T <sub>C</sub> = 25 °C	В	30 <sup>a</sup>	W	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	2 <sup>b</sup>		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
hunding to Ambient	t ≤ 10 sec	R <sub>thJA</sub>	20	25			
Junction-to-Ambient <sup>D</sup>	Steady State	' 'thJA	62	75	°C/W		
Junction-to-Case		R <sub>thJC</sub>	5	6			

#### Notes:

- a. See SOA curve for voltage derating.
- b. Surface Mounted on 1" x 1" FR-4 boad.

服务热线:400-655-8788

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Parameter	Symbol	Test Conditions	Min	Typ <sup>a</sup>	Max	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0	- 2.0	- 3.0	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V			- 1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			- 50	μΑ
		V <sub>DS</sub> = - 60 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			- 150	
On-State Drain Current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 10			Α
		V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A		0.100		
	_	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 125 °C		0.150		Ω
Drain-Source On-State Resistance <sup>b</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 175 °C		0.200		S
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2 A		0.120		
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A		8		S
Dynamic		<u> </u>		!	···········	
Input Capacitance	C <sub>iss</sub>			550		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		95		
Reverse Transfer Capacitance	C <sub>rss</sub>			60		
Total Gate Charge	$Q_g$			12.5	19	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.3		nC
Gate-Drain Charge	$Q_{gd}$	1		3.2		
Gate Resistance	$R_{g}$	f = 1 MHz		8.0		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			5	10	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = -30 \text{ V}, R_L = 3.57 \Omega$ $I_D \cong -8.4 \text{ A}, V_{GEN} = -10 \text{ V}, R_G = 2.5 \Omega$		14	25	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			15	25	ns
Fall Time <sup>c</sup>	t <sub>f</sub>	1		7	12	
Source-Drain Diode Ratings and Cha	racteristics	(T <sub>C</sub> = 25 °C) <sup>b</sup>				
Pulsed Current	I <sub>SM</sub>				- 20	Α
Forward Voltage <sup>b</sup>	V <sub>SD</sub>	I <sub>F</sub> = - 2 A, V <sub>GS</sub> = 0 V		- 0.9	- 1.3	V
Reverse Recovery Time	t <sub>rr</sub>	1 0 A 41/41 400 A/44		50	80	ns
Reverse Recovery Time	Q <sub>rr</sub>	I <sub>F</sub> = - 8 A, di/dt = 100 A/μs		80	120	nC

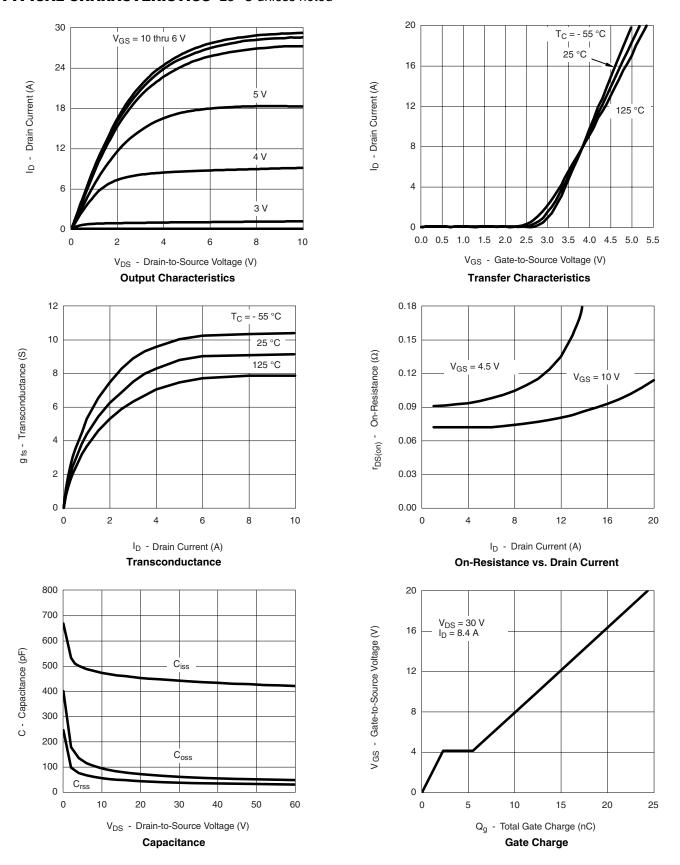
### Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

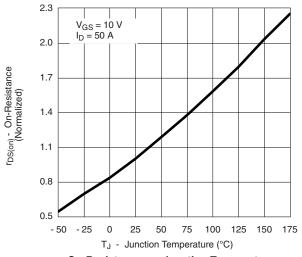


#### TYPICAL CHARACTERISTICS 25 °C unless noted

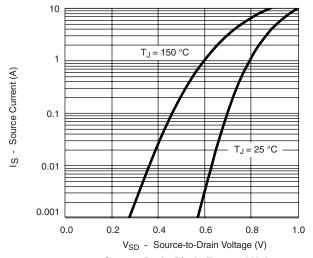




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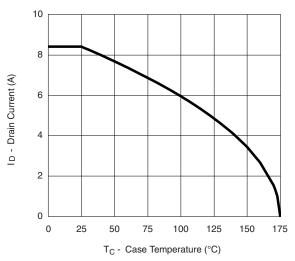




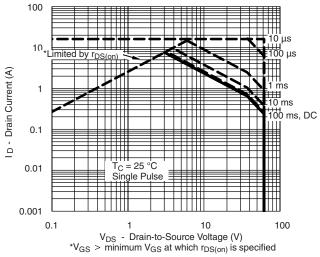


Source-Drain Diode Forward Voltage

#### THERMAL RATINGS



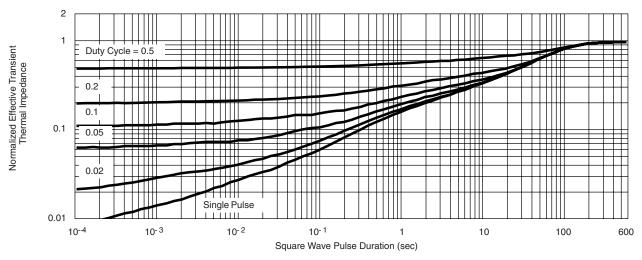
**Drain Current vs. Case Temperature** 



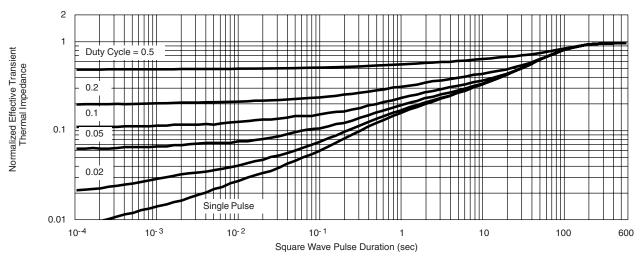
Safe Operating Area



#### THERMAL RATINGS



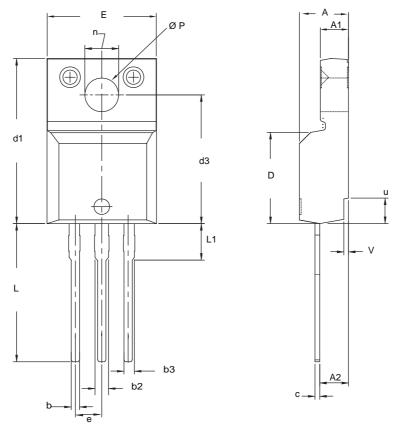
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case



## **TO-220 FULLPAK**



DIM.	MILLIN	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.	
Α	4.570	4.830	0.180	0.190	
A1	2.570	2.830	0.101	0.111	
A2	2.510	2.850	0.099	0.112	
b	0.622	0.890	0.024	0.035	
b2	1.229	1.400	0.048	0.055	
b3	1.229	1.400	0.048	0.055	
С	0.440	0.629	0.017	0.025	
D	8.650	9.800	0.341	0.386	
d1	15.88	16.120	0.622	0.635	
d3	12.300	12.920	0.484	0.509	
Е	10.360	10.630	0.408	0.419	
е	2.54	BSC	0.100	0.100 BSC	
L	13.200	13.730	0.520	0.541	
L1	3.100	3.500	0.122	0.138	
n	6.050	6.150	0.238	0.242	
ØΡ	3.050	3.450	0.120	0.136	
u	2.400	2.500	0.094	0.098	
V	0.400	0.500	0.016	0.020	

DWG: 5972

## Notes

- To be used only for process drawing.
   These dimensions apply to all TO-220, FULLPAK leadframe versions 3 leads.
   All critical dimensions should C meet C<sub>pk</sub> > 1.33.
   All dimensions include burrs and plating thickness.
   No chipping or package damage.



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