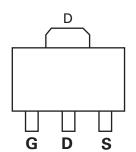
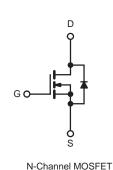


# N-Channel 100 V (D-S) MOSFET

| MOSFET PRODUCT SUMMARY |                                      |     |                       |  |  |  |
|------------------------|--------------------------------------|-----|-----------------------|--|--|--|
| V <sub>DS</sub> (V)    | $R_{DS(on)}(\Omega)$ Typ. $I_D(A)^a$ |     | Q <sub>g</sub> (Typ.) |  |  |  |
| 100                    | 0.102 at V <sub>GS</sub> = 10 V      | 4.2 |                       |  |  |  |
|                        | 0.120 at V <sub>GS</sub> = 6 V       | 3.8 | 2.9 nC                |  |  |  |
|                        | 0.125 at V <sub>GS</sub> = 4.5 V     | 3.6 |                       |  |  |  |





### **FEATURES**

- TrenchFET® Power MOSFET
- 100 % R<sub>q</sub> and UIS Tested



### **APPLICATIONS**

- DC/DC Converters / Boost Converters
- Load Switch
- LED Backlighting in LCD TVs
- · Power Management for Mobile Computing

| ABSOLUTE MAXIMUM RATINGS (TA                        | $_{\lambda}$ = 25 °C, unless oth  | nerwise noted)  |                      |          |  |
|---|-----------------------------------|-----------------|----------------------|----------|--|
| Parameter   | Symbol                            | Limit           | Unit                 |          |  |
| Drain-Source Voltage                                | V <sub>DS</sub>                   | 100             | V                    |          |  |
| Gate-Source Voltage                                 | $V_{GS}$                          | ± 20            |                      |          |  |
|   | T <sub>C</sub> = 25 °C            |                 | 4.2                  |          |  |
| Continuous Drain Current (T <sub>.1</sub> = 150 °C) | $T_C = 70  ^{\circ}C$             | I <sub>D</sub>  | 3.5                  |          |  |
| Continuous Brain Current (1) = 100 °C)              | T <sub>A</sub> = 25 °C            | טי              | 3.2 <sup>b,c</sup>   |          |  |
|   | T <sub>A</sub> = 70 °C            |                 | 2.8 <sup>b,c</sup>   | A        |  |
| Pulsed Drain Current (t = 300 μs)                   |                                   | I <sub>DM</sub> | 15                   | 7 ^      |  |
| Continuous Source-Drain Diode Current               | T <sub>C</sub> = 25 °C            | I <sub>S</sub>  | 2.1                  |          |  |
| Continuous Source-Dialit Diode Current              | T <sub>A</sub> = 25 °C            | 'S              | 1 <sup>b, c</sup>    |          |  |
| Single Pulse Avalanche Current                      | L = 0.1 mH                        | I <sub>AS</sub> | 3                    |          |  |
| Single Pulse Avalanche Energy                       | L=0.11111                         | E <sub>AS</sub> | 0.45                 | mJ       |  |
|   | T <sub>C</sub> = 25 °C            | P <sub>D</sub>  | 2.5                  |          |  |
| Maximum Power Dissipation                           | T <sub>C</sub> = 70 °C            |                 | 1.6                  | $\Box$ w |  |
| Maximum Fower Dissipation                           | T <sub>A</sub> = 25 °C            |                 | 1.25 <sup>b, c</sup> |          |  |
|   | T <sub>A</sub> = 70 °C            |                 | 0.8 <sup>b, c</sup>  |          |  |
| Operating Junction and Storage Temperature Range    | T <sub>J</sub> , T <sub>stg</sub> | - 55 to 150     | °C                   |          |  |

| THERMAL RESISTANCE RATINGS                  |              |                   |         |      |      |  |  |  |
|---|--------------|-------------------|---------|------|------|--|--|--|
| Parameter                                   | Symbol       | Typical           | Maximum | Unit |      |  |  |  |
| Maximum Junction-to-Ambient <sup>b, d</sup> | ≤ 5 s        | R <sub>thJA</sub> | 75      | 100  | °C/W |  |  |  |
| Maximum Junction-to-Foot (Drain)            | Steady State | $R_{th,IF}$       | 40      | 50   |      |  |  |  |

#### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s
- d. Maximum under steady state conditions is 166 °C/W.



| Parameter                                     | Symbol                  | Test Conditions  | Min. | Typ.  | Max.  | Unit   |  |  |
|---|-------------------------|--|------|-------|-------|--------|--|--|
| Static  |                         |  | l    | 1     |       |        |  |  |
| Drain-Source Breakdown Voltage                | V <sub>DS</sub>         | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$                          | 100  |       |       | V      |  |  |
| V <sub>DS</sub> Temperature Coefficient       | $\Delta V_{DS}/T_{J}$   |  |      | 59    |       | > 1/06 |  |  |
| V <sub>GS(th)</sub> Temperature Coefficient   | $\Delta V_{GS(th)}/T_J$ | I <sub>D</sub> = 250 μA  |      | - 4.8 |       | mV/°C  |  |  |
| Gate-Source Threshold Voltage                 | V <sub>GS(th)</sub>     | $V_{DS} = V_{GS}, I_D = 250 \mu A$                                     | 1.2  |       | 3     | V      |  |  |
| Gate-Source Leakage                           | I <sub>GSS</sub>        | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$                      |      |       | ± 100 | nA     |  |  |
| Zana Cata Malta da Busin Comunant             |                         | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V                         |      |       | - 1   | μA     |  |  |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>        | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C |      |       | - 10  |        |  |  |
| On-State Drain Current <sup>a</sup>           | I <sub>D(on)</sub>      | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$                        | 5    |       |       | Α      |  |  |
|   |                         | $V_{GS} = 10 \text{ V}, I_{D} = 2 \text{ A}$                           |      | 0.102 |       | Ω      |  |  |
| Drain-Source On-State Resistance <sup>a</sup> | R <sub>DS(on)</sub>     | $V_{GS} = 6 \text{ V}, I_{D} = 1 \text{ A}$                            |      | 0.120 |       |        |  |  |
|   | Ī                       | V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1 A                          |      | 0.125 |       |        |  |  |
| Forward Transconductance <sup>a</sup>         | 9 <sub>fs</sub>         | $V_{DS} = 20 \text{ V}, I_{D} = 2 \text{ A}$                           |      | 5     |       | S      |  |  |
| Dynamic <sup>b</sup>                          | L                       |  | I.   |       | L     | ı      |  |  |
| Input Capacitance                             | C <sub>iss</sub>        |  |      | 196   |       |        |  |  |
| Output Capacitance                            | C <sub>oss</sub>        | $V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$       |      | 67    |       | pF     |  |  |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>        |  |      | 14    |       |        |  |  |
|   |                         | $V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 2.2 \text{ A}$  |      | 5.2   | 10.4  | ).4    |  |  |
| Total Gate Charge                             | Q <sub>g</sub>          |  |      | 2.9   | 5.8   | nC     |  |  |
| Gate-Source Charge                            |                         | $V_{DS} = 50 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 2.2 \text{ A}$   |      | 1     |       |        |  |  |
| Gate-Drain Charge                             | $Q_{gd}$                |  |      | 1.4   |       |        |  |  |
| Gate Resistance                               | $R_{g}$                 | f = 1 MHz  | 0.9  | 4.3   | 8.6   | Ω      |  |  |
| Turn-On Delay Time                            | t <sub>d(on)</sub>      |  |      | 40    | 60    |        |  |  |
| Rise Time                                     | t <sub>r</sub>          | $V_{DD} = 50 \text{ V}, R_{L} = 27.7 \Omega$                           |      | 68    | 102   | ns     |  |  |
| Turn-Off Delay Time                           | t <sub>d(off)</sub>     | $I_D$ = 1.8 A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$                   |      | 14    | 21    |        |  |  |
| Fall Time                                     | t <sub>f</sub>          |  |      | 20    | 30    |        |  |  |
| Turn-On Delay Time                            | t <sub>d(on)</sub>      |  |      | 8     | 16    |        |  |  |
| Rise Time                                     | t <sub>r</sub>          | $V_{DD} = 50 \text{ V}, R_{L} = 27.7 \Omega$                           |      | 10    | 20    |        |  |  |
| Turn-Off Delay Time                           | t <sub>d(off)</sub>     | $I_D$ = 1.8 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$                    |      | 10    | 20    |        |  |  |
| Fall Time                                     | t <sub>f</sub>          |  |      | 7     | 14    |        |  |  |
| <b>Drain-Source Body Diode Characteristi</b>  | cs                      |  |      | L     |       |        |  |  |
| Continuous Source-Drain Diode Current         | I <sub>S</sub>          | T <sub>C</sub> = 25 °C   |      |       | - 2.1 |        |  |  |
| Pulse Diode Forward Current <sup>a</sup>      | I <sub>SM</sub>         |  |      |       | - 8   | A      |  |  |
| Body Diode Voltage                            | V <sub>SD</sub>         | I <sub>S</sub> = 1.8 A   |      | - 0.8 | - 1.2 | V      |  |  |
| Body Diode Reverse Recovery Time              | t <sub>rr</sub>         |  |      | 23    | 35    | ns     |  |  |
| Body Diode Reverse Recovery Charge            | Q <sub>rr</sub>         | $I_F = 1.8 \text{ A}, dI/dt = 100 \text{ A/}\mu\text{s},$              |      | 21    | 32    | nC     |  |  |
| Reverse Recovery Fall Time                    | t <sub>a</sub>          | T <sub>J</sub> = 25 °C   |      | 17    |       |        |  |  |
| Reverse Recovery Rise Time                    | t <sub>b</sub>          |  |      | 6     |       | ns     |  |  |

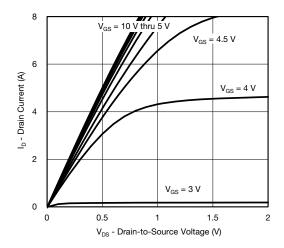
### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

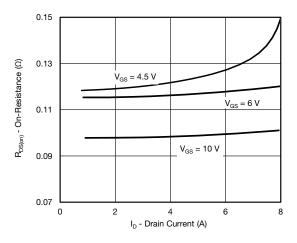
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

E-mail: China@VBsemi TEL:86-755-83251052

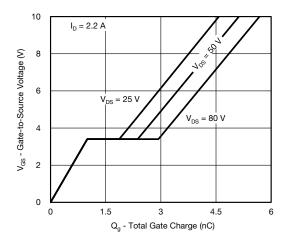




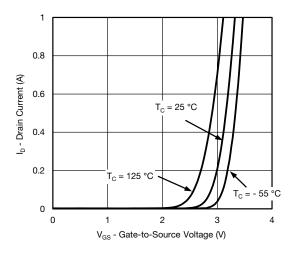
### **Output Characteristics**



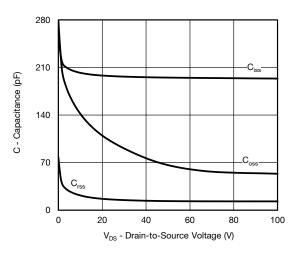
On-Resistance vs. Drain Current and Gate Voltage



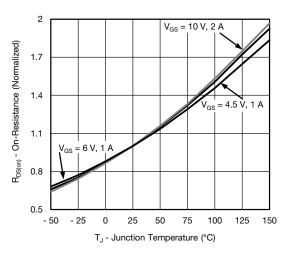
**Gate Charge** 



**Transfer Characteristics** 

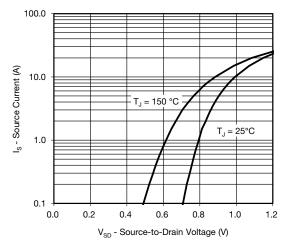


Capacitance

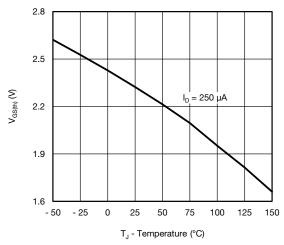


On-Resistance vs. Junction Temperature

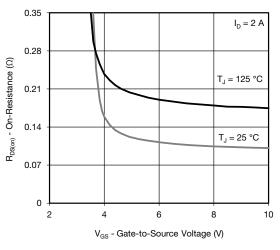




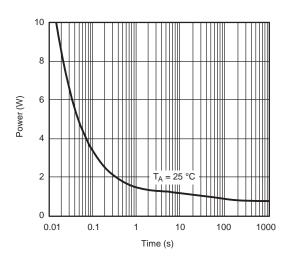
### Source-Drain Diode Forward Voltage



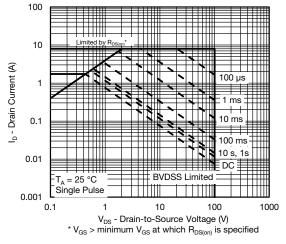
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage

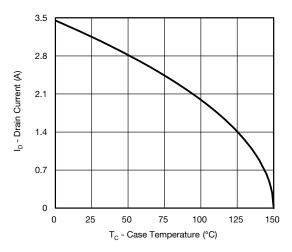


Single Pulse Power

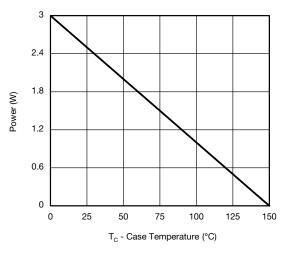


**Safe Operating Area** 

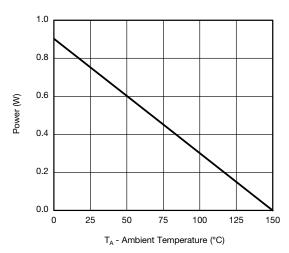




### **Current Derating\***





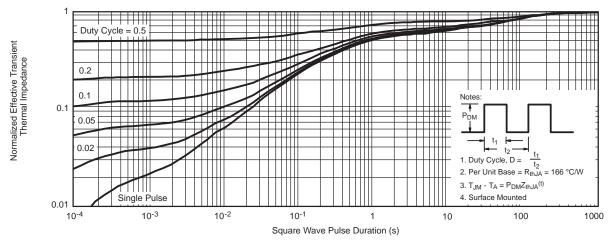


Power, Junction-to-Ambient

E-mail: China@VBsemi TEL:86-755-83251052

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max.)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





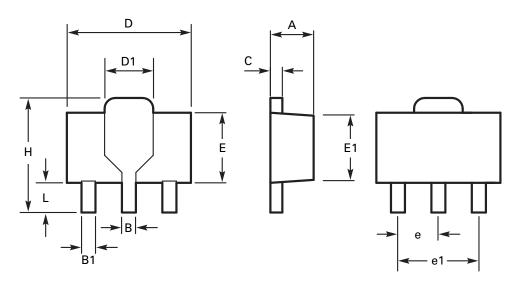
### Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



# Package outline - SOT89



| DIM | Millin | neters | Inc   | Inches DIM Millimeters In |    | DIM Millimeters Inches |      | hes       |       |
|-----|--------|--------|-------|---------------------------|----|------------------------|------|-----------|-------|
|     | Min    | Max    | Min   | Max                       |    | Min                    | Max  | Min       | Max   |
| Α   | 1.40   | 1.60   | 0.550 | 0.630                     | Е  | 2.29                   | 2.60 | 0.090     | 0.102 |
| В   | 0.44   | 0.56   | 0.017 | 0.022                     | E1 | 2.13                   | 2.29 | 0.084     | 0.090 |
| B1  | 0.36   | 0.48   | 0.014 | 0.019                     | е  | 1.50 BSC               |      | 0.059 BSC |       |
| С   | 0.35   | 0.44   | 0.014 | 0.017                     | e1 | 3.00 BSC               |      | 0.118 BSC |       |
| D   | 4.40   | 4.60   | 0.173 | 0.181                     | Н  | 3.94                   | 4.25 | 0.155     | 0.167 |
| D1  | 1.62   | 1.83   | 0.064 | 0.072                     | L  | 0.89                   | 1.20 | 0.035     | 0.047 |

Note: Controlling dimensions are in millimeters. Approximate dimensions are provided in inches

E-mail: China@VBsemi TEL:86-755-83251052

7



# **Disclaimer**

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.tw)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

## **Material Category Policy**

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be RoHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.tw)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.

E-mail: China@VBsemi TEL:86-755-83251052

# **X-ON Electronics**

Largest Supplier of Electrical and Electronic Components

Click to view similar products for MOSFET category:

Click to view products by VBsemi Elec manufacturer:

Other Similar products are found below:

614233C 648584F NTNS3A92PZT5G IRFD120 IRFF430 JANTX2N5237 2N7000 2SK2464-TL-E FCA20N60\_F109 FDZ595PZ AOD464 2SK2267(Q) 2SK2545(Q,T) 405094E 423220D MIC4420CM-TR VN1206L 614234A 715780A SSM6J414TU,LF(T 751625C IPP60R600P6XKSA1 RJK60S5DPK-M0#T0 PSMN4R2-30MLD TK31J60W5,S1VQ(O 2SK2614(TE16L1,Q) DMN1017UCP3-7 EFC2J004NUZTDG FCAB21350L1 P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 NTE2969 NTE6400A DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 SSM6P54TU,LF DMP22D4UFO-7B IPS60R3K4CEAKMA1 DMN1006UCA6-7 DMN16M9UCA6-7 STF5N65M6 STU5N65M6 C3M0021120D DMN13M9UCA6-7 BSS340NWH6327XTSA1 MCM3400A-TP DMTH10H4M6SPS-13 IRF40SC240ARMA1