

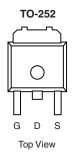
N-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a		
60	0.025 at V _{GS} = 10 V	35		
60	0.030 at V _{GS} = 4.5 V	30		

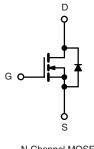
FEATURES

- TrenchFET[®] Power MOSFET
- 175 °C Junction Temperature





Drain Connected to Tab



N-Channel MOSFET

Parameter		Symbol	Limit	Unit	
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current $(T_J = 175 \ ^{\circ}C)^{b}$	T _C = 25 °C	L_	35		
	T _C = 100 °C	I _D	28		
Pulsed Drain Current		I _{DM}	100	А	
Continuous Source Current (Diode Conduction)		۱ _S	23		
Avalanche Current		I _{AS}	20		
Single Avalanche Energy (Duty Cycle \leq 1 %)	L = 0.1 mH	E _{AS}	20	mJ	
Mauimura Davian Diasia stian	T _C = 25 °C	PD	100	w	
Maximum Power Dissipation	T _A = 25 °C		3 ^a		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \le 10 \text{ sec}$	R _{th IA}	18	22	°C/W
Maximum Junction-to-Ambient*	Steady State		40	50	
Maximum Junction-to-Case		R _{thJC}	3.2	4	

Notes:

a. Surface Mounted on 1" x 1" FR4 board, t \leq 10 sec.

Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μ A	60			v	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.0	2.0	3.0		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1		
	I _{DSS}	$V_{DS} = 60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μA	
		$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 ^{\circ}\text{C}$			250		
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 V, V_{GS} = 10 V$	50			А	
Drain-Source On-State Resistance ^b		V _{GS} = 10 V, I _D = 15 A		0.025	0.031		
		V_{GS} = 10 V, I _D = 15 A, T _J = 125 °C			0.055	Ω	
	r _{DS(on)}	V_{GS} = 10 V, I _D = 15 A, T _J = 175 °C			0.069		
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		0.030	0.045		
Forward Transconductance ^b	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		20		S	
Dynamic ^a							
Input Capacitance	C _{iss}			670		pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		140			
Reverse Transfer Capacitance	C _{rss}			60			
Total Gate Charge ^c	Qg			11	17	nC	
Gate-Source Charge ^c	Q _{gs}	V_{DS} = 30 V, V_{GS} = 10 V, I_D = 23 A		3			
Gate-Drain Charge ^c	Q _{gd}			3			
Turn-On Delay Time ^c	t _{d(on)}			8	15		
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V}, \text{ R}_{\text{L}} = 1.3 \Omega$ $\text{I}_{\text{D}} \cong 23 \text{ A}, \text{ V}_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 2.5 \Omega$		15	25	ns	
Turn-Off Delay Time ^c	t _{d(off)}			30	45		
Fall Time ^c	t _f			25	40		
Source-Drain Diode Ratings and Cha	racteristics	(T _C = 25 °C)			I		
Pulsed Current	I _{SM}				50	А	
Diode Forward Voltage	V _{SD}	$I_{F} = 15 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		1.0	1.5	V	
Reverse Recovery Time	t _{rr}	I _F = 15 A, di/dt = 100 A/μs		30	60	ns	

Notes:

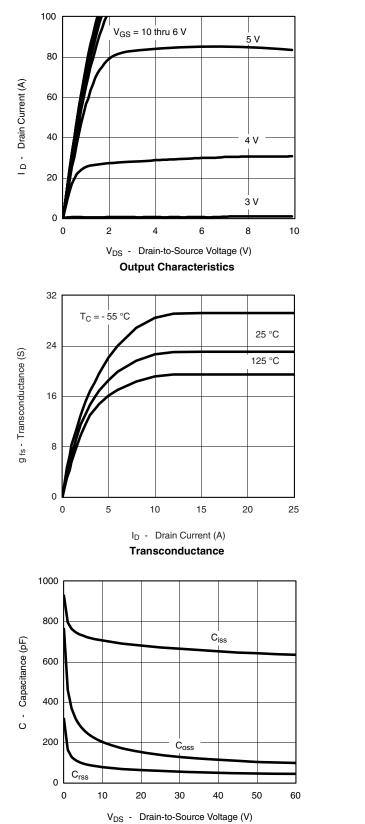
a. For design aid only; not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. Independent of operating temperature.

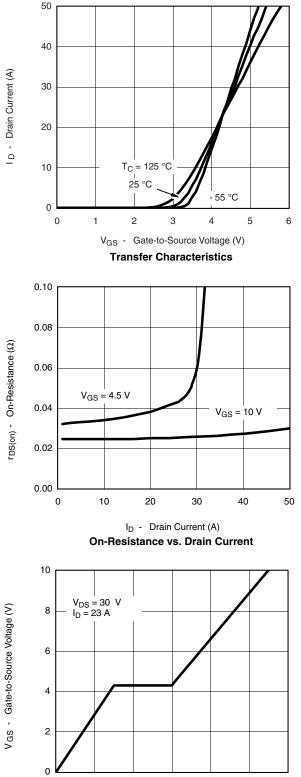
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





Capacitance

TYPICAL CHARACTERISTICS 25 °C unless noted



0

2

4

6

Qg - Total Gate Charge (nC)

Gate Charge

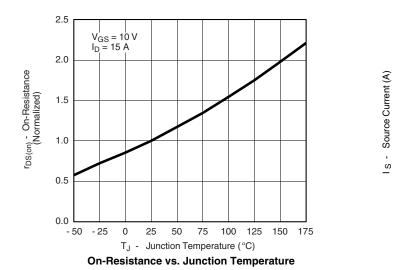
8

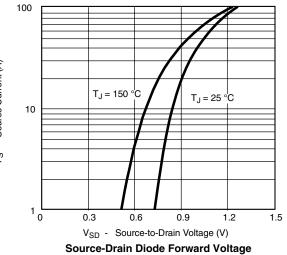
10

12



TYPICAL CHARACTERISTICS 25 °C unless noted





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THERMAL RATINGS





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