

## P-Channel 200V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\wedge)$ $I_D(A)$ $Q_g(T_2)$		Q <sub>g</sub> (Typ.)	
- 200	1.0 at V <sub>GS</sub> = - 10 V	- 2.0	8.0	
- 200	1.1 at V <sub>GS</sub> = - 6.0 V	- 1.8	0.0	

#### FEATURES

- Halogen-free According to IEC 61249-2-21
   Available
- TrenchFET<sup>®</sup> Power MOSFET
- Ultra Low On-Resistance
- Small Size

#### **APPLICATIONS**

Active Clamp Circuits in DC/DC Power Supplies



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P-Channel MOSFET

Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 200		V
Gate-Source Voltage		V <sub>GS</sub>	± 20		
Quality Durin Quarter (T. 450 00)8 h	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 2.0	- 1.68	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 70 °C		- 1.8	- 1.56	
Pulsed Drain Current		I <sub>DM</sub>	- 5.8		А
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		۱ <sub>S</sub>	- 1.0	- 0.6	
Single Pulse Avalanche Current	L = 1.0 mH	I <sub>AS</sub>	4.0		
Single Pulse Avalanche Energy	L = 1.0 mm	E <sub>AS</sub> 1.2		mJ	
M · D D · · · · ab	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.45	0.95	W
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70 °C		0.8	0.48	
Operating Junction and Storage Temperature Ra	nge	T <sub>J</sub> , T <sub>stg</sub>	- 55	to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Mauineura lunation to Ambienta	t ≤ 5 s	R <sub>thJA</sub>	75	100	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		120	166	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	40	50	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

HALOGEN

**FREE** Available



	Symbol		Limits				
Parameter		Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$	- 200			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 2.5		- 4.5	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current		$V_{DS} = -200 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = - 200 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10	μA	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}\!\le$ - 15 V, $V_{GS}$ = 10 V	- 1.0			А	
Drain-Source On-Resistance <sup>a</sup>	D	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -0.5 \text{ A}$		0.80 1.00		- ^	
	R <sub>DS(on)</sub>	$V_{GS}$ = - 6.0 V, I <sub>D</sub> = - 0.5 A	0.90 1.10		1.10		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -0.5 \text{ A}$		1.8		S	
Diode Forward Voltage	V <sub>SD</sub>	$I_{S} = -1.0 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		- 0.85	- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg	<u>)</u> ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) (		8.0	12	nC	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 10 V I <sub>D</sub> ≅ - 0.5 A		1.3			
Gate-Drain Charge	Q <sub>gd</sub>			2.5			
Gate Resistance	Rg	f = 1.0 MHz		8.0		^	
Input Capacitance	C <sub>iss</sub>			370	510		
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ = - 25 V, $V_{GS}$ = 0 V, f = 1 MHz		28		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			16			
Switching <sup>c</sup>							
Turn-On Time	t <sub>d(on)</sub>	N 400 N D 400		8	12	ns	
	tr	V <sub>DD</sub> = - 100 V, R <sub>L</sub> = 100 ∧ I <sub>D</sub> ≅ - 1.0 A, V <sub>GEN</sub> = - 10 V		11	17		
Turn Off Time	t <sub>d(off)</sub>	$R_{g} = 6 \land$		16	25		
Turn-Off Time	t <sub>f</sub>	- ·y - · ·		11	17		
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 0.5 A, dl/dt = 100 A/μs		140	200	nC	

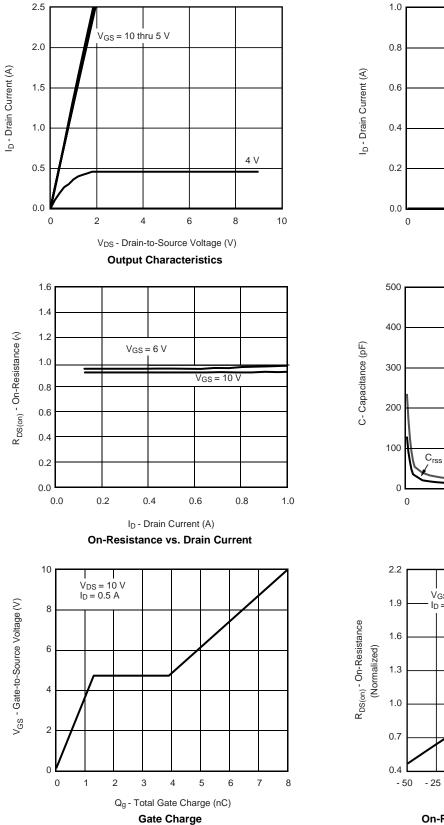
Notes:

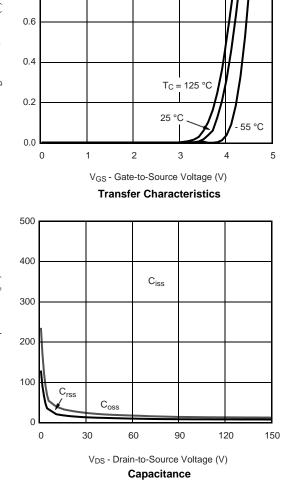
a. Pulse test: PW ≤ 300 µs duty cycle ≤ 2 %.
b. For DESIGN AID ONLY, not subject to production testing.
c. Switching time is essentially independent of operating temperature.

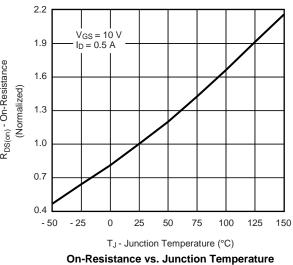
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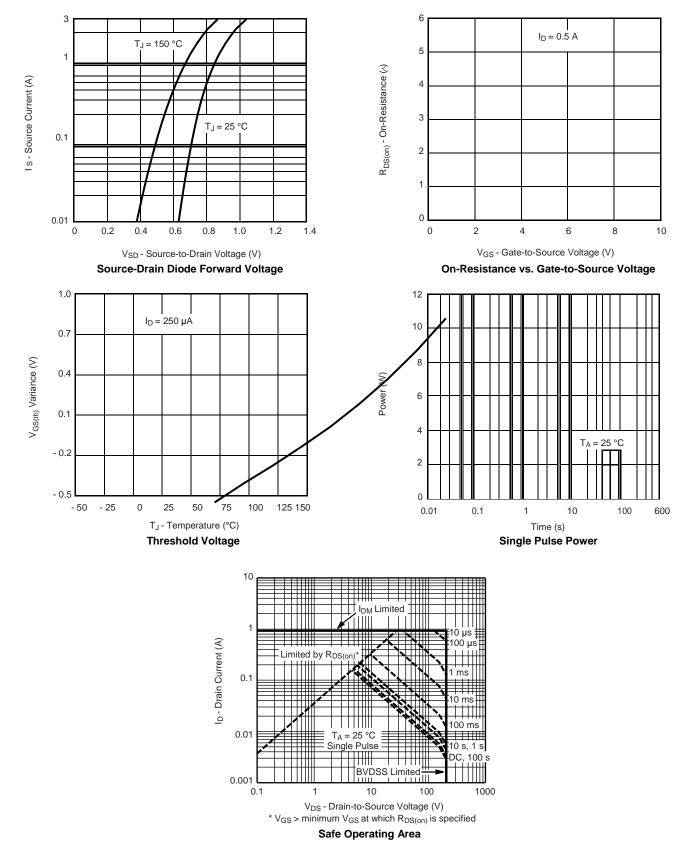






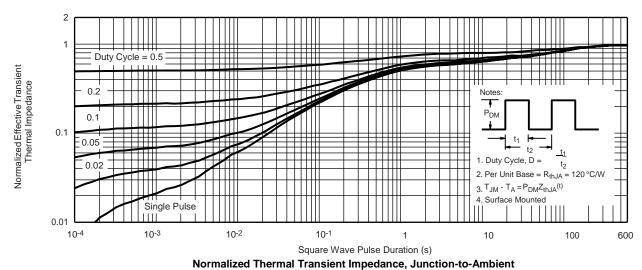






#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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