

### P-Channel 60-V (D-S) MOSFET

| PRODUCT SUMMARY                            |        |    |  |
|--|--------|----|--|
| V <sub>DS</sub>                            | -60    | V  |  |
| R <sub>DS(on)</sub> V <sub>GS</sub> = 10 V | 62     | mΩ |  |
| $R_{DS(on)}$ $V_{GS} = 4.5$ V              | 74     | mΩ |  |
| I <sub>D</sub>                             | -40    | А  |  |
| Configuration                              | Single |    |  |

### FEATURES

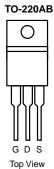
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % UIS Tested

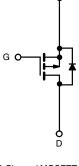
#### **APPLICATIONS**

Load Switch

s







P-Channel MOSFET

| Parameter  |                         | Symbol                            | Limit           | Unit |  |
|--|-------------------------|-----------------------------------|-----------------|------|--|
| Gate-Source Voltage                                |                         | V <sub>GS</sub>                   | ± 20            | V    |  |
| Continuous Drain Current (T <sub>J</sub> = 175 °C) | T <sub>C</sub> = 25 °C  | 1-                                | -40             |      |  |
|  | T <sub>C</sub> = 100 °C | I <sub>D</sub>                    | -30             | 1    |  |
| Pulsed Drain Current                               |                         | I <sub>DM</sub>                   | - 90            | А    |  |
| Continuing Source Current (Diode Conduction)       |                         | ۱ <sub>S</sub>                    | - 30            |      |  |
| Avalanche Current                                  | I <sub>AS</sub>         | - 28                              | 1               |      |  |
| Single Pulse Avalanche Energy                      | L = 0.1 mH              | E <sub>AS</sub>                   | 7.2             | mJ   |  |
| Maximum Power Dissipation                          | T <sub>C</sub> = 25 °C  | Р                                 | 60 <sup>a</sup> | w    |  |
|  | T <sub>A</sub> = 25 °C  | P <sub>D</sub>                    | 2 <sup>b</sup>  | v    |  |
| Operating Junction and Storage Temperature Range   |                         | T <sub>J</sub> , T <sub>stq</sub> | - 55 to 175     | °C   |  |

| IERMAL RESISTANCE RATINGS        |                        |                   |         |         |      |
|----------------------------------|------------------------|-------------------|---------|---------|------|
| Parameter                        |                        | Symbol            | Typical | Maximum | Unit |
| hundling to Archiegh             | $t \le 10 \text{ sec}$ | R <sub>thJA</sub> | 20      | 25      |      |
| Junction-to-Ambient <sup>b</sup> | Steady State           |                   | 62      | 75      | °C/W |
| Junction-to-Case                 |                        | R <sub>thJC</sub> | 5       | 6       |      |

Notes:

a. See SOA curve for voltage derating.

b. Surface Mounted on 1" x 1" FR-4 boad.

| <b>SPECIFICATIONS</b> $T_J = 25$              |   |  |       | -                | 1     |      |  |
|---|---|--|-------|------------------|-------|------|--|
| Parameter                                     | Symbol  | Test Conditions  | Min   | Тур <sup>а</sup> | Max   | Unit |  |
| Static  |   |  |       |                  |       |      |  |
| Drain-Source Breakdown Voltage                | V <sub>(BR)DSS</sub>  | $V_{GS} = 0 V, I_D = -250 \mu A$   | - 60  |                  |       | v    |  |
| Gate Threshold Voltage                        | V <sub>GS(th)</sub>   | $V_{DS} = V_{GS}, I_D = -250 \ \mu A$  | - 1.0 |                  | - 3.0 |      |  |
| Gate-Body Leakage                             | I <sub>GSS</sub>  | $V_{DS} = 0 V, V_{GS} = \pm 20 V$  |       |                  | ± 100 | nA   |  |
|   |   | $V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$   |       |                  | - 1   |      |  |
| Zero Gate Voltage Drain Current               | I <sub>DSS</sub>  | $V_{DS}$ = - 60 V, $V_{GS}$ = 0 V, $T_{J}$ = 125 °C  |       |                  | - 50  | μA   |  |
|   |   | - 150  |       |                  |       |      |  |
| On-State Drain Current <sup>b</sup>           | I <sub>D(on)</sub>  | V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V  | - 10  |                  |       | А    |  |
|   |   | V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A   |       | 62               |       |      |  |
|   | -   | $V_{GS}$ = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 125 °C   |       | 80               |       | mΩ   |  |
| Drain-Source On-State Resistance <sup>b</sup> | <sup>r</sup> DS(on)   | $V_{GS}$ = - 10 V, I <sub>D</sub> = - 5 A, T <sub>J</sub> = 175 °C   |       | 110              |       |      |  |
|   |   | V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 2 A  |       | 74               |       |      |  |
| Forward Transconductanceb                     | 9 <sub>fs</sub>   | V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A   |       | 8                |       | S    |  |
| Dynamic                                       | +   | •  |       | •                |       |      |  |
| Input Capacitance                             | C <sub>iss</sub>  |  |       | 1300             |       |      |  |
| Output Capacitance                            | C <sub>oss</sub>  | V <sub>DS</sub> = - 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz   |       | 120              |       | pF   |  |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>  |  |       | 90               |       |      |  |
| Total Gate Charge                             | Qg  |  |       | 13               |       |      |  |
| Gate-Source Charge                            | $\begin{array}{c c c c c c c } \hline V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -2 \text{ A} \\ \hline V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -2 \text{ A} \\ \hline V_{DS} = -15 \text{ V}, \text{ I}_{D} = -5 \text{ A} \\ \hline \hline C_{iss} \\ \hline C_{oss} \\ \hline C_{oss} \\ \hline Unce & C_{rss} \\ \hline Q_{g} \\ \hline Q_{g} \\ \hline Q_{gs} \\ \hline Q_{gs} \\ \hline Q_{gd} \\ \hline R_{g} \\ \hline F = 1 \text{ MHz} \\ \hline \hline \\ \hline \\ \hline \\ Ince \\ \hline \\ \hline \\ \hline \\ Ince \\ \hline \\ \hline \\ \hline \\ \hline \\ Ince \\ \hline \\ $ |  | 2.3   |                  | nC    |      |  |
| Gate-Drain Charge                             | Q <sub>gd</sub>   |  |       | 3.2              |       | 1    |  |
| Gate Resistance                               | R <sub>g</sub>  | f = 1 MHz  |       | 8.0              |       | Ω    |  |
| Turn-On Delay Time <sup>c</sup>               | t <sub>d(on)</sub>  |  |       | 5                | 10    |      |  |
| Rise Time <sup>c</sup>                        | tr  | $V_{DD} = -30 \text{ V}, \text{ R}_{\text{L}} = 3.57 \Omega$   |       | 14               | 25    | ns   |  |
| Turn-Off Delay Time <sup>c</sup>              | t <sub>d(off)</sub>   | $\text{I}_{\text{D}}\cong$ - 8.4 A, $\text{V}_{\text{GEN}}$ = - 10 V, $\text{R}_{\text{G}}$ = 2.5 $\Omega$ |       | 15               | 25    |      |  |
| Fall Time <sup>c</sup>                        | t <sub>f</sub>  | 1  |       | 7                | 12    |      |  |
| Source-Drain Diode Ratings and Cha            | racteristics  | (T <sub>C</sub> = 25 °C) <sup>b</sup>  |       | <u> </u>         |       |      |  |
| Pulsed Current                                | I <sub>SM</sub>   |  |       | - 20             |       | А    |  |
| Forward Voltage <sup>b</sup>                  | V <sub>SD</sub>   | I <sub>F</sub> = - 2 A, V <sub>GS</sub> = 0 V  |       | - 0.9            | - 1.3 | V    |  |
| Reverse Recovery Time                         | t <sub>rr</sub>   |  |       | 50               | 80    | ns   |  |
| Reverse Recovery Time                         | Q <sub>rr</sub>   | I <sub>F</sub> = - 8 A, di/dt = 100 A/μs   |       | 80               | 120   | nC   |  |

Notes:

a. Guaranteed by design, not subject to production testing.

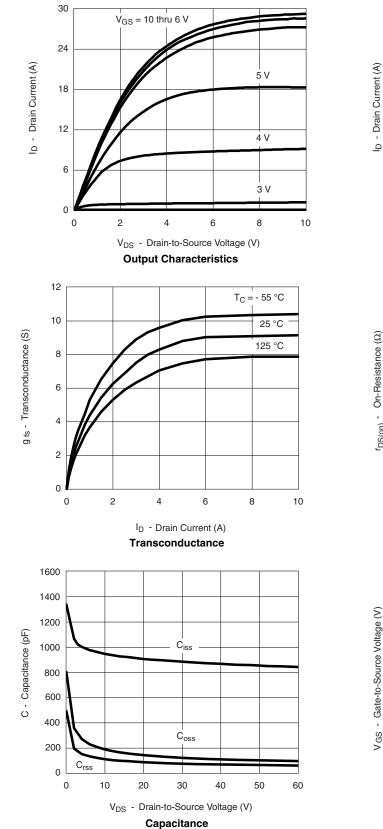
b. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

c. Independent of operating temperature.

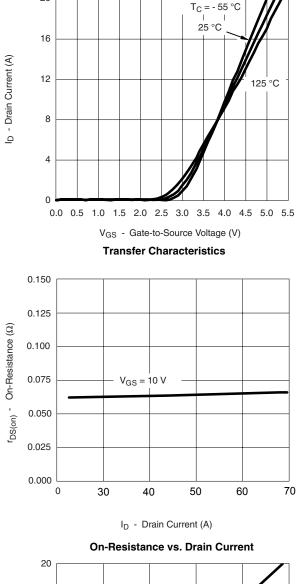
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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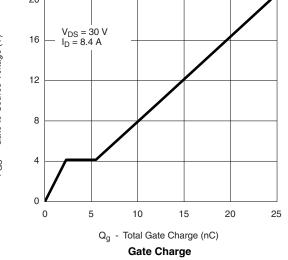




### TYPICAL CHARACTERISTICS 25 °C unless noted

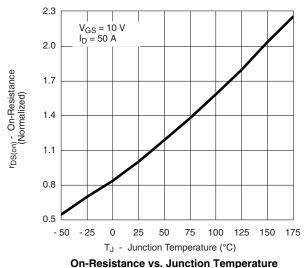


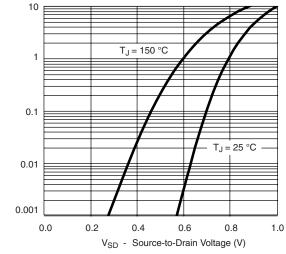
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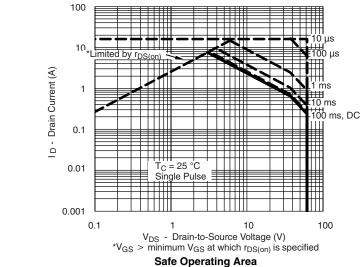
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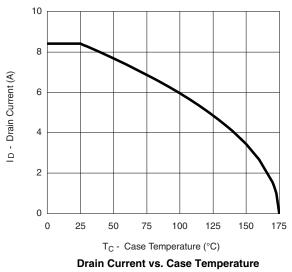


IS - Source Current (A)

Source-Drain Diode Forward Voltage

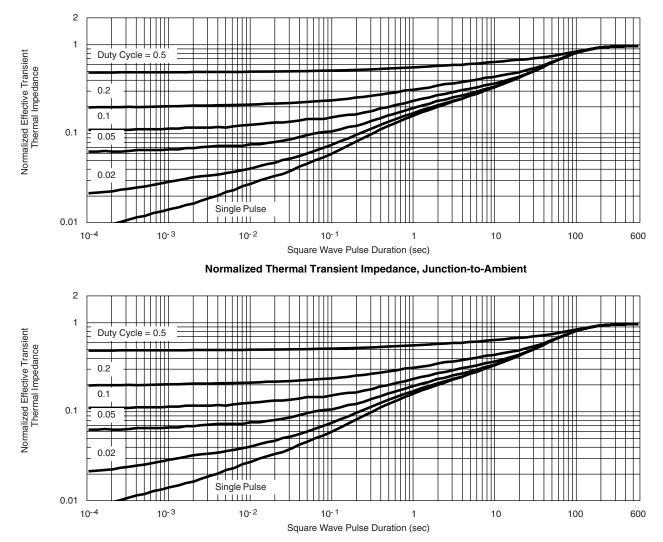


**THERMAL RATINGS** 



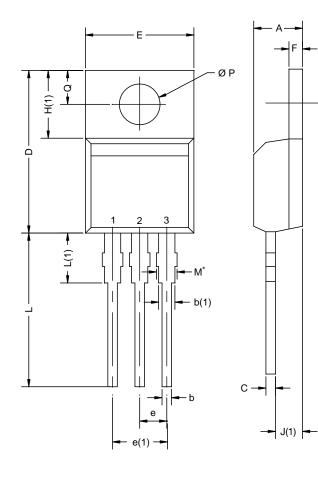


#### THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case





### **TO-220AB**

| MILLIMETERS |   | INC   | HES  |
|-------------|---|---|--|
| MIN.        | MAX.  | MIN.  | MAX.   |
| 4.25        | 4.65  | 0.167   | 0.183  |
| 0.69        | 1.01  | 0.027   | 0.040  |
| 1.20        | 1.73  | 0.047   | 0.068  |
| 0.36        | 0.61  | 0.014   | 0.024  |
| 14.85       | 15.49   | 0.585   | 0.610  |
| 10.04       | 10.51   | 0.395   | 0.414  |
| 2.41        | 2.67  | 0.095   | 0.105  |
| 4.88        | 5.28  | 0.192   | 0.208  |
| 1.14        | 1.40  | 0.045   | 0.055  |
| 6.09        | 6.48  | 0.240   | 0.255  |
| 2.41        | 2.92  | 0.095   | 0.115  |
| 13.35       | 14.02   | 0.526   | 0.552  |
| 3.32        | 3.82  | 0.131   | 0.150  |
| 3.54        | 3.94  | 0.139   | 0.155  |
| 2.60        | 3.00  | 0.102   | 0.118  |
|             | MIN.   4.25   0.69   1.20   0.36   14.85   10.04   2.41   4.88   1.14   6.09   2.41   13.35   3.32   3.54 | MIN. MAX.   4.25 4.65   0.69 1.01   1.20 1.73   0.36 0.61   14.85 15.49   10.04 10.51   2.41 2.67   4.88 5.28   1.14 1.40   6.09 6.48   2.41 2.92   13.35 14.02   3.32 3.82   3.54 3.94 | MIN. MAX. MIN.   4.25 4.65 0.167   0.69 1.01 0.027   1.20 1.73 0.047   0.36 0.61 0.014   14.85 15.49 0.585   10.04 10.51 0.395   2.41 2.67 0.095   4.88 5.28 0.192   1.14 1.40 0.045   6.09 6.48 0.240   2.41 2.92 0.095   13.35 14.02 0.526   3.32 3.82 0.131   3.54 3.94 0.139 |

#### Notes

 $^{\star}$  M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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