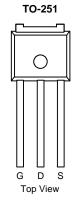
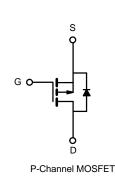


P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^d	Q _g (Typ.)			
- 30	0.018 at V _{GS} = - 10 V	- 40	13 nC			
	0.022 at V _{GS} = - 4.5 V	- 35	13110			





FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested

APPLICATIONS

- Load Switch
- · Battery Switch



ROHS
COMPLIANT
HALOGEN
FREE

ABSOLUTE MAXIMUM RATINGS T	A = 25 °C, unless other	erwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 30	V		
Gate-Source Voltage	V_{GS}	± 20	V		
	T _C = 25 °C		- 40	1	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C	I _D	- 35	A	
Continuous Diain Current (1) = 130 °C)	T _A = 25 °C		- 30.0 ^{a, b}		
	T _A = 70 °C	1	- 28 ^{a, b}		
Pulsed Drain Current	I _{DM} - 150	- 150			
Continuous Courses Drain Diade Current	T _C = 25 °C	L	- 3.5		
Continuous Source-Drain Diode Current	T _A = 25 °C	l _S	- 2.1 ^{a, b}		
	T _C = 25 °C		40	W	
Maximum Dawar Dissipation	T _C = 70 °C] _{B.}	32		
Maximum Power Dissipation	T _A = 25 °C	- P _D	2.5 ^{a, b}		
	T _A = 70 °C]	1.6 ^{a, b}		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, c}	t ≤ 10 s	R _{thJA}	40	50	°C/W	
Maximum Junction-to-Foot	Steady State	R _{thJF}	24	30	C/VV	

Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 95 °C/W.
- d. Based on $T_C = 25$ °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 250A		- 31		m\//0C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	I _D = - 250 μA		4.5		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Cata Valta na Busin Comunet	1	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 5	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α	
		V _{GS} = - 10 V, I _D = - 7.0 A		0.018		1	
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	V _{GS} = - 4.5 V, I _D = - 5.6 A		0.022		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 7.0 A		18		S	
Dynamic ^b				<u> </u>			
Input Capacitance	C _{iss}			1455			
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		180		pF	
Reverse Transfer Capacitance	C _{rss}			145			
·		$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -7.0 \text{ A}$		25	38	3	
Total Gate Charge	Q _g V _{DS} = 13 V, V _{GS} = 10 V, I _D = 7.3 X		13	20	1		
Gate-Source Charge	Q_{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -7.0 \text{ A}$		3.5		nC	
Gate-Drain Charge	Q_{gd}			5.5		1	
Gate Resistance	R _g	f = 1 MHz	0.4	2.0	4.0	Ω	
Turn-On Delay Time	t _{d(on)}			10	20		
Rise Time	t _r			13	20	1	
Turn-Off DelayTime	t _{d(off)}	$I_{D} \cong -5.6 \text{ A}, V_{GEN} = -10 \text{ V}, R_{g} = 1 \Omega$		23	35	1	
Fall Time	t _f	,		9	18	1	
Turn-On Delay Time	t _{d(on)}			38	57	ns	
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 2.7 \Omega$		89	134	1	
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -5.6 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		22	33	1	
Fall Time	t _f	Ĭ		11	17	1	
Drain-Source Body Diode Characteris							
Continous Source-Drain Diode Current	I _S	T _C = 25 °C			- 6.5		
Pulse Diode Forward Current	I _{SM}	-			- 30	A	
Body Diode Voltage	V _{SD}	I _S = - 5.6 A, V _{GS} = 0 V		- 0.71	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			22	33	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 50 A 31/34 400 A/35 T 0500		17	26	nC	
Reverse Recovery Fall Time	t _a	$I_F = -5.6 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		13			
Reverse Recovery Rise Time	t _b			9		ns	

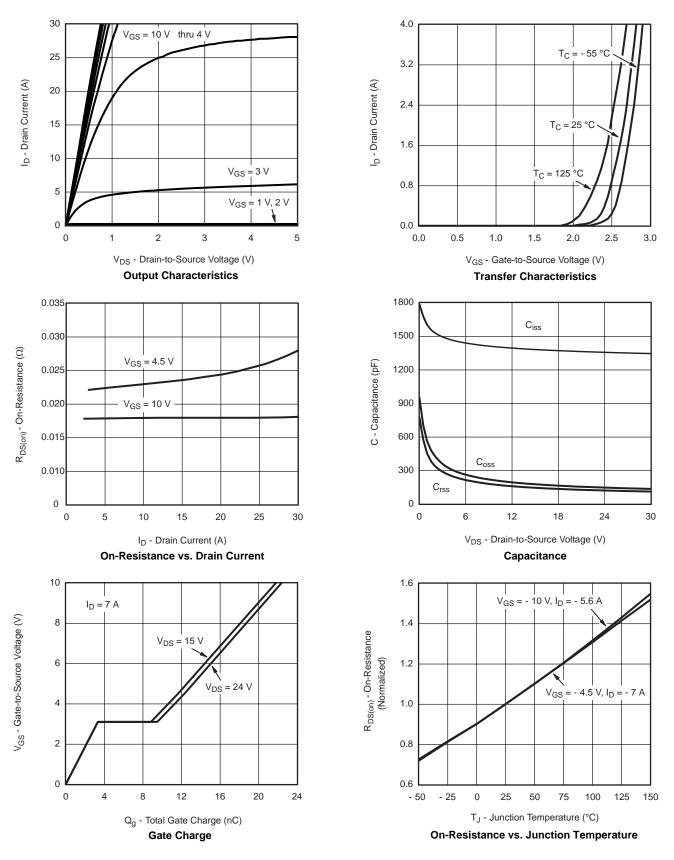
Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

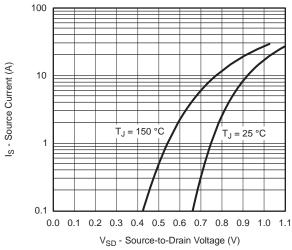
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$

b. Guaranteed by design, not subject to production testing.

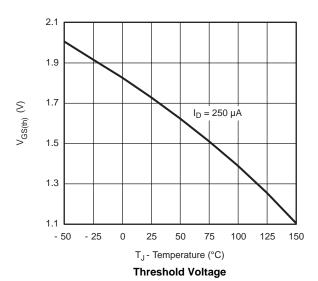








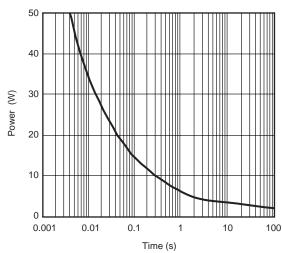
Source-Drain Diode Forward Voltage



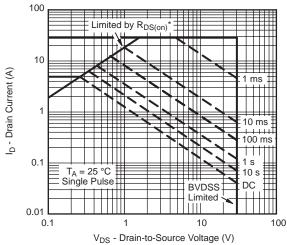
 C_{O} C_{O

V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



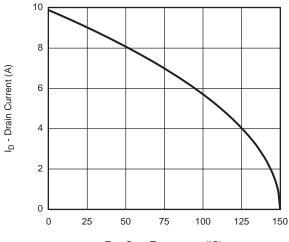
Single Pulse Power, Junction-to-Ambient



* V_{GS} > minimum V_{GS} at which R_{DS(on)} is specified

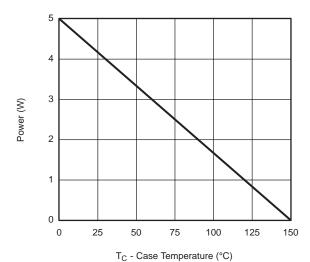
Safe Operating Area



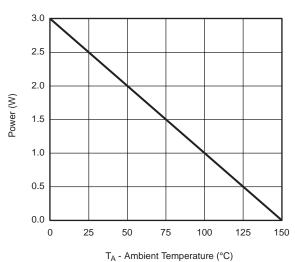


T_C - Case Temperature (°C)

Current Derating*



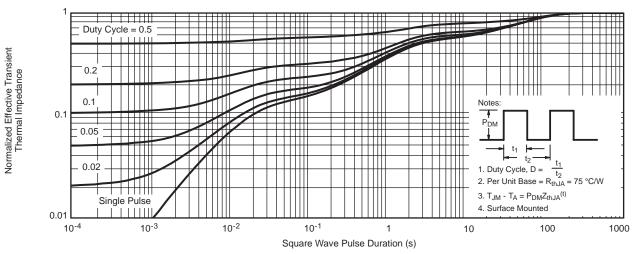
Power, Junction-to-Foot



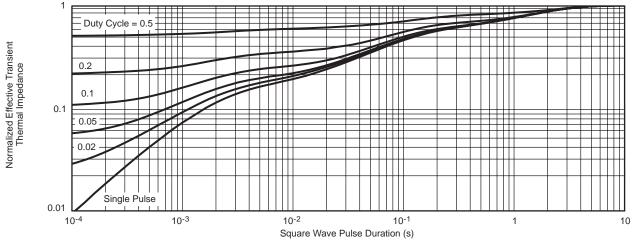
Power Derating, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





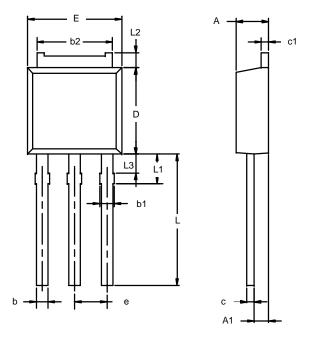
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

	MILLIM	IETERS	INC	HES	
Dim	Min	Max	Min	Max	
Α	2.21	2.38	0.087	0.094	
A 1	0.89	1.14	0.035	0.045	
b	0.71	0.89	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.43	0.206	0.214	
С	0.46	0.58	0.018	0.023	
с1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
Е	6.48	6.73	0.255	0.265	
е	2.28	2.28 BSC 0.090 BSC			
L	3.89	9.53	0.153	0.375	
L1	1.91	2.28	0.075	0.090	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.045	0.060	
ECN: S-03946—Rev. E, 09-Jul-01 DWG: 5346					



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