

P-Channel 100 V (D-S) MOSFET

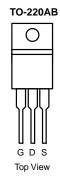
PRODUCT	SUMMARY		
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)
- 100	0.100 at V _{GS} = - 10 V	- 23	11.7
- 100	0.120 at V_{GS} = - 4.5 V	- 20	11.7

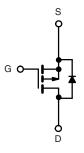
FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Power Switch
- DC/DC Converters





P-Channel MOSFET

ABSOLUTE MAXIMUM RATING	S T _C = 25 °C, unless oth	erwise noted		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 100	v
Gate-Source Voltage		V _{GS}	± 20	v
Continuous Drain Current ($T_1 = 150 \ ^{\circ}C$)	T _C = 25 °C	I	- 23	
Continuous Drain Current (1j = 150°C)	T _C = 70 °C	D'D	- 16	А
Pulsed Drain Current		I _{DM}	- 70	
Avalanche Current		I _{AS}	- 18	
Single Avalanche Energy ^a	L = 0.1 mH	E _{AS}	16.2	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	P	52.1 ^b	14/
	T _A = 25 °C ^c	– P _D –	2.5	— W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient (PCB Mount) ^c	R _{thJA}	50	°C/W
Junction-to-Case (Drain)	R _{thJC}	3.9	0/10

Notes:

a. Duty cycle ≤ 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).



COMPLIANT HALOGEN

MP5940					AB	VBse
					www.V	'Bsemi.
SPECIFICATIONS $T_J = 25^{\circ}$						
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	N		100		1	1
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 V, I_D = -250 \mu A$	- 100			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	- 1		- 2.5	
Gate-Body Leakage	IGSS	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 250	nA
		V _{DS} = - 100 V, V _{GS} = 0 V			- 1	μA
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = - 100 V, V_{GS} = 0 V, T_{J} = 125 °C			- 50	
		V_{DS} = - 100 V, V_{GS} = 0 V, T_{J} = 150 °C			- 250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \leq$ - 10 V, V_{GS} = - 10 V	- 15			А
Durin Courses On Chata Desistence	B	$V_{GS} = -10$ V, $I_D = -3.6$ A		0.100		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 3.4 A		0.120		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -3.6 \text{ A}$		12		S
Dynamic ^b		· · · · · ·				
Input Capacitance	C _{iss}			1055		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = - 50 V, f = 1 MHz		65		
Reverse Transfer Capacitance	C _{rss}			41		
		$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -3.6 \text{ A}$		23.2	34.8	
Total Gate Charge ^c	Qg			11.7	17.6	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = -50 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.6 \text{ A}$		3.5		
Gate-Drain Charge ^c	Q _{gd}			4.8		
Gate Resistance	R _q	f = 1 MHz	1.2	5.7	11.5	Ω
Turn-On Delay Time ^c	t _{d(on)}			7	14	
Rise Time ^c	t _r	V _{DD} = - 50 V, R _I = 17.2 Ω		12	18	- ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 2.9 A, V_{GEN} = - 10 V, R_a = 1 Ω		33	50	
Fall Time ^c	t _f			9	18	
Drain-Source Body Diode Ratings a	nd Characteri	stics T _C = 25 °C ^b		1 -		
Continuous Current	۱ _s				- 8.8	
Pulsed Current	I _{SM}				- 15	A
Forward Voltage ^a	V _{SD}	I _F = - 2.9 A, V _{GS} = 0 V		- 0.8	- 1.5	V
Reverse Recovery Time	t _{rr}			50	75	ns
Peak Reverse Recovery Current	· .	I _F = - 2.9 A, dl/dt = 100 A/μs		- 4	- 6	A
	IRM(REC)	$r_{\rm F} = 2.070, {\rm an} {\rm ar} = 10070 {\rm m}$		- 4	- 0	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

Q_{rr}

c. Independent of operating temperature.

Reverse Recovery Charge

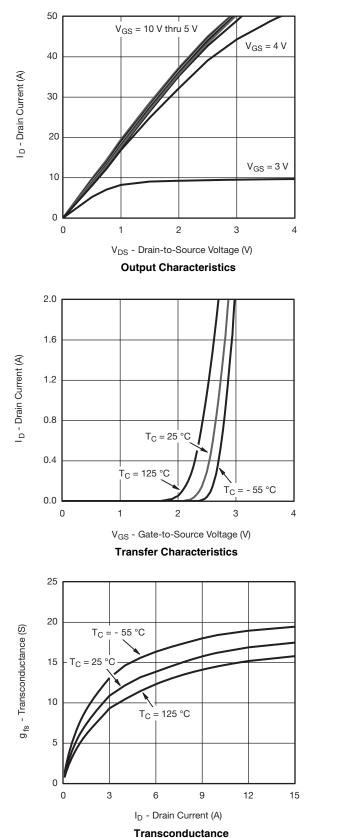
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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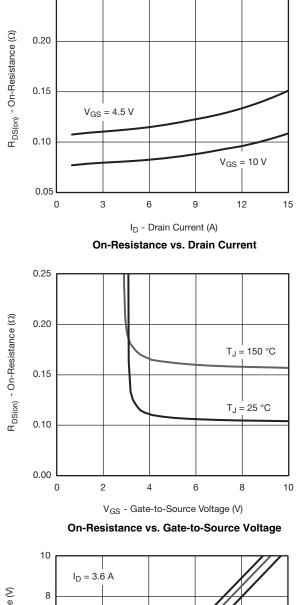
147

nC

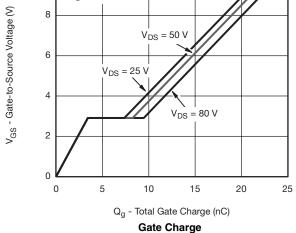




TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

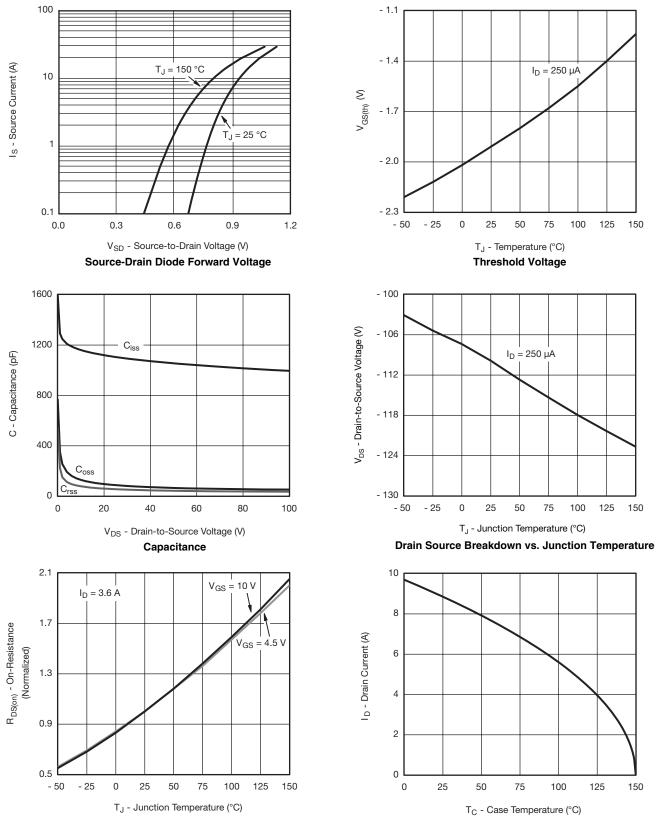


0.25





TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

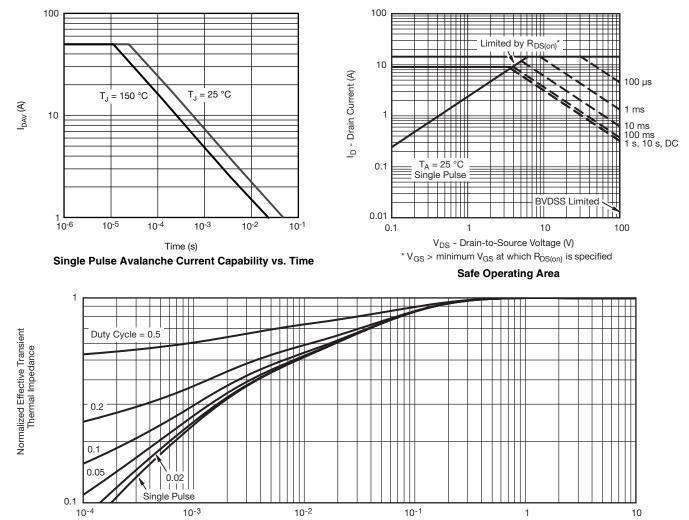


On-Resistance vs. Junction Temperature

Current Derating



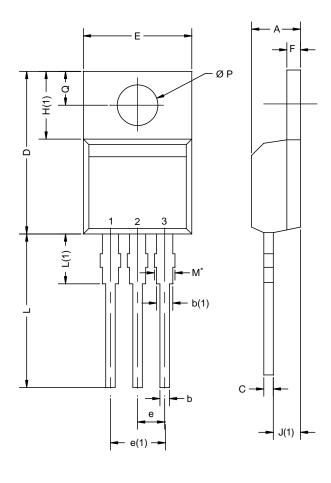
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Case



TO-220AB



	MILLIN	IETERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12- DWG: 547	0208-Rev. N, 1	08-Oct-12			

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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