

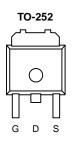
N-Channel 60 V (D-S) MOSFET

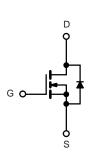
PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^a			
60	0.010 at V _{GS} = 10 V	58			
60	0.013 at V _{GS} = 4.5 V	56			

FEATURES

- 175 °C Junction Temperature
- TrenchFET® Power MOSFET
- Material categorization:







N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)							
Parameter	Symbol	Limit	Unit				
Gate-Source Voltage		V_{GS}	± 20	V			
Continuous Drain Current /T 475 °C\b	T _C = 25 °C	L	58				
Continuous Drain Current (T _J = 175 °C) ^b	T _C = 100 °C	l _D	48 ^a	I			
Pulsed Drain Current	I _{DM}	100	A				
Continuous Source Current (Diode Conduction)	I _S	50 ^a					
Avalanche Current	I _{AS}	50	1				
Single Avalanche Energy (Duty Cycle ≤ 1 %)	L = 0.1 mH	E _{AS}	125	mJ			
Maximum Power Dissipation	T _C = 25 °C	P _D	136	W			
Maximum Fower Dissipation	T _A = 25 °C	' D	3 ^b , 8.3 ^{b, c}]			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Marian and haration to Ambienta	t ≤ 10 sec	R _{thJA}	15	18	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		40	50		
Maximum Junction-to-Case		R _{thJC}	0.85	1.1		

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- $c.\ t \leq 10\ s.$



Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static	Symbol	rest conditions	IVIIII.	тур.	IVIAA.	Onic	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	60	T			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	1	2	3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V, } V_{GS} = \pm 20 \text{ V}$	'		± 100	nA	
Cate Body Leakage	1688	$V_{DS} = 60 \text{ V}, V_{GS} = 20 \text{ V}$			1	ПА	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 \text{ °C}$				μΑ	
Zero Gate voltage Diam Guirent	יטפט	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			250	μΑ	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 6 \text{ V}, V_{TS} = 10 \text{ V}$	60		200	A	
On-State Drain Current	'D(on)	$V_{GS} = 10 \text{ V, } I_{D} = 20 \text{ A}$	00	0.010			
		$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 125 ^{\circ}\text{C}$		0.016		Ω	
Drain-Source On-State Resistance ^b	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 175 \text{ °C}$		0.020			
		$V_{GS} = 4.5 \text{ V}, I_D = 15 \text{ A}$		0.020			
Forward Transconductance ^b	9 _{fs}	$V_{DS} = 15 \text{ V}, I_D = 20 \text{ A}$		60		S	
Dynamic	SIS	V _{DS} = 10 1, 1 _D = 20 11		1 00			
Input Capacitance	C _{iss}			2650			
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		470		pF	
Reverse Transfer Capacitance	C _{rss}	65 - 7 55 - 7		225		Pi	
Total Gate Charge ^c	Q _g			47	70		
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		10		nC	
Gate-Drain Charge ^c	Q _{gd}			12			
Turn-On Delay Time ^c	t _{d(on)}			10	20		
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V}, R_1 = 0.6 \Omega$		15	25		
Turn-Off Delay Time ^c	t _{d(off)}	$V_{DD} = 30 \text{ V}, K_L = 0.0 \Omega$ $I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_q = 2.5 \Omega$		35	50	ns	
Fall Time ^c	t _f	2 2 2 32 4 9		20	30		
Source-Drain Diode Ratings and Cha	'	L T _C = 25 °C)					
Pulsed Current	I _{SM}				60	Α	
Diode Forward Voltage	V _{SD}	I _F = 20 A, V _{GS} = 0 V		1	1.5	V	
Reverse Recovery Time	t _{rr}	I _F = 20 A, di/dt = 100 A/μs		45	100	ns	

Notes:

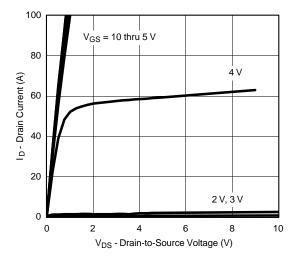
- a. For design aid only; not subject to production testing.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

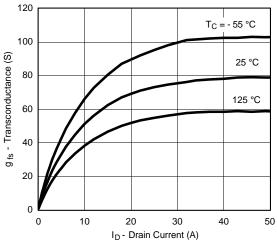
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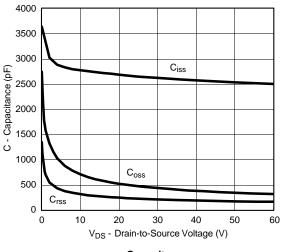
TYPICAL CHARACTERISTICS (25 °C unless noted)



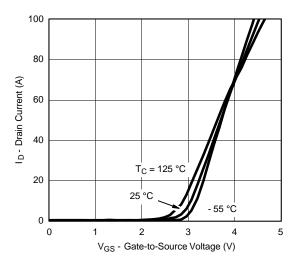
Output Characteristics



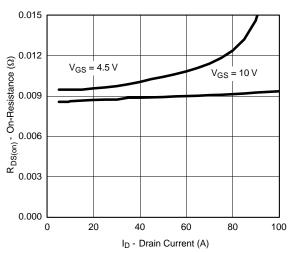
Transconductance



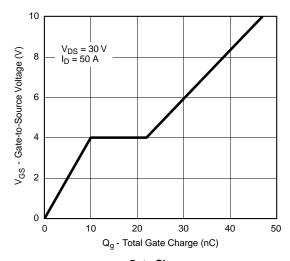
Capacitance



Transfer Characteristics



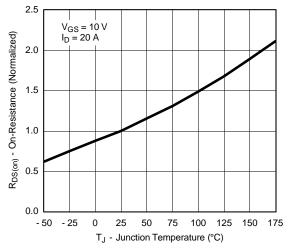
On-Resistance vs. Drain Current



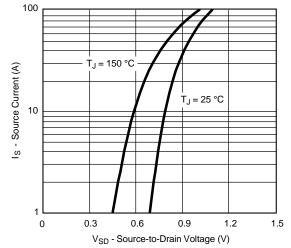
Gate Charge



TYPICAL CHARACTERISTICS (25 °C unless noted)



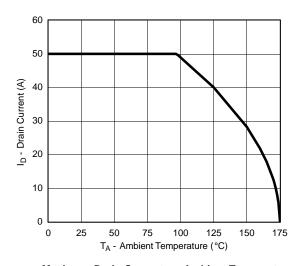
On-Resistance vs. Junction Temperature

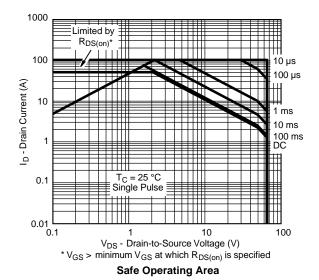


Source-Drain Diode Forward Voltage

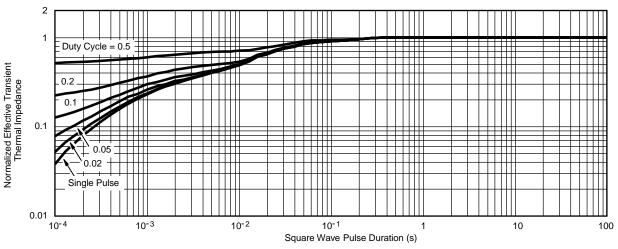


THERMAL RATINGS





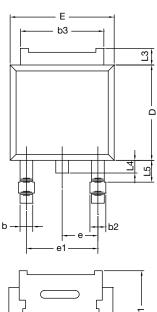
Maximum Drain Current vs. Ambient Temperature

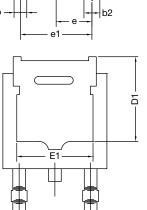


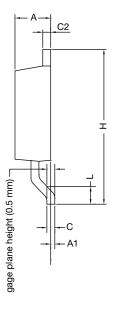
Normalized Thermal Transient Impedance, Junction-to-Case



TO-252AA CASE OUTLINE







	MILLIMETERS		INC	HES		
DIM.	MIN.	MAX.	MIN.	MAX.		
Α	2.18	2.38	0.086	0.094		
A1	-	0.127	-	0.005		
b	0.64	0.88	0.025	0.035		
b2	0.76	1.14	0.030	0.045		
b3	4.95	5.46	0.195	0.215		
С	0.46	0.61	0.018	0.024		
C2	0.46	0.89	0.018	0.035		
D	5.97	6.22	0.235	0.245		
D1	5.21	-	0.205	-		
Е	6.35	6.73	0.250	0.265		
E1	4.32	-	0.170	-		
Н	9.40	10.41	0.370	0.410		
е	2.28	BSC	0.090	BSC		
e1	4.56	4.56 BSC		BSC		
L	1.40	1.78	0.055	0.070		
L3	0.89	1.27	0.035	0.050		
L4	-	1.02	-	0.040		
L5	1.14	1.52	0.045	0.060		
ECN: X12-0247-Rev. M, 24-Dec-12						

ECN: X12-0247-Rev. M, 24-Dec-1: DWG: 5347

Note

• Dimension L3 is for reference only.



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