

## Dual N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)
20	0.086 at V <sub>GS</sub> = 4.5 V	2.6 <sup>a</sup>	5.0 nC
	0.110 at V <sub>GS</sub> = 2.5 V	2.5 <sup>a</sup>	
	0.180 at V <sub>GS</sub> = 1.8 V	2.3 <sup>a</sup>	

### FEATURES

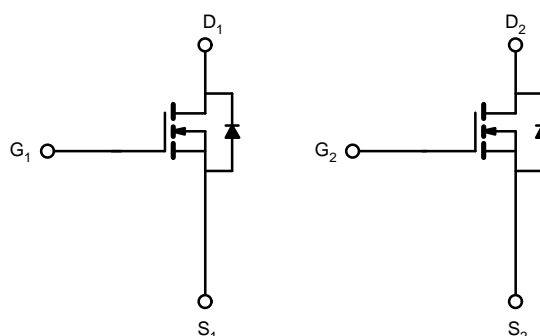
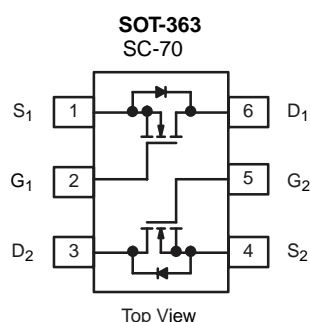
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- Typical ESD Protection 2100 V HBM
- Compliant to RoHS Directive 2002/95/EC



RoHS  
COMPLIANT

### APPLICATIONS

- Load Switch for Portable Applications



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	20	V
Gate-Source Voltage	V <sub>GS</sub>	± 12	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	2.6 <sup>a</sup>
		T <sub>C</sub> = 70 °C	2.2 <sup>a</sup>
		T <sub>A</sub> = 25 °C	2.3 <sup>a, b, c</sup>
		T <sub>A</sub> = 70 °C	1.8 <sup>b, c</sup>
Pulsed Drain Current	I <sub>DM</sub>	8	A
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	
		T <sub>A</sub> = 25 °C	2.10 <sup>b, c</sup>
Maximum Power Dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	2.70
		T <sub>C</sub> = 70 °C	1.70
		T <sub>A</sub> = 25 °C	1.5 <sup>b, c</sup>
		T <sub>A</sub> = 70 °C	1.0 <sup>b, c</sup>
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	R <sub>thJA</sub>	130	170	°C/W	
Maximum Junction-to-Foot (Drain)	R <sub>thJF</sub>	80	100		

Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 220 °C/W.

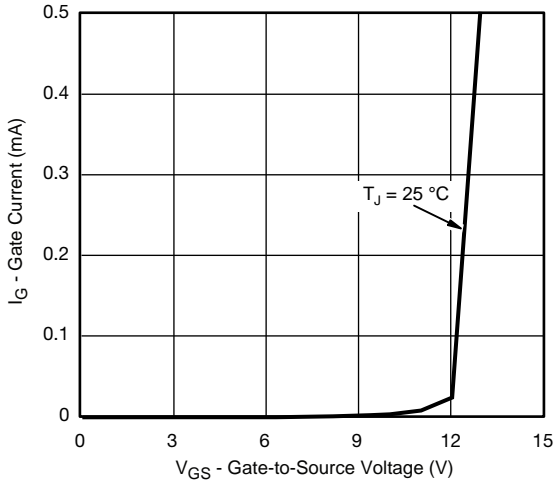
SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	20			V
V <sub>DS</sub> Temperature Coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA		20		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>			- 2.3		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	0.5		2.0	V
Gate-Source Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V			± 25	μA
		V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 4.5 V			1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≤ 5 V, V <sub>GS</sub> = 4.5 V	4			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1 A		0.086		Ω
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 1 A		0.110		
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 0.2 A		0.180		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 4 V, I <sub>D</sub> = 1.5 A		4		S
<b>Dynamic<sup>b</sup></b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 8 V, I <sub>D</sub> = 1.5 A		5.0		nC
				3.0		
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1.5 A		1.0		nC
Gate-Drain Charge	Q <sub>gd</sub>			2.0		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.4	1.9	3.8	kΩ
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, R <sub>L</sub> = 8.3 Ω I <sub>D</sub> ≅ 1.2 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω		43	65	ns
Rise Time	t <sub>r</sub>			80	120	
Turn-Off Delay Time	t <sub>d(off)</sub>			480	720	
Fall Time	t <sub>f</sub>			220	330	
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 10 V, R <sub>L</sub> = 8.3 Ω I <sub>D</sub> ≅ 1.2 A, V <sub>GEN</sub> = 8 V, R <sub>g</sub> = 1 Ω		22	33	ns
Rise Time	t <sub>r</sub>			46	70	
Turn-Off Delay Time	t <sub>d(off)</sub>			645	968	
Fall Time	t <sub>f</sub>			215	323	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C		2.6		A
Pulse Diode Forward Current	I <sub>SM</sub>			4		
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1.2 A, V <sub>GS</sub> = 0 V		0.8	1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.2 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		9	18	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			2	4	nC
Reverse Recovery Fall Time	t <sub>a</sub>			5		ns
Reverse Recovery Rise Time	t <sub>b</sub>			4		

Notes:

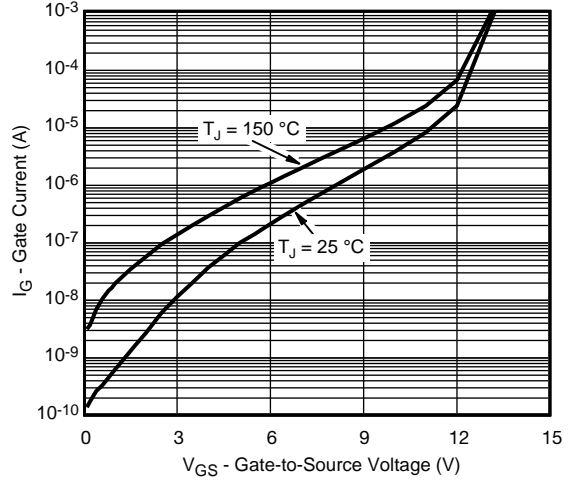
- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

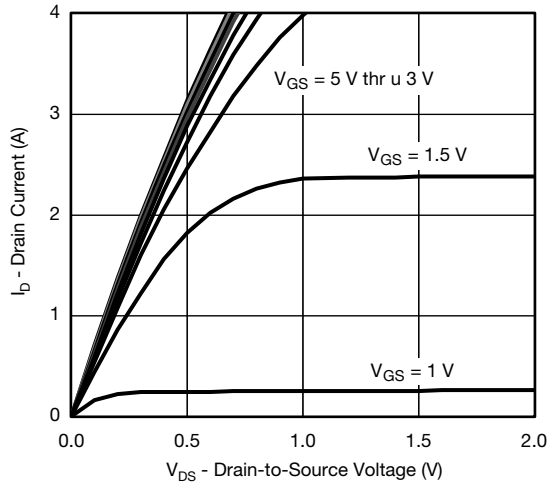
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



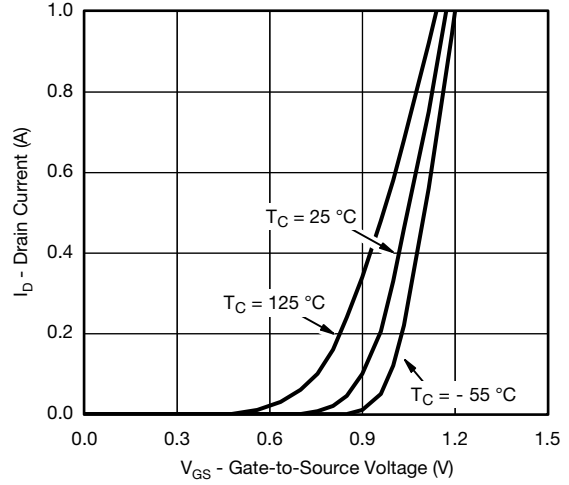
Gate Current vs. Gate-to-Source Voltage



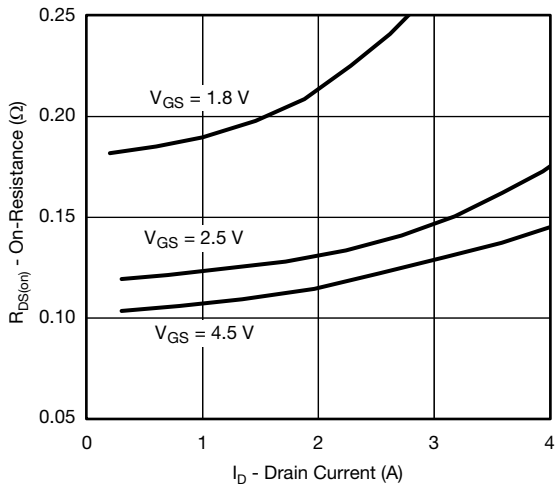
Gate Current vs. Gate-to-Source Voltage



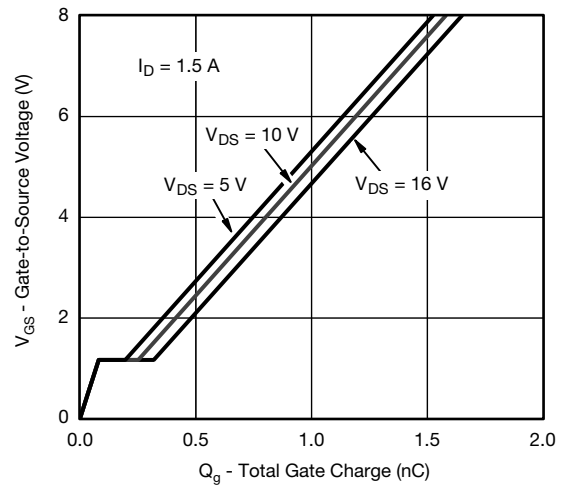
Output Characteristics



Transfer Characteristics



On-Resistance vs. Drain Current

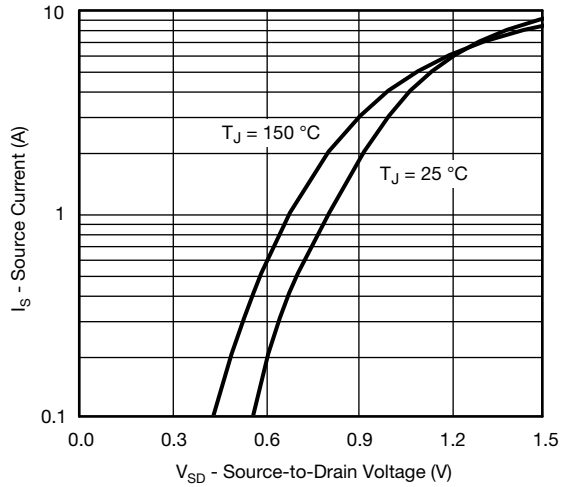


Gate Charge

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**On-Resistance vs. Junction Temperature**



**Source-Drain Diode Forward Voltage**



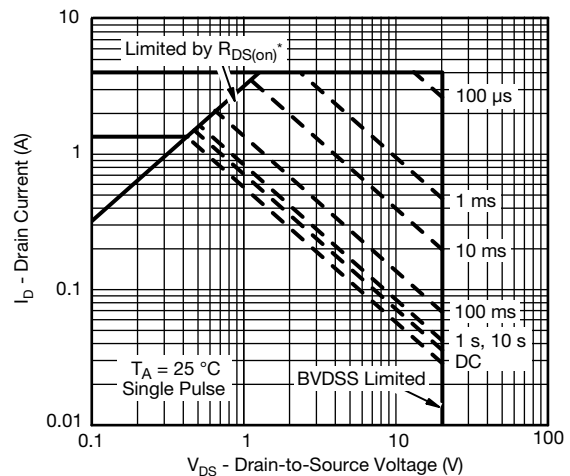
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**

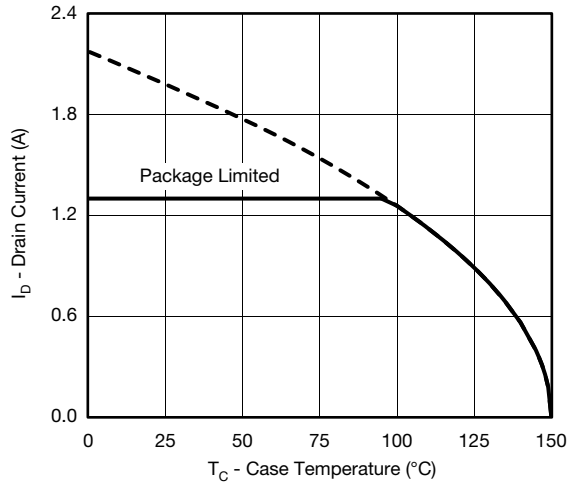


**Single Pulse Power, Junction-to-Ambient**

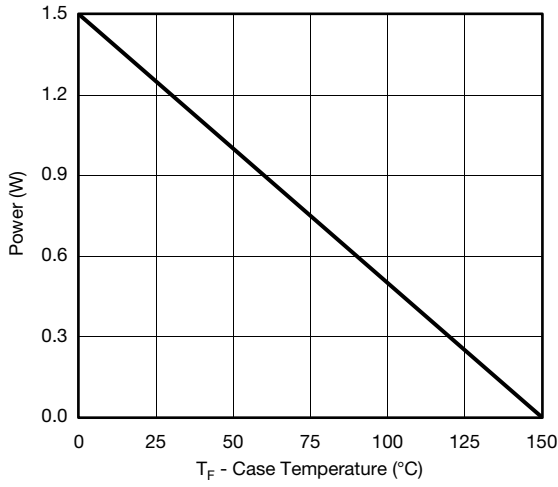


**Safe Operating Area, Junction-to-Ambient**  
\*  $V_{GS} >$  minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

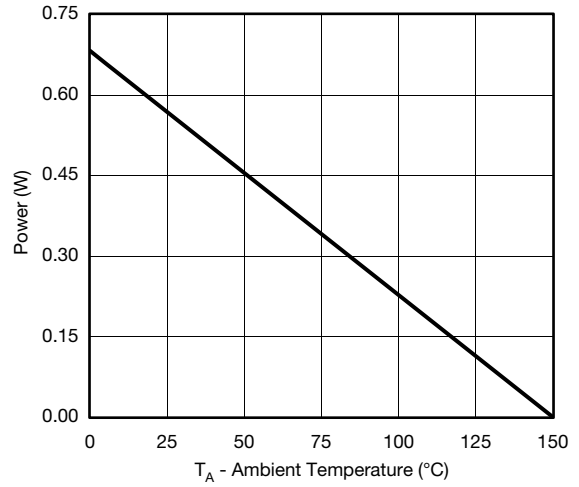
**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Current Derating\***



**Power, Junction-to-Foot**



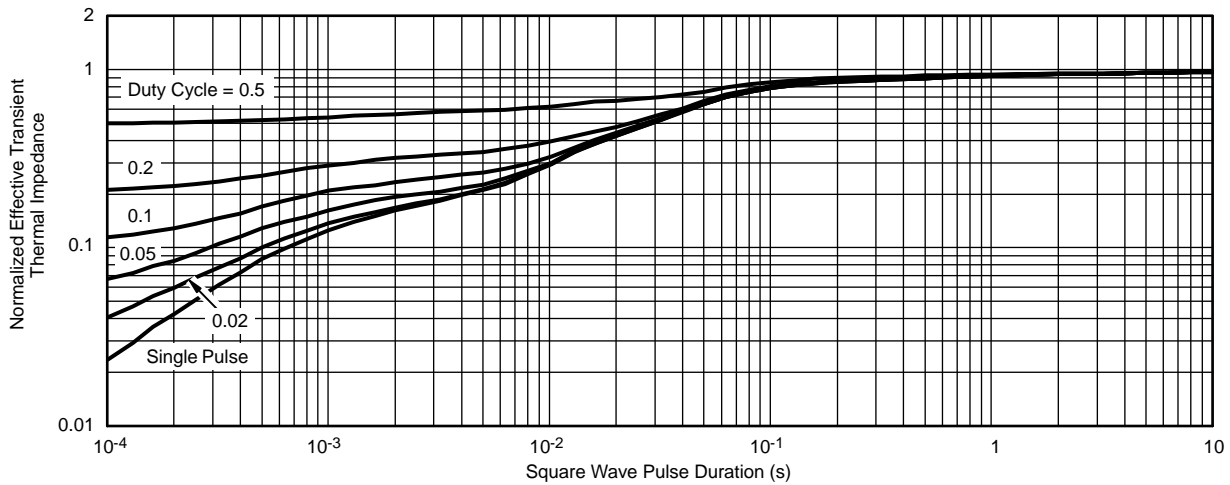
**Power, Junction-to-Ambient**

\* The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

**TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Foot**

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