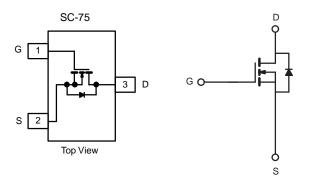


# N-Channel 20 V (D-S) MOSFET

| PRODUCT SUMMARY     |   |                                 |                       |  |  |  |
|---------------------|---|---------------------------------|-----------------------|--|--|--|
| V <sub>DS</sub> (V) | R <sub>DS(on)</sub> (Ω) MAX.              | I <sub>D</sub> (A) <sup>c</sup> | Q <sub>g</sub> (TYP.) |  |  |  |
| 20                  | $0.270 \text{ at V}_{GS} = 4.5 \text{ V}$ | 0.85                            | 4.4.0                 |  |  |  |
|                     | 0.390 at V <sub>GS</sub> = 2.5 V          | 0.70                            | 1.4 nC                |  |  |  |



### **FEATURES**

- TrenchFET® power MOSFET
- 100 % R<sub>g</sub> tested

## **APPLICATIONS**

- Smart phones, tablet PC's
  - DC/DC converters
    - Boost converters
  - Load switch, OVP switch



| <b>ABSOLUTE MAXIMUM RATINGS</b> $(T_A = 25)$       | o°C, unless other      | wise noted)                       |                     |    |  |
|--|------------------------|-----------------------------------|---------------------|----|--|
| PARAMETER  | SYMBOL                 | LIMIT                             | UNIT                |    |  |
| Drain-Source Voltage                               |                        | V <sub>DS</sub>                   | 20                  | V  |  |
| Gate-Source Voltage                                |                        | V <sub>GS</sub>                   | ± 12                | V  |  |
|  | T <sub>C</sub> = 25 °C |                                   | 0.85                |    |  |
| Continuous Drain Current (T. – 150 °C)             | T <sub>C</sub> = 70 °C |                                   | 0.65                |    |  |
| Continuous Drain Current (T <sub>J</sub> = 150 °C) | T <sub>A</sub> = 25 °C | I <sub>D</sub>                    | 0.7 <sup>a, b</sup> |    |  |
|  | T <sub>A</sub> = 70 °C |                                   | 0.6 <sup>a, b</sup> | Α  |  |
| Pulsed Drain Current (t = 300 μs)                  |                        | I <sub>DM</sub>                   | 6                   |    |  |
| Continuous Course Dunie Die de Course              | T <sub>C</sub> = 25 °C |                                   | 0.4                 |    |  |
| Continuous Source-Drain Diode Current              | T <sub>A</sub> = 25 °C | I <sub>S</sub>                    | 0.3                 |    |  |
|  | T <sub>C</sub> = 25 °C |                                   | 0.5                 |    |  |
| Manipular Danier Dispiration                       | T <sub>C</sub> = 70 °C |                                   | 0.3                 | w  |  |
| Maximum Power Dissipation                          | T <sub>A</sub> = 25 °C | P <sub>D</sub>                    | 0.4 <sup>a, b</sup> | VV |  |
|  | T <sub>A</sub> = 70 °C |                                   | 0.3 <sup>a, b</sup> |    |  |
| Operating Junction and Storage Temperature Range   |                        | T <sub>J</sub> , T <sub>stg</sub> | -55 to +150         | °C |  |
| Soldering Recommendations (Peak Temperature)       |                        | 260                               | -0                  |    |  |

| THERMAL RESISTANCE RATINGS       |              |                   |      |      |      |  |
|----------------------------------|--------------|-------------------|------|------|------|--|
| PARAMETER                        |              | SYMBOL            | TYP. | MAX. | UNIT |  |
| Maximum Junction-to-Ambient a, d | t ≤ 10 s     | R <sub>thJA</sub> | 250  | 300  | °C/W |  |
| Maximum Junction-to-Foot (Drain) | Steady State | $R_{thJF}$        | 225  | 270  | C/W  |  |

## Notes

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Based on  $T_C$  = 25 °C.
- d. Maximum under steady state conditions is 360 °C/W.

服务热线:400-655-8788

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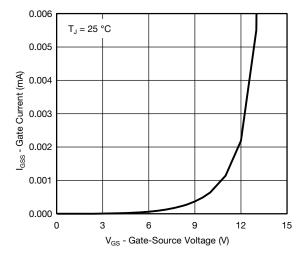
| PARAMETER                                   | SYMBOL                  | TEST CONDITIONS   | MIN. | TYP.  | MAX. | UNIT        |  |
|---|-------------------------|---|------|-------|------|-------------|--|
| Static                                      |                         |   |      |       | •    |             |  |
| Drain-Source Breakdown Voltage              | V <sub>DS</sub>         | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$   | 20   | -     | -    | V           |  |
| V <sub>DS</sub> Temperature Coefficient     | $\Delta V_{DS}/T_{J}$   | L 050 A   | -    | 32    | -    | mV/°C       |  |
| V <sub>GS(th)</sub> Temperature Coefficient | $\Delta V_{GS(th)}/T_J$ | I <sub>D</sub> = 250 μA   | -    | -3    | -    |             |  |
| Gate-Source Threshold Voltage               | V <sub>GS(th)</sub>     | $V_{DS} = V_{GS}, I_{D} = 250 \mu A$  | 0.5  | -     | 1.0  | V           |  |
| Cata Carriaga Laglaga                       |                         | V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 4.5 V  | -    | -     | 0.1  | μΑ          |  |
| Gate-Source Leakage                         | I <sub>GSS</sub>        | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$   | -    | -     | ± 20 |             |  |
| Zana Oala Vallana Buria Oanad               | ,                       | V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V   | -    | -     | 0.1  |             |  |
| Zero Gate Voltage Drain Current             | I <sub>DSS</sub>        | V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C                       | -    | -     | 10   |             |  |
| On-State Drain Current a                    | I <sub>D(on)</sub>      | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$   | 2    | -     | -    | Α           |  |
| Drain-Source On-State Resistance a          | Book )                  | V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 1 A   |      |       | -    |             |  |
| Diani-Source On-State Hesistance            | R <sub>DS(on)</sub>     | V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 0.5 A   | -    | 0.390 | -    | - Ω         |  |
| Forward Transconductance <sup>a</sup>       | 9 <sub>fs</sub>         | V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1.4 A  | -    | 5     | -    | S           |  |
| Dynamic <sup>b</sup>                        |                         |   |      |       |      | •           |  |
| Input Capacitance                           | C <sub>iss</sub>        |   | -    | 105   | -    | pF          |  |
| Output Capacitance                          | C <sub>oss</sub>        | $V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$                            | -    | 23    | -    |             |  |
| Reverse Transfer Capacitance                | C <sub>rss</sub>        |   | -    | 11    | -    |             |  |
| Total Cata Chausa                           | $Q_g$                   | V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1.4 A                      | -    | 2.7   | 4.1  | nC          |  |
| Total Gate Charge                           |                         |   | -    | 1.4   | 2.1  |             |  |
| Gate-Source Charge                          | Q <sub>gs</sub>         | $V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 1.4 \text{ A}$                        | -    | 0.3   | -    |             |  |
| Gate-Drain Charge                           | $Q_{gd}$                |   | -    | 0.5   | -    |             |  |
| Gate Resistance                             | R <sub>g</sub>          | f = 1 MHz   | 1.4  | 7     | 14   | Ω           |  |
| Turn-On Delay Time                          | t <sub>d(on)</sub>      |   | -    | 2     | 4    |             |  |
| Rise Time                                   | t <sub>r</sub>          | $V_{DD} = 15 \text{ V}, R_1 = 13.6 \Omega$  | -    | 9     | 18   |             |  |
| Turn-Off Delay Time                         | t <sub>d(off)</sub>     | $I_D \cong 1.1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$                           | -    | 8     | 16   |             |  |
| Fall Time                                   | t <sub>f</sub>          |   | -    | 8     | 16   |             |  |
| Turn-On Delay Time                          | t <sub>d(on)</sub>      |   | -    | 8     | 16   | ns          |  |
| Rise Time                                   | t <sub>r</sub>          | $V_{DD} = 15 \text{ V}, R_L = 13.6 \Omega$  | -    | 13    | 20   | -<br>-<br>- |  |
| Turn-Off Delay Time                         | t <sub>d(off)</sub>     | $I_D \cong 1.1 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$                          | -    | 15    | 23   |             |  |
| Fall Time                                   | t <sub>f</sub>          |   | -    | 6     | 12   |             |  |
| Drain-Source Body Diode Characterist        | ics                     |   | •    | •     | •    |             |  |
| Continuous Source-Drain Diode Current       | Is                      | T <sub>C</sub> = 25 °C  | -    | -     | 0.4  |             |  |
| Pulse Diode Forward Current <sup>a</sup>    | I <sub>SM</sub>         |   | -    | -     | 6    | A           |  |
| Body Diode Voltage                          | V <sub>SD</sub>         | I <sub>F</sub> = 1.1 A  | -    | 0.8   | 1.2  | V           |  |
| Body Diode Reverse Recovery Time            | t <sub>rr</sub>         |   | -    | 8     | 16   | ns          |  |
| Body Diode Reverse Recovery Charge          | Q <sub>rr</sub>         | Q <sub>rr</sub>   |      | 3     | 6    | nC          |  |
| Reverse Recovery Fall Time                  | t <sub>a</sub>          | $I_F = 1.1 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$ | -    | 5     | -    |             |  |
| Reverse Recovery Rise Time                  | t <sub>b</sub>          |   | -    | 3     | _    | ns          |  |

## Notes

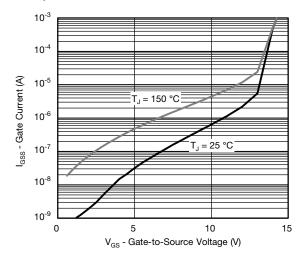
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

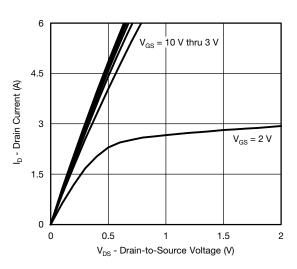




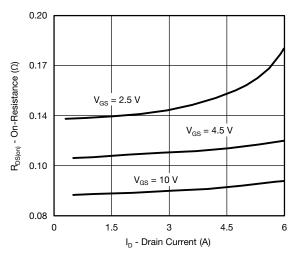




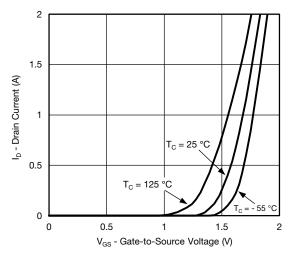
Gate Source Voltage vs. Gate Current

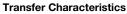


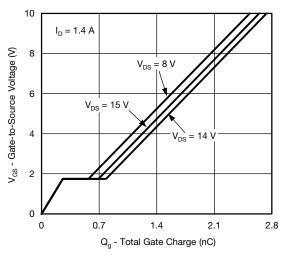
**Output Characteristics** 



On-Resistance vs. Drain Current

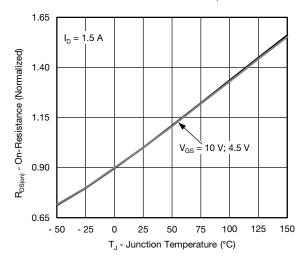




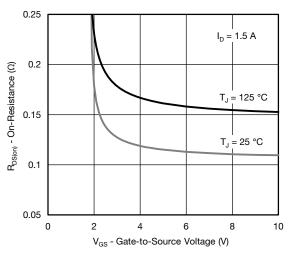


**Gate Charge** 

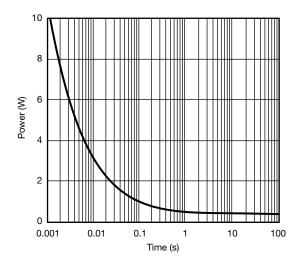




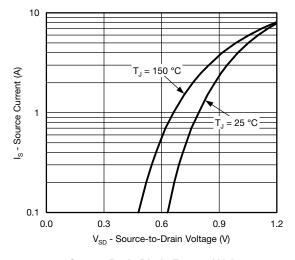
On-Resistance vs. Junction Temperature



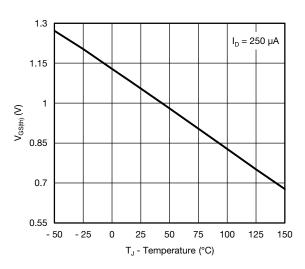
On-Resistance vs. Gate-to-Source Voltage



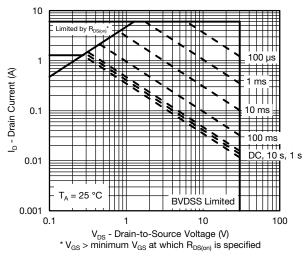
Single Pulse Power, Junction-to-Ambient



Source-Drain Diode Forward Voltage

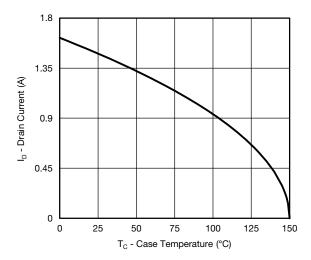


Threshold Voltage

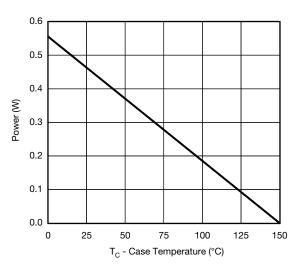


Safe Operating Area, Junction-to-Ambient

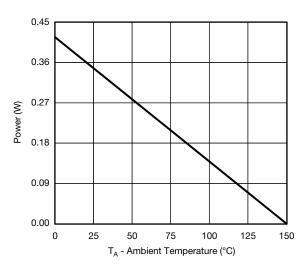




#### **Current Derating\***



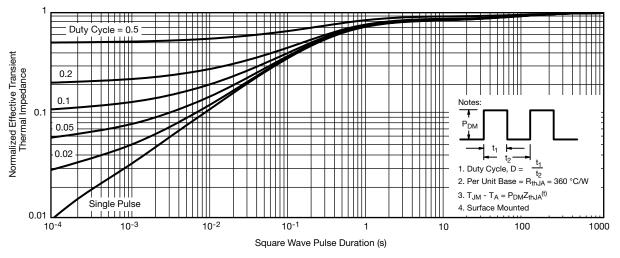




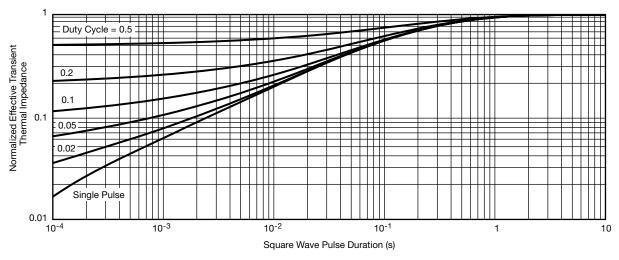
Power, Junction-to-Ambient

<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J \text{ (max.)}} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





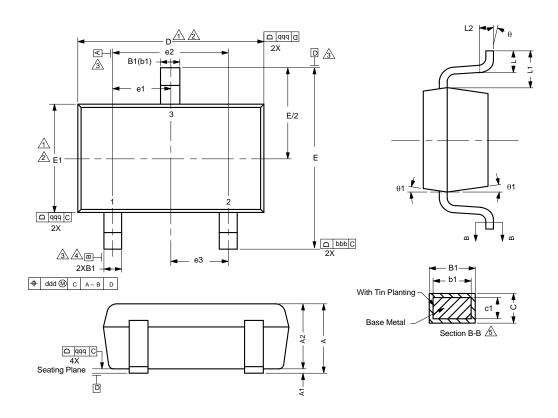
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



## SC-75A: 3 Leads



#### **Notes**

Dimensions in millimeters will govern.

Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.

Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interelead flash, but including any mismatch between the top and bottom of the plastic body.

Datums A, B and D to be determined 0.10 mm from the lead tip.

A Terminal positions are shown for reference only.

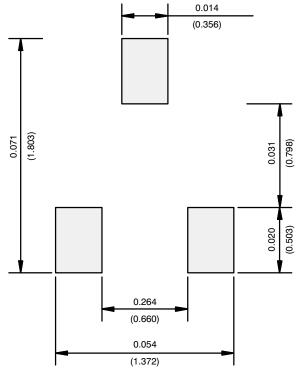
5 These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

| DIMENSIONS | TOLERANCES |  |  |
|------------|------------|--|--|
| aaa        | 0.10       |  |  |
| bbb        | 0.10       |  |  |
| ccc        | 0.10       |  |  |
| ddd        | 0.10       |  |  |

| DIM.                  | I         |       |      |      |
|-----------------------|-----------|-------|------|------|
| Dilvi.                | MIN.      | NOM.  | MAX. | NOTE |
| А                     | -         | -     | 0.80 |      |
| A <sub>1</sub>        | 0.00      | -     | 0.10 |      |
| A <sub>2</sub>        | 0.65      | 0.70  | 0.80 |      |
| B <sub>1</sub>        | 0.19      | -     | 0.24 | 5    |
| b <sub>1</sub>        | 0.17      | -     | 0.21 |      |
| С                     | 0.13      | -     | 0.15 | 5    |
| C <sub>1</sub>        | 0.10      | -     | 0.12 | 5    |
| D                     | 1.48      | 1.575 | 1.68 | 1, 2 |
| Е                     | 1.50      | 1.60  | 1.70 |      |
| E <sub>1</sub>        | 0.66      | 0.76  | 0.86 | 1, 2 |
| e <sub>1</sub>        | 0.50 BSC  |       |      |      |
| e <sub>2</sub>        | 1.00 BSC  |       |      |      |
| <b>e</b> <sub>3</sub> | 0.50 BSC  |       |      |      |
| L                     | 0.15      | 0.205 | 0.30 |      |
| L <sub>1</sub>        | 0.40 ref. |       |      |      |
| L <sub>2</sub>        | 0.15 BSC  |       |      |      |
| θ                     | 0°        | -     | 8°   |      |
| $\theta_1$            | 4°        | -     | 10°  | _    |



## **RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)



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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 DMN2990UFB-7B
IPB80P04P405ATMA2 2N7002W-G MCAC30N06Y-TP MCQ7328-TP NTMC083NP10M5L NVMFS2D3P04M8LT1G BXP7N65D
BXP4N65F AOL1454G WMJ80N60C4 BXP2N20L BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR
DMNH15H110SK3-13 SLF10N65ABV2 BSO203SP BSO211P