

N-Channel 200-V (D-S) MOSFET

| PRODUCT SUMMARY | | | | |
|--------------------------|---------------------------------|--------------------|-----------------------|--|
| V _{(BR)DSS} (V) | $R_{DS(on)}(\Omega)$ | I _D (A) | Q _g (Typ.) | |
| 200 | 0.038 at V _{GS} = 15 V | 45 | 57 | |
| | 0.043 at V _{GS} = 10 V | 40 | 57 | |

FEATURES

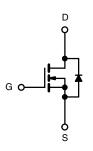
- TrenchFET® Power MOSFETS
- 175 °C Junction Temperature
- 100 % R_g and UIS Tested



APPLICATIONS

- Power Supply
- Lighting Systems





N-Channel MOSFET

| ABSOLUTE MAXIMUM RATINGS | $T_A = 25 ^{\circ}C$, unless oth | erwise noted | | | |
|---|-------------------------------------|-----------------------------------|-----------------|-----|--|
| Parameter | Symbol | Limit | Unit | | |
| Drain-Source Voltage | V _{DS} | 200 | V | | |
| Gate-Source Voltage | | V _{GS} | ± 25 | v | |
| Continuous Drain Current (T _{.1} = 175 °C) | T _C = 25 °C | I_ | 45 | | |
| Continuous Diain Current (1) = 175 C) | T _C = 100 °C | l _D | 26 | | |
| Pulsed Drain Current | | I _{DM} | 150 | Α Α | |
| Single Pulse Avalanche Current | L = 0.1 mH | I _{AS} | 20 | | |
| Single Pulse Avalanche Energy ^a | L = 0.111111 | E _{AS} | 20 | mJ | |
| Maximum Power Dissipation ^a | T _C = 25 °C | В | 55 ^b | 14/ | |
| | T _A = 25 °C ^c | $ P_{D}$ | 3.12 | W | |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 175 | °C | |

| THERMAL RESISTANCE RATINGS | | | | |
|--|-------------------|-------|-------|--|
| Parameter | Symbol | Limit | Unit | |
| Junction-to-Ambient (PCB Mount) ^c | R _{thJA} | 40 | °C/W | |
| Junction-to-Case (Drain) | R _{thJC} | 0.75 | *C/VV | |

Notes:

- a. Duty cycle \leq 1 %.
- b. See SOA curve for voltage derating.
- c. When Mounted on 1" square PCB (FR-4 material).

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| Parameter | Symbol | Test Conditions | Min. | Тур. | Max. | Unit |
|---|----------------------|---|------|-------|-------|------|
| Static | | | | | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$ | 200 | | | V |
| Gate Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_D = 250 \mu A$ | 2.5 | | 4.5 | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$ | | | ± 100 | nA |
| | | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$ | | | ± 300 | |
| Zero Gate Voltage Drain Current | | V _{DS} = 200 V, V _{GS} = 0 V | | | 1 | μΑ |
| | I _{DSS} | V _{DS} = 200 V, V _{GS} = 0 V, T _J = 100 °C | | | 25 | |
| | | V _{DS} = 200 V, V _{GS} = 0 V, T _J = 150 °C | | | 250 | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \ge 10 \text{ V}, V_{GS} = 10 \text{ V}$ | 40 | | | Α |
| Drain-Source On-State Resistance ^a | | V _{GS} = 10 V, I _D = 20 A | | 0.038 | | Ω |
| | | V _{GS} = 15 V, I _D = 20 A | | 0.043 | | |
| | R _{DS(on)} | $V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}, T_J = 100 ^{\circ}\text{C}$ | | 0.088 | | |
| | | V _{GS} = 10 V, I _D = 20 A, T _J = 150 °C | | 0.120 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = 15 V, I _D = 20 A | 25 | | | S |
| Dynamic ^b | | | | • | | |
| Input Capacitance | C _{iss} | | | 3100 | | pF |
| Output Capacitance | C _{oss} | V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz | | 300 | | |
| Reverse Transfer Capacitance | C _{rss} | | | 135 | | |
| | Qg | $V_{DS} = 100 \text{ V}, V_{GS} = 15 \text{ V}, I_D = 50 \text{ A}$ | | 85 | 127 | |
| Total Gate Charge ^c | | | | 57 | 85 | 20 |
| Gate-Source Charge ^c | Q_{gs} | $V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$ | | 14 | | nC |
| Gate-Drain Charge ^c | Q_{gd} | | | 20 | | |
| Gate Resistance | R_{g} | f = 1 MHz | | 1.2 | 1.8 | Ω |
| Turn-On Delay Time ^c | t _{d(on)} | | | 16 | 25 | - ns |
| Rise Time ^c | t _r | V_{DD} = 100 V, R_L = 2 Ω | | 170 | 260 | |
| Turn-Off Delay Time ^c | t _{d(off)} | $I_D \cong 50 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$ | | 27 | 42 | |
| Fall Time ^c | t _f | | | 9 | 18 | |
| Source-Drain Diode Ratings and Cha | aracteristics 7 | 「 _C = 25 °C | | | | |
| Continuous Current | I _S | | | | 36 | |
| Pulsed Current | I _{SM} | | | | 80 | Α |
| Forward Voltage ^a | V _{SD} | I _F = 20 A, V _{GS} = 0 V | | 0.86 | 1.5 | V |
| Reverse Recovery Time | t _{rr} | | | 116 | 175 | ns |
| Peak Reverse Recovery Current | I _{RM(REC)} | | | 9 | 14 | Α |
| Reverse Recovery Charge | Q _{rr} | I _F = 40 A, di/dt = 100 A/μs | | 0.53 | 0.8 | μC |
| Reverse Recovery Fall Time | t _a | , | | 84 | - | |
| Reverse Recovery Rise Time | t _b | | | 32 | | nS |

Notes:

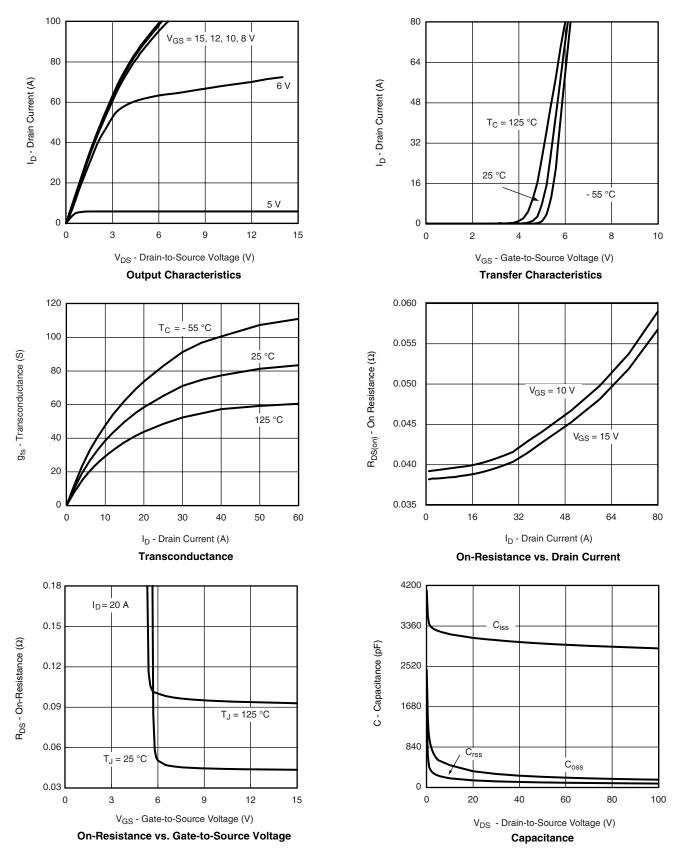
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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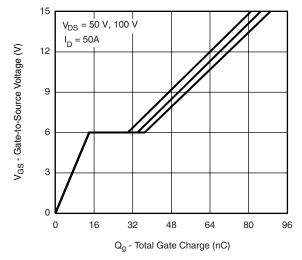


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

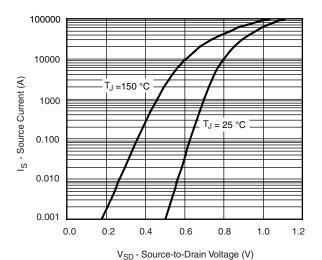




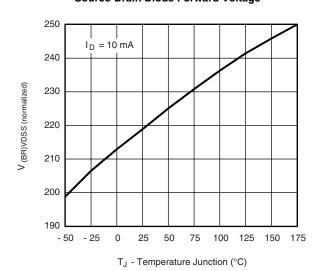
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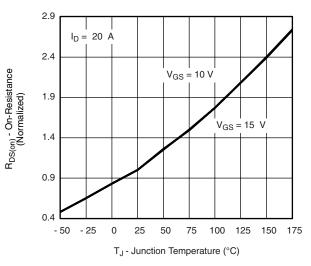




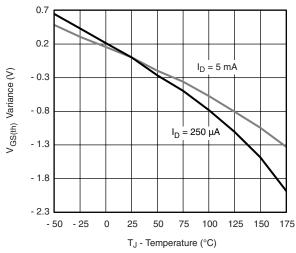
Source-Drain Diode Forward Voltage



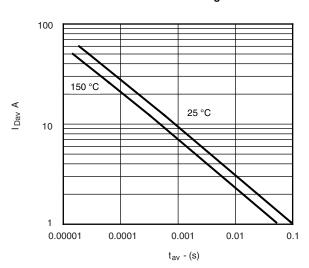
Drain Source Breakdown vs. Junction Temperature



On-Resistance vs. Junction Temperature



Threshold Voltage

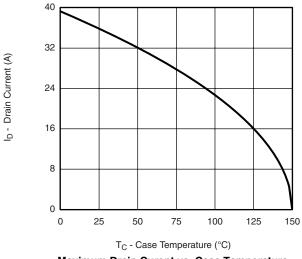


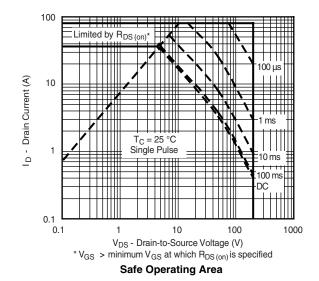
Single Pulse Avalanche Current Capability vs. Time

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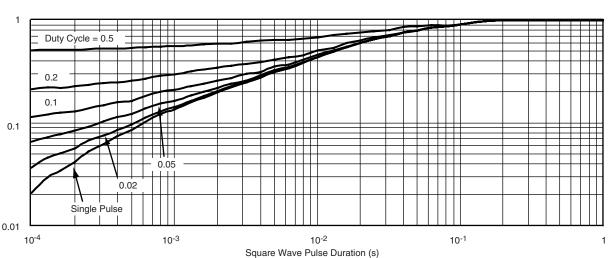


THERMAL RATINGS





Maximum Drain Curent vs. Case Temperature



Normalized Thermal Transient Impedance, Junction-to-Case

Normalized Effective Transient Thermal Impedance

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