

N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A)	Q _g (Typ.)	
40	0.0010 at V _{GS} = 10 V	280	240 nC	
	0.0012 at V _{GS} = 4.5 V	250	240110	

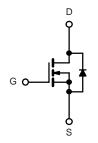
FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested



APPLICATIONS

- · Synchronous Rectification
- Power Supplies



N-Channel MOSFET

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D2PAK (TO-263)

ABSOLUTE MAXIMUM RATINGS	S T _A = 25 °C, unle	ss otherwise not	ed		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	40	V	
Gate-Source Voltage		V _{GS}	± 25]	
	T _C = 25 °C		280		
Continuous Drain Current (T, = 175 °C)	T _C = 70 °C		220		
Continuous Diain Curient (1 _J = 175 °C)	T _A = 25 °C	I _D	229 ^b	\Box A	
	T _A = 70 °C		223 ^b	T A	
Pulsed Drain Current		I _{DM}	750		
Avalanche Current Pulse	L = 0.1 mH	I _{AS}	80	7	
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	320	V	
Continuous Source-Drain Diode Current	T _C = 25 °C	I.	110 ^{a, c}	A	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	2.6 ^b	7 ^	
	T _C = 25 °C		312 ^a		
Maximum Power Dissipation	T _C = 70 °C	P _D	200	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	
	T _A = 25 °C	r _D	3.13 ^b	W	
	T _A = 70 °C		2.0 ^b		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^b	Steady State	R _{thJA}	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R_{thJC}	0.33	0.4]	

Notes:

- a. Based on T_C = 25 °C.
- b. Surface Mounted on 1" x 1" FR4 board.
- c. Calculated based on maximum junction temperature. Package limitation current is 110 \mbox{A} .



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	45			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		41		m\//°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1 _D = 230 μΛ		- 8		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.2		2.5	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	1	V _{DS} = 40 V, V _{GS} = 0 V			1	
	I _{DSS}	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			10	μA
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α
Drain-Source On-State Resistance ^a	D	V _{GS} = 10 V, I _D = 30 A		0.0010		
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.0012		Ω
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_{D} = 30 \text{ A}$		180		S
Dynamic ^b				•		
Input Capacitance	C _{iss}			9335		pF
Output Capacitance	C _{oss}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1150		
Reverse Transfer Capacitance	C _{rss}			850		
Total Gate Charge	Q_{g}			160	260	
Gate-Source Charge	Q_{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 20 \text{ A}$		40		nC
Gate-Drain Charge	Q _{gd}			22		
Gate Resistance	R_{g}	f = 1 MHz		0.85	1.3	Ω
Turn-On Delay Time	t _{d(on)}			20	30	
Rise Time	t _r	V_{DD} = 20 V, R_L = 1.0 Ω		11	17	- ns
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 20 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		77	115	
Fall Time	t _f			10	15	
Turn-On Delay Time	t _{d(on)}			102	155	
Rise Time	t _r	V_{DD} = 20 V, R_L = 1.0 Ω		62	95	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 20 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		180	270	
Fall Time	t _f			60	90	
Drain-Source Body Diode Characteristic	s					
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			110	^
Pulse Diode Forward Current ^a	I _{SM}				200	Α
Body Diode Voltage	V _{SD}	I _S = 20 A		0.8	1.2	V
Body Diode Reverse Recovery Time	t _{rr}			50	75	ns
Body Diode Reverse Recovery Charge	Q _{rr}	1 00 A di/dt 400 A/v- T 05 00		70	105	nC
Reverse Recovery Fall Time	t _a	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		30		
Reverse Recovery Rise Time	t _b	 		20		ns

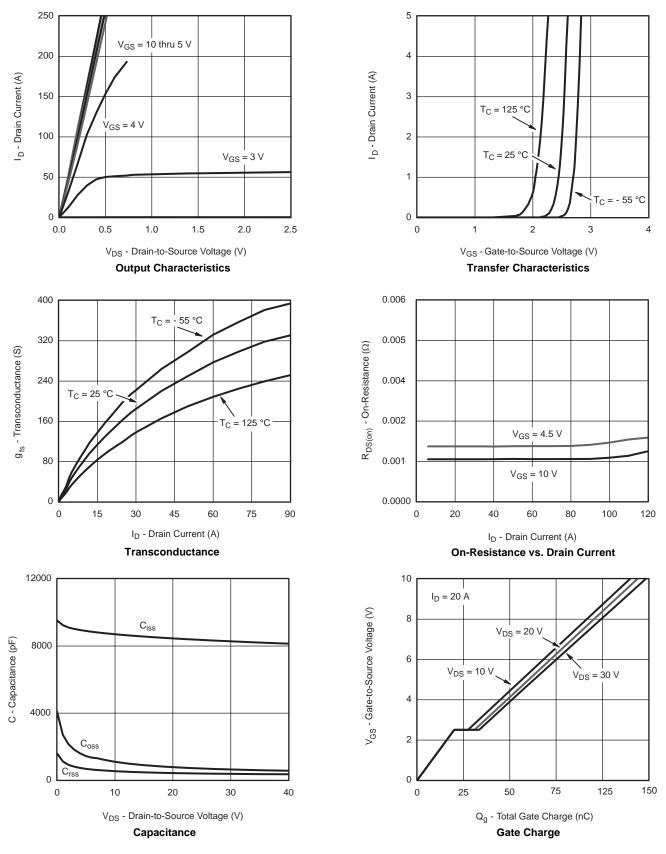
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

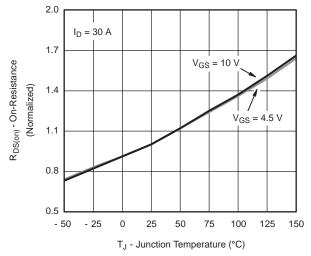


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

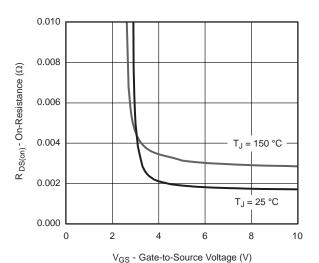




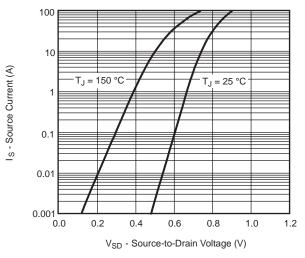
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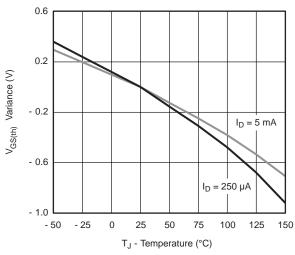
On-Resistance vs. Junction Temperature



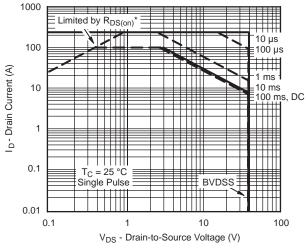
On-Resistance vs. Gate-to-Source Voltage



Forward Diode Voltage vs. Temperature



Threshold Voltage

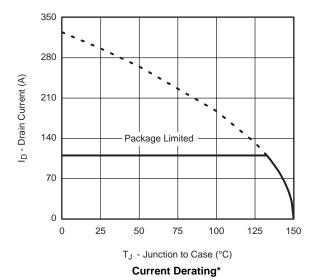


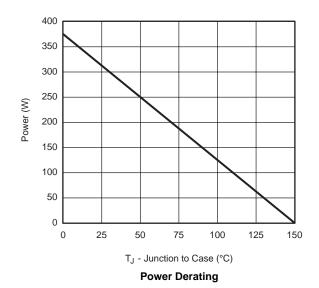
* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

Safe Operating Area, Junction-to-Ambient

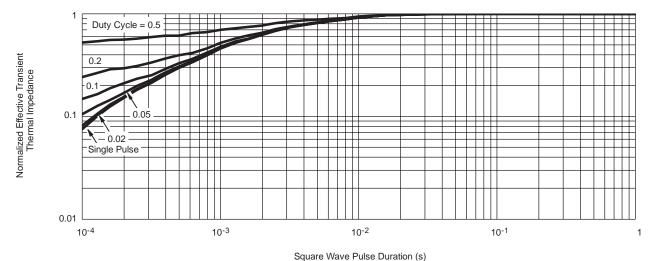


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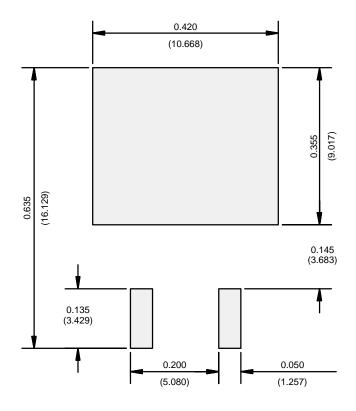
* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Case



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
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