

RoHS

COMPLIANT

N-Channel 60 V (D-S) MOSFET

FEATURES

Definition

• TrenchFET® Power MOSFET

• 100 % R_g and UIS Tested

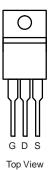
• Halogen-free According to IEC 61249-2-21

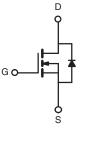
• Package with Low Thermal Resistance

• Compliant to RoHS Directive 2002/95/EC

PRODUCT SUMMARY	
V _{DS} (V)	60
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.003
$R_{DS(on)}$ (Ω) at V_{GS} = 4.5 V	0.009
I _D (A)	210
Configuration	Single

TO-220AB





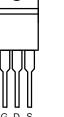
N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T_C =	= 25 °C, unles	s otherwise noted)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	± 20	v
Continuous Drain Current	T _C = 25 °C	1-	210	
	T _C = 125 °C	I _D	120 ^a	
Continuous Source Current (Diode Conduction) ^a		I _S	120 ^a	А
Pulsed Drain Current ^b		I _{DM}	480	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	75	
Single Pulse Avalanche Energy		E _{AS}	281	mJ
Maximum Power Dissipation ^b	T _C = 25 °C	PD	375	W
	T _C = 125 °C	۲D	125	٧V
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.4	C/W

Notes

- a. Package limited.
- b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



SPECIFICATIONS ($T_C = 25 \ ^{\circ}C$	unless otherv	vise noted)					
PARAMETER	SYMBOL	TES	T CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static		•					
Drain-Source Breakdown Voltage	V _{DS}	V _{GS}	= 0, I_D = 250 μ A	60	-	-	v
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS}=V_{GS},\ I_{D}=250\ \mu A$		-	3.5	v
Gate-Source Leakage	I _{GSS}	V _{DS} =	$0 \text{ V}, \text{ V}_{\text{GS}} = \pm 20 \text{ V}$	-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 60 V	-	-	1.0	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μA
		$V_{GS} = 0 V$	V _{DS} = 60 V, T _J = 175 °C	-	-	350	
On-State Drain Current ^a	I _{D(on)}	$V_{GS} = 10 V$	$V_{DS} \ge 5 V$	120	-	-	Α
		$V_{GS} = 10 V$	I _D = 30 A	-	0.003	-	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 30 A, T _J = 125 °C	-	0.006	-	Ω
Drain-Source On-State Resistance	R _{DS(on)}	$V_{GS} = 10 \text{ V}$	I _D = 30 A, T _J = 175 °C	-	0.008	-	
		$V_{GS} = 4.5 V$	I _D = 20 A	-	0.009	-	
Forward Transconductanceb	g _{fs}	V _{DS}	= 15 V, I _D = 30 A	-	109	-	S
Dynamic ^b		• •					•
Input Capacitance	C _{iss}			-	9300	-	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = 25 V, f = 1 MHz	-	1000	-	pF
Reverse Transfer Capacitance	C _{rss}			-	750	-	
Total Gate Charge ^c	Qg			-	180	-	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	V _{DS} = 30 V, I _D = 110 A	-	24.7	-	nC
Gate-Drain Charge ^c	Q _{gd}			-	50.4	-	
Gate Resistance	R _g		f = 1 MHz	0.5	1.1	1.6	Ω
Turn-On Delay Time ^c	t _{d(on)}			-	19	29	
Rise Time ^c	t _r	V _{DD} =	30 V, $R_l = 0.27 \Omega$	-	23	35	1
Turn-Off Delay Time ^c	t _{d(off)}		$V_{GEN} = 10 \text{ V}, \text{ R}_{g} = 2.5 \Omega$	-	83	125	ns
Fall Time ^c	t _f	1		-	35	53	1
Source-Drain Diode Ratings and Char	acteristics ^b					•	<u>.</u>
Pulsed Current ^a	I _{SM}			-	-	480	Α
Forward Voltage	V _{SD}	I _F =	= 100 A, V _{GS} = 0	-	0.9	1.5	V
							•

Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

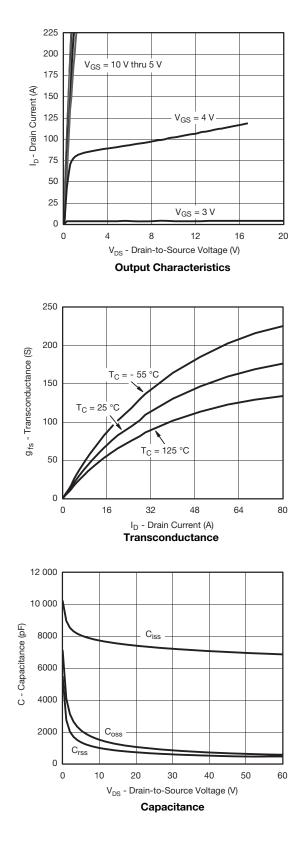
c. Independent of operating temperature.

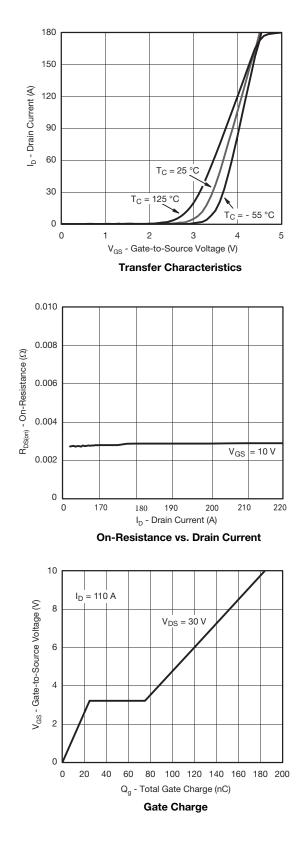
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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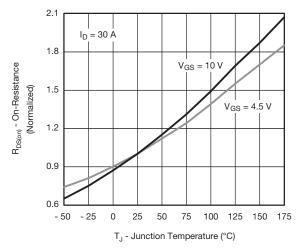
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



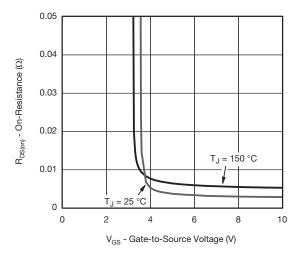




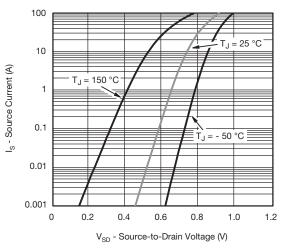
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



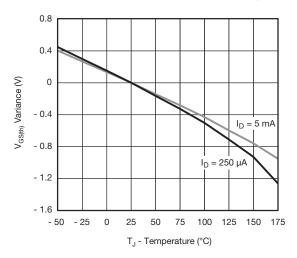




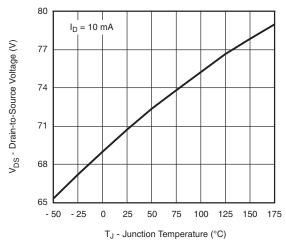
On-Resistance vs. Gate-to-Source Voltage



Source Drain Diode Forward Voltage



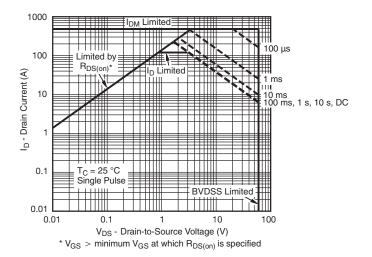




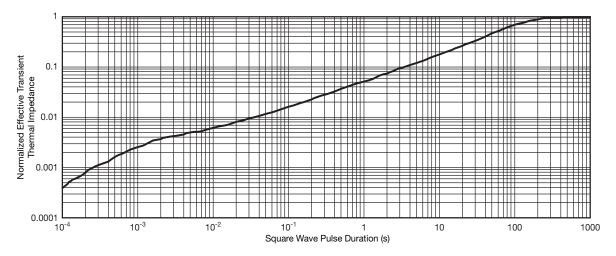
Drain Source Breakdown vs. Junction Temperature



THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



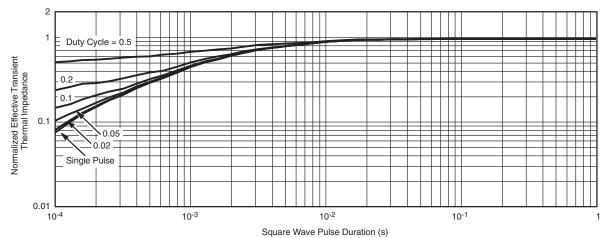
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

• The characteristics shown in the two graphs

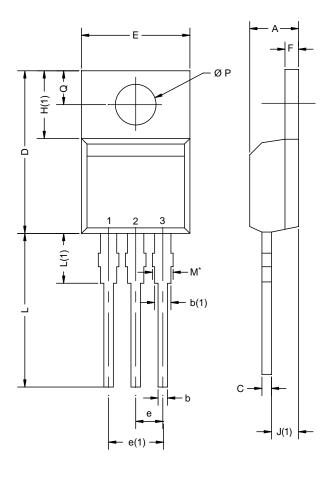
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



TO-220AB



MA 4.6 1.0 1.7 0.6 5 15.4 4 10.4 2.6 5.2	5 0.1 1 0.0 3 0.0 11 0.0 49 0.5 51 0.3 77 0.0	IN.	MAX. 0.183 0.040 0.068 0.024 0.610 0.414 0.105 0.208
1.0 1.7 0.6 5 15.4 4 10.3 2.6 5.2	1 0.0 3 0.0 1 0.0 49 0.5 51 0.3 7 0.0	027 047 014 585 395 095	0.040 0.068 0.024 0.610 0.414 0.105
1.7 0.6 5 15.4 4 10.3 2.6 5.2	3 0.0 1 0.0 49 0.5 51 0.3 7 0.0	047 014 585 395 095	0.068 0.024 0.610 0.414 0.105
0.6 5 15.4 4 10.3 2.6 5.2	1 0.0 49 0.5 51 0.3 7 0.0	014 585 395 095	0.024 0.610 0.414 0.105
5 15.4 4 10.9 2.6 5.2	49 0.5 51 0.3 7 0.0	585 395 095	0.610 0.414 0.105
4 10.9 2.6 5.2	51 0.3 7 0.0	395)95	0.414 0.105
2.6	7 0.0)95	0.105
5.2			
-	8 0.1	92	0 208
			0.200
1.4	0 0.0	045	0.055
6.4	8 0.2	240	0.255
2.9	2 0.0	095	0.115
5 14.0	0.5	526	0.552
3.8	2 0.1	131	0.150
3.9	4 0.1	139	0.155
3.0	0 0.1	102	0.118
	3.9 3.0	3.94 0.1	3.94 0.139 3.00 0.102

Notes

* M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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