

IPT015N10N5-VB Datasheet

N-Channel 100 V (D-S) MOSFET

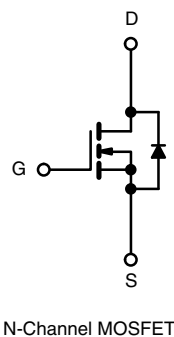
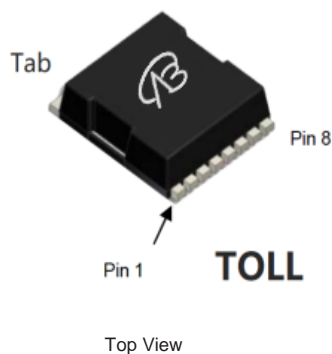
PRODUCT SUMMARY			
V _{DS} (V)	R _{DS(on)} (Ω) MAX.	I _D (A)	Q _g (TYP.)
100	0.002at V _{GS} = 10 V	200	74 nC

FEATURES

- SGT technology Power MOSFET
- Maximum 175 °C junction temperature
- 100 % R_g and UIS tested



RoHS
COMPLIANT
HALOGEN
FREE



APPLICATIONS

- Power supplies:
 - Uninterruptible power supplies
 - AC/DC switch-mode power supplies
 - Lighting
- Synchronous rectification
- DC/DC converter
- Motor drive switch
- DC/AC inverter
- Solar micro inverter
- Class D audio amplifier

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current (T _J = 150 °C)	T _C = 25 °C	I _D	200	A
	T _C = 70 °C		120	
Pulsed Drain Current (t = 100 μs)		I _{DM}	600	
Avalanche Current	L = 0.5 mH	I _{AS}	75	
Single Avalanche Energy ^a		E _{AS}	1500	mJ
Maximum Power Dissipation ^a	T _C = 25 °C	P _D	370 ^b	W
	T _C = 100 °C		180 ^b	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) ^c		R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	0.4	

Notes

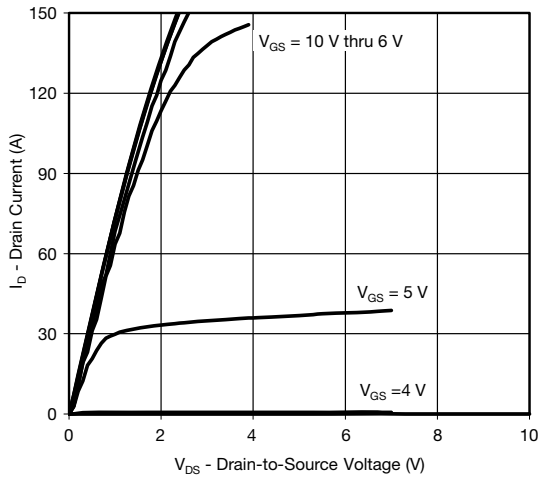
- Duty cycle ≤ 1 %.
- See SOA curve for voltage derating.
- When mounted on 1" square PCB (FR4 material).

SPECIFICATIONS ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise noted)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2.0	-	4.5	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$	-	-	± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
		$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	150	
		$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	5	mA
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \geq 10\text{ V}, V_{GS} = 10\text{ V}$	90	-	-	A
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 50\text{ A}$	-	0.002	-	Ω
		$V_{GS} = 7.5\text{ V}, I_D = 65\text{ A}$	-	0.075	-	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 15\text{ V}, I_D = 30\text{ A}$	-	75	-	S
Dynamic ^b						
Input Capacitance	C_{ISS}	$V_{GS} = 0\text{ V}, V_{DS} = 100\text{ V}, f = 1\text{ MHz}$	-	9600	-	pF
Output Capacitance	C_{OSS}		-	246	-	
Reverse Transfer Capacitance	C_{RSS}		-	21	-	
Total Gate Charge ^c	Q_g	$V_{DS} = 100\text{ V}, V_{GS} = 10\text{ V}, I_D = 60\text{ A}$	-	74	96	nC
Gate-Source Charge ^c	Q_{gs}		-	16.7	-	
Gate-Drain Charge ^c	Q_{gd}		-	16.9	-	
Gate Resistance	R_g	$f = 1\text{ MHz}$	1.5	3	5	Ω
Turn-On Delay Time ^c	$t_{d(on)}$	$V_{DD} = 100\text{ V}, R_L = 1.66\text{ }\Omega$ $I_D \cong 60\text{ A}, V_{GEN} = 10\text{ V}, R_g = 1\text{ }\Omega$	-	15	29	ns
Rise Time ^c	t_r		-	115	210	
Turn-Off Delay Time ^c	$t_{d(off)}$		-	30	70	
Fall Time ^c	t_f		-	80	150	
Drain-Source Body Diode Ratings and Characteristics ^b ($T_C = 25\text{ }^\circ\text{C}$)						
Pulsed Current ($t = 100\text{ }\mu\text{s}$)	I_{SM}		-	-	600	A
Forward Voltage ^a	V_{SD}	$I_F = 10\text{ A}, V_{GS} = 0\text{ V}$	-	0.8	1.2	V
Reverse Recovery Time	t_{rr}	$I_F = 30\text{ A}, di/dt = 100\text{ A}/\mu\text{s}$	-	240	480	ns
Peak Reverse Recovery Charge	$I_{RM(REC)}$		-	11	20	A
Reverse Recovery Charge	Q_{rr}		-	0.9	1.8	μC

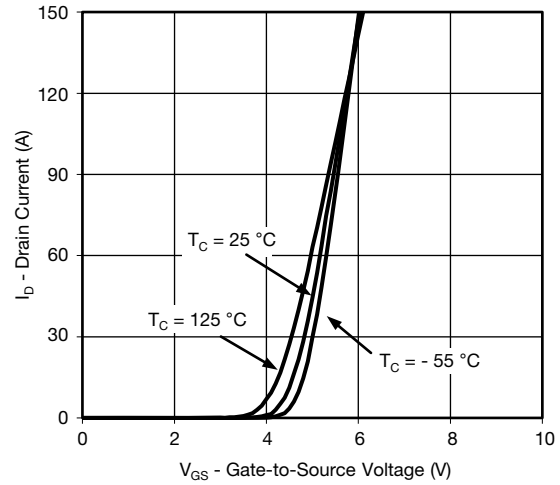
Notes

- a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.
 b. Guaranteed by design, not subject to production testing.
 c. Independent of operating temperature.

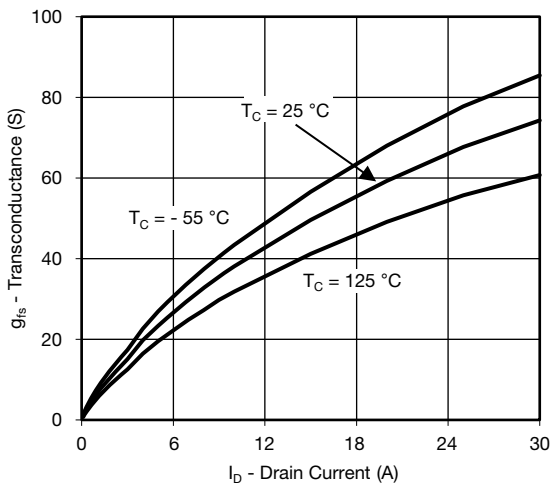
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



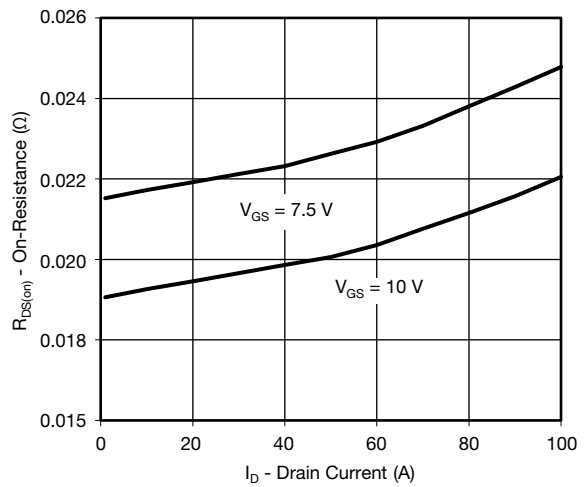
Output Characteristics



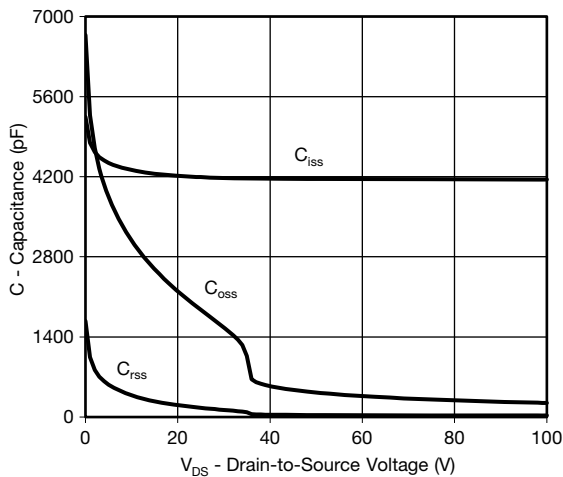
Transfer Characteristics



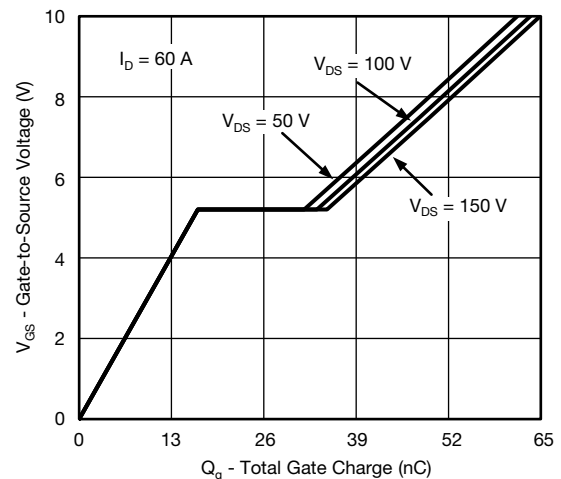
Transconductance



On-Resistance vs. Drain Current

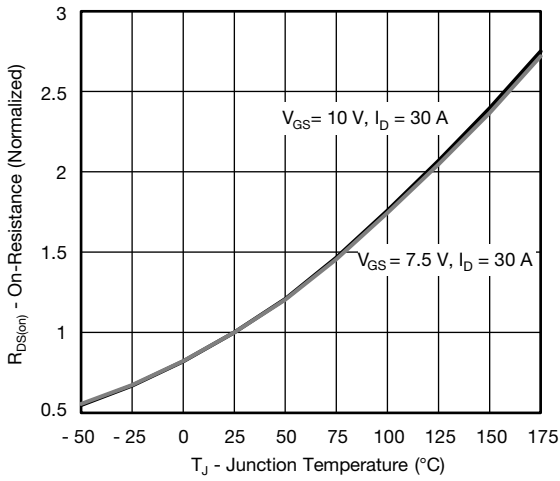


Capacitance

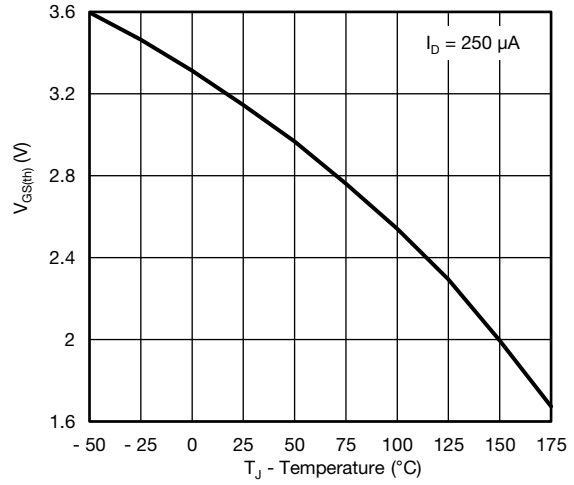


Gate Charge

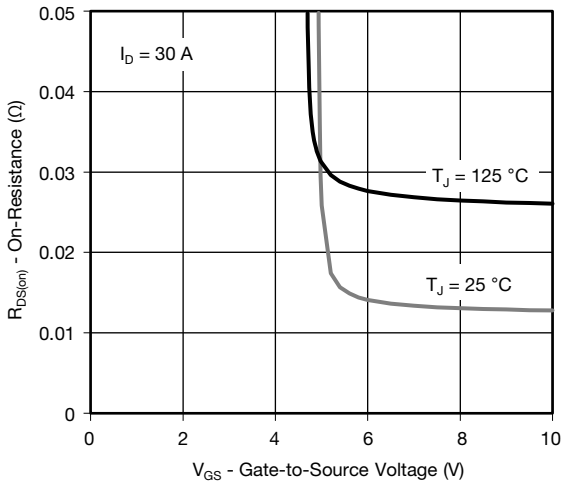
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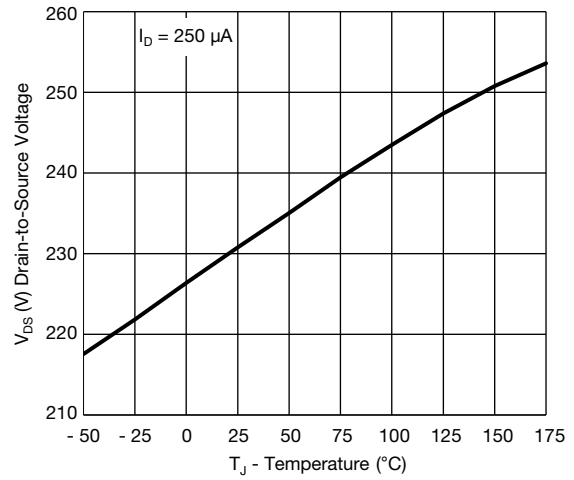
On-Resistance vs. Junction Temperature



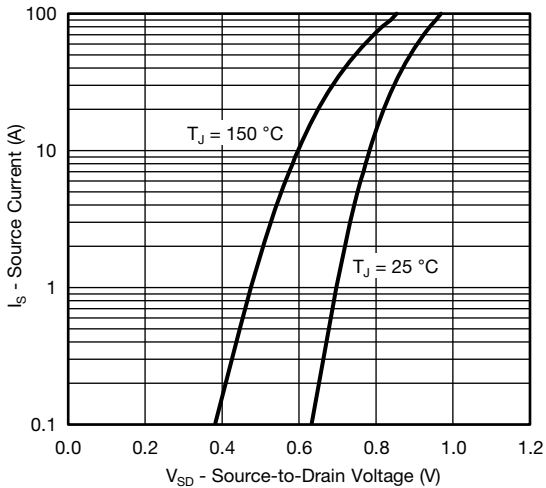
Threshold Voltage



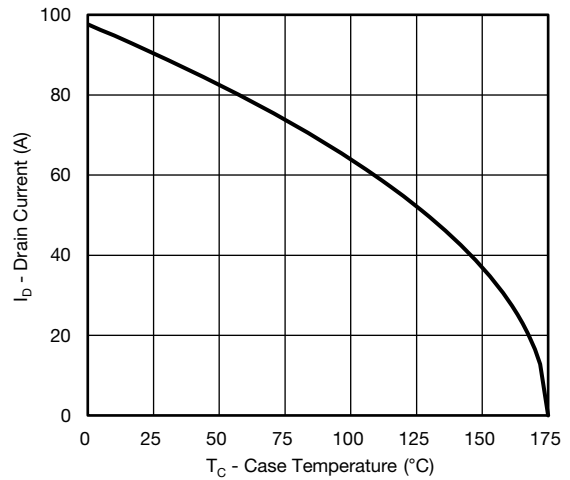
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature

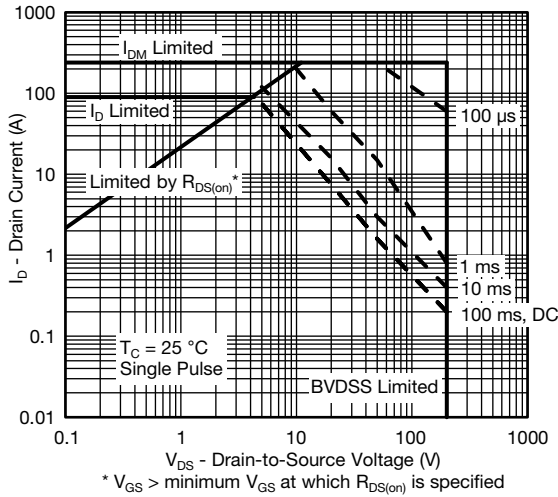


Source Drain Diode Forward Voltage

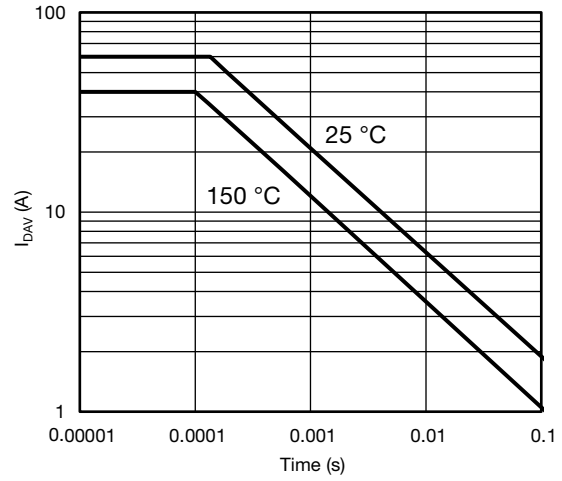


Current De-rating

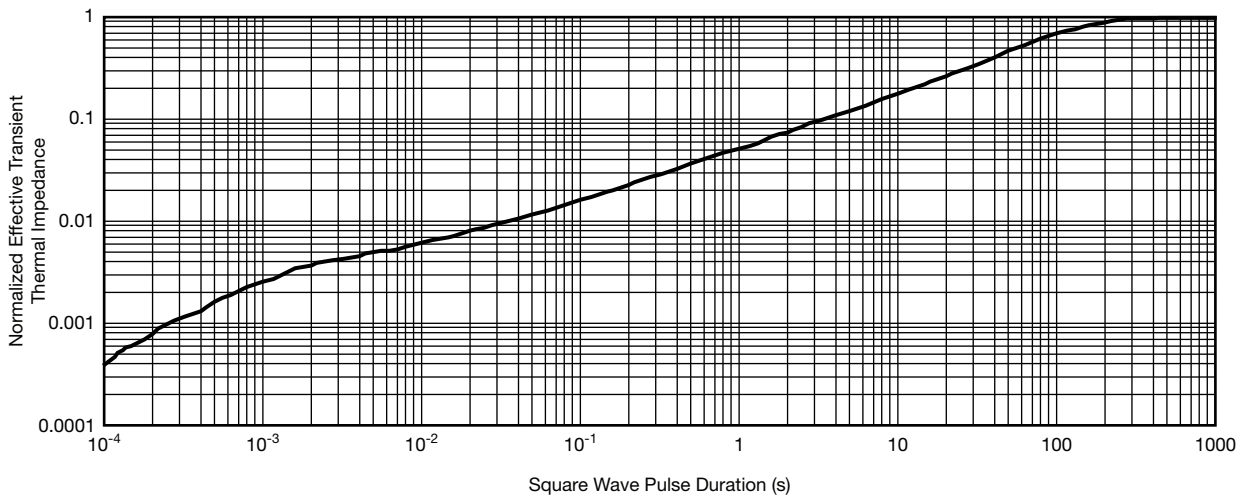
THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)



Safe Operating Area

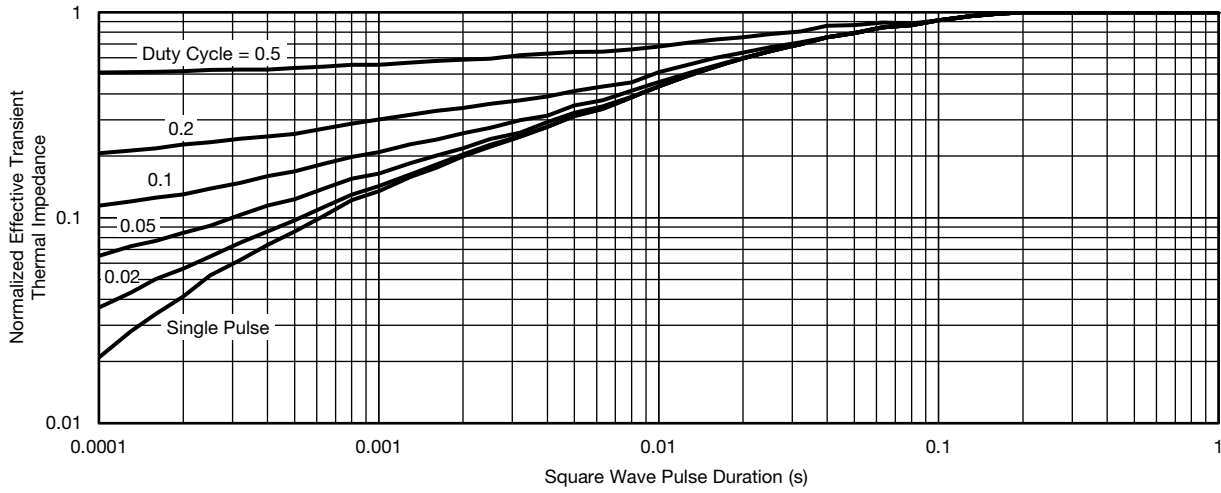


Single Pulse Avalanche Current Capability vs. Time



Normalized Thermal Transient Impedance, Junction-to-Ambient

THERMAL RATINGS ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise noted)

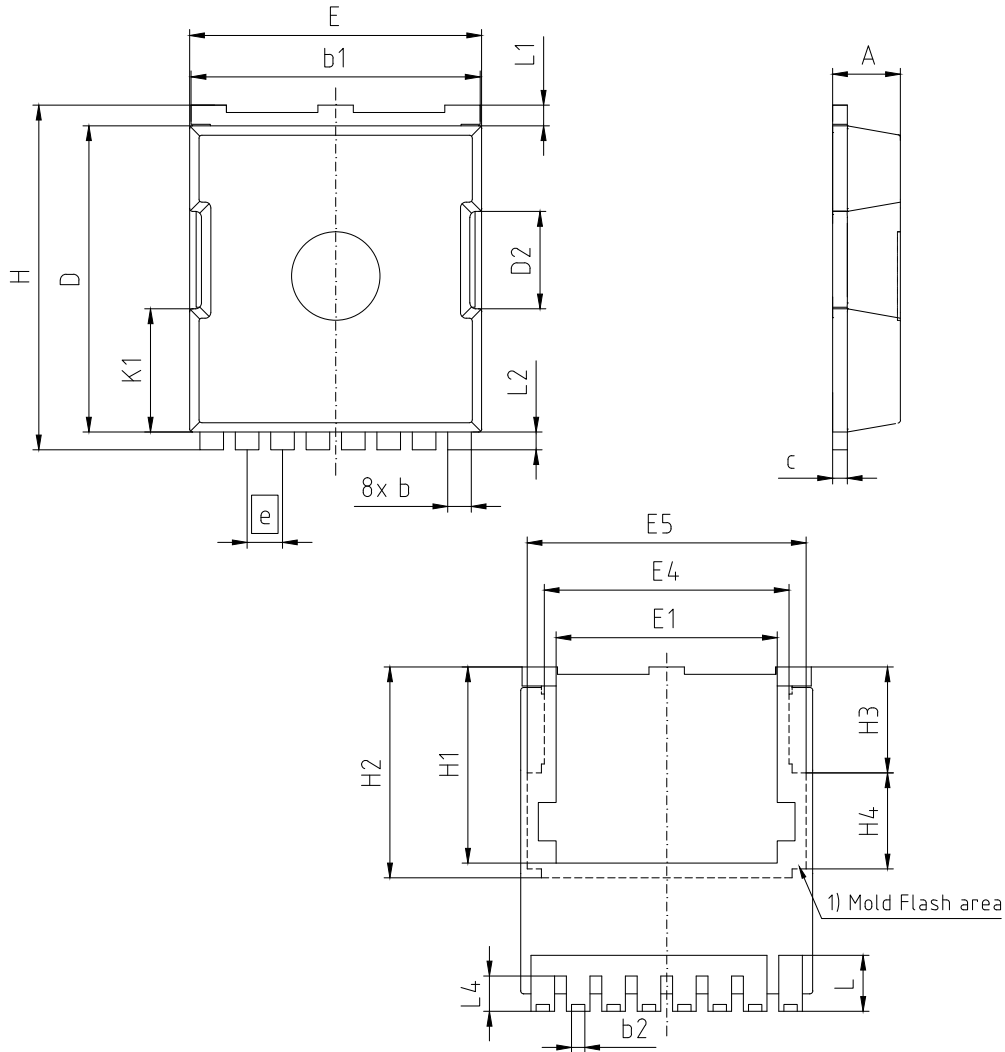


Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction to Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction to Case (25 °C)
 are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Package Outlines



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	2.20	2.40	0.087	0.094
b	0.70	0.90	0.028	0.035
b1	9.70	9.90	0.382	0.390
b2	0.42	0.50	0.017	0.020
c	0.40	0.60	0.016	0.024
D	10.28	10.58	0.405	0.416
D2	3.30		0.130	
E	9.70	10.10	0.382	0.398
E1	7.50		0.295	
E4	8.50		0.335	
E5	9.46		0.372	
e	1.20 (BSC)		0.047 (BSC)	
H	11.48	11.88	0.452	0.468
H1	6.55	6.75	0.258	0.266
H2	7.15		0.281	
H3	3.59		0.141	
H4	3.26		0.128	
N	8		8	
K1	4.18		0.165	
L	1.60	2.10	0.063	0.083
L1	0.70		0.028	
L2	0.60		0.024	
L4	1.00	1.30	0.039	0.051

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SCALE

EUROPEAN PROJECTION

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REVISION
02

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