

P-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ.)		
- 100	0.200 at V _{GS} = - 10 V	- 3.0	13.2 nC		
- 100	0.230 at V _{GS} = - 6 V	- 2.4	13.2110		

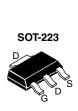
FEATURES

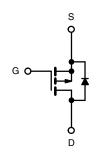
- TrenchFET[®] Power MOSFET
- 100% R_q and UIS Tested

RoHS COMPLIANT HALOGEN FREE

APPLICATIONS

- Active Clamp in Intermediate DC/ DC Power Supplies
- H-Bridge High Side Switch for Lighting Application





P-Channel MOSFET

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	- 100	V
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		- 3.0	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C		-2.1	
Continuous Drain Current (1) = 150 C)	T _A = 25 °C	l _D	- 2 ^{a, b}	
	T _A = 70 °C		- 1.6 ^{a, b}	
Pulsed Drain Current		I _{DM}	- 12	A
Continuous Course Dunin Diada Current	T _C = 25 °C		- 4.9	
Continuous Source-Drain Diode Current	T _A = 25 °C	ls =	- 2.5 ^{a, b}	
Avalanche Current		I _{AS}	- 15	
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	11.25	mJ
	T _C = 25 °C		6.5	
Manine de Deute Dissipation	T _C = 70 °C	ь	4.8	w
Maximum Power Dissipation	T _A = 25 °C	P _D	3.1 ^{a, b}	vv
	T _A = 70 °C		2 ^{a, b}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{a, b}	t ≤ 10 s	R_{thJA}	33	40	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	17	21	C/ VV

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. Maximum under steady state conditions is 80 $^{\circ}\text{C/W}.$

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Parameter	Symbol	Symbol Test Conditions		Тур.	Max.	Unit
Static				•	•	,
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$				V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μA		- 165		mV/°C
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$,		- 6.6		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 2		- 4	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zava Cata Valtaga Dvain Current	1	V _{DS} = - 100 V, V _{GS} = 0 V			- 1	
Zero Gate Voltage Drain Current	IDSS	V _{DS} = - 100 V, V _{GS} = 0 V, T _J = 55 °C			- 10	μΑ
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 8			Α
5	В	$V_{GS} = -10 \text{ V}, I_D = -3 \text{ A}$		0.200		Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 6 V, I _D = - 2 A		0.230		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = 3 A		12		S
Dynamic ^b						
Input Capacitance	C _{iss}			819		
Output Capacitance	C _{oss}	$V_{DS} = -35 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		51		pF
Reverse Transfer Capacitance	C _{rss}			32		
Tatal Oata Obanna		$V_{DS} = -50 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -3 \text{ A}$		17.5	32	
Total Gate Charge	Q_g			13.2	25	
Gate-Source Charge	Q_{gs}	$V_{DS} = -50 \text{ V}, V_{GS} = -6 \text{ V}, I_{D} = -3 \text{ A}$		3.4		nC
Gate-Drain Charge	Q_{gd}			6.4		
Gate Resistance	R_{g}	f = 1 MHz		6.1	9.2	Ω
Turn-On Delay Time	t _{d(on)}			10	20	
Rise Time	t _r	$V_{DD} = -50 \text{ V}, R_{L} = 25 \Omega$		55	95	
Turn-Off DelayTime	t _{d(off)}	$t_{d(off)}$ $I_D \cong -3 \text{ A}, V_{GEN} = -6 \text{ V}, R_g = 1 \Omega$		20	40	
Fall Time	ì,	-		15	30]
Turn-On Delay Time	t _{d(on)}			11	18	ns
Rise Time	ì,	$V_{DD} = -50 \text{ V}, R_{L} = 25 \Omega$		18	32	1
Turn-Off DelayTime	t _{d(off)}	$I_D \cong -3 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		32	58	1
Fall Time	ì,	-		20	35	1
Drain-Source Body Diode Characterist	tics					
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 13	
Pulse Diode Forward Current ^a	I _{SM}				- 15	Α
Body Diode Voltage V _{SD}		I _S = - 3 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}			65	90	ns
Body Diode Reverse Recovery Charge	Q _{rr}	L = 2 A dl/dt = 100 A/vs T = 05 °C		180	270	nC
Reverse Recovery Fall Time	t _a	$I_F = -3 \text{ A, dI/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 ^{\circ}\text{C}$		45		
Reverse Recovery Rise Time	t _b			20		ns

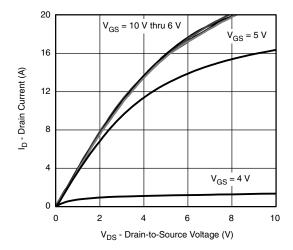
Notes:

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

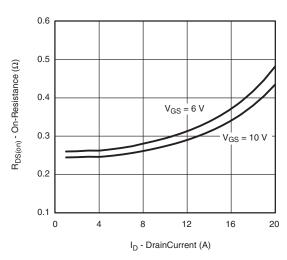
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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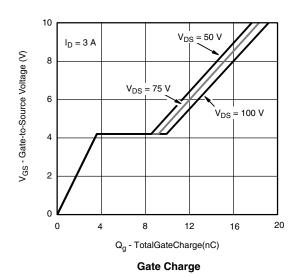




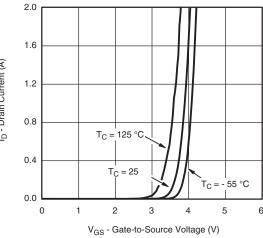
Output Characteristics



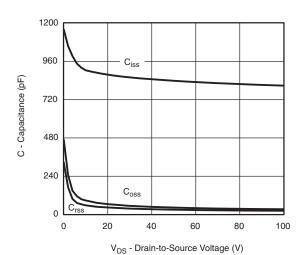
On-Resistance vs. Drain Current and Gate Voltage



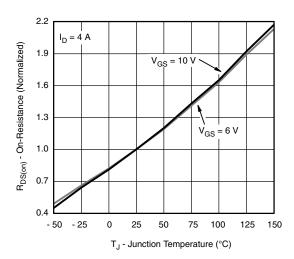
I_D - Drain Current (A)



Transfer Characteristics

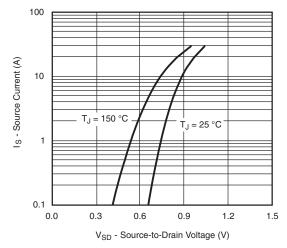


Capacitance

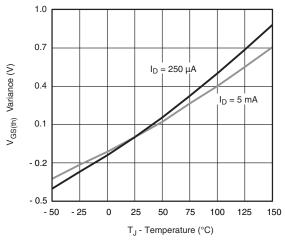


On-Resistance vs. Junction Temperature

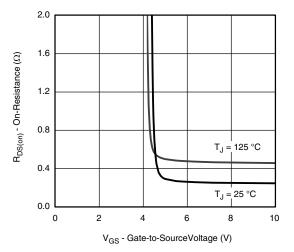




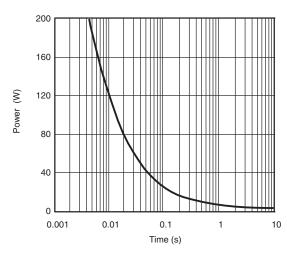
Source-Drain Diode Forward Voltage



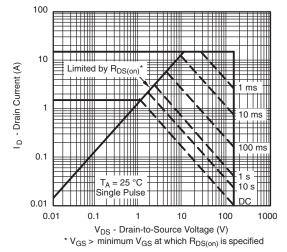
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



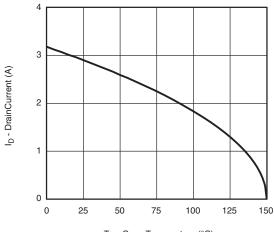
Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

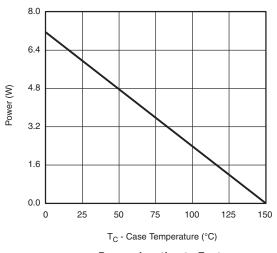
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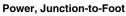


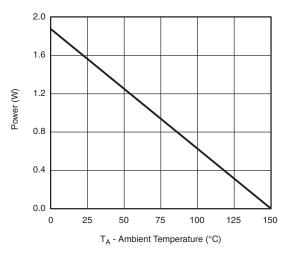


T_C - Case Temperature (°C)

Current Derating*





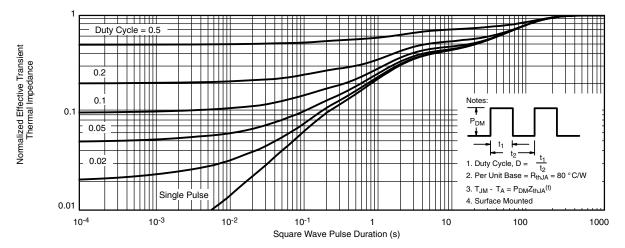


Power, Junction-to-Ambient

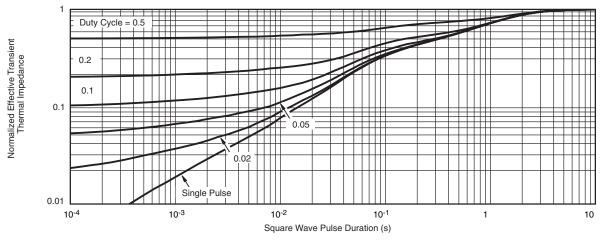
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^{*} The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





Normalized Thermal Transient Impedance, Junction-to-Ambient

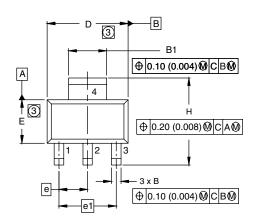


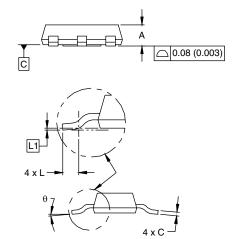
Normalized Thermal Transient Impedance, Junction-to-Foot

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SOT-223 (HIGH VOLTAGE)





DIM.	MILLIMETERS		MIL	INCHES	
	MIN.	MAX.	MIN.	MAX.	
Α	1.55	1.80	0.061	0.071	
В	0.65	0.85	0.026	0.033	
B1	2.95	3.15	0.116	0.124	
С	0.25	0.35	0.010	0.014	
D	6.30	6.70	0.248	0.264	
Е	3.30	3.70	0.130	0.146	
е	2.30 BSC		0.0905 BSC		
e1	4.60 BSC		0.181 BSC		
Н	6.71	7.29	0.264	0.287	
L	0.91	-	0.036	-	
L1	0.061 BSC		0.0024	I BSC	
θ	-	10'	-	10'	

ECN: S-82109-Rev. A, 15-Sep-08

DWG: 5969

Notes

- 1. Dimensioning and tolerancing per ASME Y14.5M-1994.
- 2. Dimensions are shown in millimeters (inches).
- 3. Dimension do not include mold flash.
- 4. Outline conforms to JEDEC outline TO-261AA.

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