## N－Channel 60－V（D－S）MOSFET

| PRODUCT SUMMARY |  |  |
| :---: | :---: | :---: |
| $\mathrm{V}_{\mathrm{DS}}(\mathbf{V})$ | $\mathbf{R}_{\mathrm{DS}(\mathrm{on})}(\Omega)$ | $\mathrm{I}_{\mathbf{D}}(\mathbf{A})$ |
| 60 | 0.029 at $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}$ | 7.0 |
|  | 0.033 at $\mathrm{V}_{\mathrm{GS}}=4.5 \mathrm{~V}$ | 5.6 |

## FEATURES

－Halogen－free According to IEC 61249－2－21 Definition
－TrenchFET ${ }^{\circledR}$ Power MOSFETs
－ $175{ }^{\circ} \mathrm{C}$ Maximum Junction Temperature
－Compliant to RoHS Directive 2002／95／EC


N －Channel MOSFET

## ABSOLUTE MAXIMUM RATINGS $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ ，unless otherwise noted

| Parameter |  | Symbol | 10 s | Steady State | UnitV |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Drain－Source Voltage |  | $V_{\text {DS }}$ | 60 |  |  |
| Gate－Source Voltage |  | $\mathrm{V}_{\mathrm{GS}}$ | $\pm 20$ |  | V |
| Continuous Drain Current（ $\left.\mathrm{T}_{J}=175{ }^{\circ} \mathrm{C}\right)^{\text {a }}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | $I_{\text {D }}$ | 7.0 | 6.0 | A |
|  | $\mathrm{T}_{\mathrm{A}}=70^{\circ} \mathrm{C}$ |  | 6.1 | 5.0 |  |
| Pulsed Drain Current |  | $\mathrm{I}_{\text {DM }}$ | 40 |  |  |
| Avalanche Current |  | $\mathrm{I}_{\text {AS }}$ | 15 |  |  |
| Single Pulse Avalanche Energy |  | $\mathrm{E}_{\text {AS }}$ | 11 |  | mJ |
| Maximum Power Dissipation ${ }^{\text {a }}$ | $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ | $P_{\text {D }}$ | 3.3 | 1.7 | W |
|  | $\mathrm{T}_{\mathrm{A}}=70^{\circ} \mathrm{C}$ |  | 2.3 | 1.2 |  |
| Operating Junction and Storage Temperature Range |  | $\mathrm{T}_{\mathrm{J}}, \mathrm{T}_{\text {stg }}$ | － 55 to 175 |  | ${ }^{\circ} \mathrm{C}$ |

## THERMAL RESISTANCE RATINGS

| Parameter | Symbol | Typical | Maximum | Unit |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Maximum Junction－to－Ambient $^{2}$ | $\mathrm{t} \leq 10 \mathrm{~s}$ | $\mathrm{R}_{\text {thJA }}$ | 36 | 45 |  |
|  |  |  | 75 | 90 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |
| Maximum Junction－to－Foot（Drain） | Steady State |  | 20 |  |  |

## Notes：

a．Surface Mounted on 1 ＂x 1＂FR4 board．

| SPECIFICATIONS $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ ，unless otherwise noted |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Parameter | Symbol | Test Conditions | Min． | Typ． | Max． | Unit |
| Static |  |  |  |  |  |  |
| Drain－Source Breakdown Voltage | $\mathrm{V}_{\text {DS }}$ | $\mathrm{V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=250 \mu \mathrm{~A}$ | 60 |  |  | V |
| Gate Threshold Voltage | $\mathrm{V}_{\mathrm{GS}(\mathrm{th})}$ | $\mathrm{V}_{\mathrm{DS}}=\mathrm{V}_{\mathrm{GS}}, \mathrm{I}_{\mathrm{D}}=250 \mu \mathrm{~A}$ | 1 |  | 3 |  |
| Gate－Body Leakage | IGSS | $\mathrm{V}_{\mathrm{DS}}=0 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}= \pm 20 \mathrm{~V}$ |  |  | $\pm 100$ | nA |
| Zero Gate Voltage Drain Current | IDSS | $\mathrm{V}_{\mathrm{DS}}=60 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  |  | 1 | $\mu \mathrm{A}$ |
|  |  | $\mathrm{V}_{\mathrm{DS}}=60 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}, \mathrm{~T}_{\mathrm{J}}=55^{\circ} \mathrm{C}$ |  |  | 20 |  |
| On－State Drain Current ${ }^{\text {a }}$ | $\mathrm{I}_{\mathrm{D} \text {（on）}}$ | $\mathrm{V}_{\mathrm{DS}} \geq 5 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}$ | 40 |  |  | A |
| Drain－Source On－State Resistance ${ }^{\text {a }}$ | $\mathrm{R}_{\mathrm{DS} \text {（on）}}$ | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=6.0 \mathrm{~A}$ |  | 0.028 |  | $\Omega$ |
|  |  | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=6.0 \mathrm{~A}, \mathrm{~T}_{J}=125^{\circ} \mathrm{C}$ |  | 0.032 |  |  |
|  |  | $\mathrm{V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=6.0 \mathrm{~A}, \mathrm{~T}_{\mathrm{J}}=175^{\circ} \mathrm{C}$ |  | 0.040 |  |  |
|  |  | $\mathrm{V}_{\mathrm{GS}}=4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=5.1 \mathrm{~A}$ |  | 0.033 |  |  |
| Forward Transconductance ${ }^{\text {a }}$ | $\mathrm{g}_{\text {fs }}$ | $\mathrm{V}_{\mathrm{DS}}=15 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=6.0 \mathrm{~A}$ |  | 25 |  | S |
| Diode Forward Voltage ${ }^{\text {a }}$ | $\mathrm{V}_{\text {SD }}$ | $\mathrm{I}_{\mathrm{S}}=1.7 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ |  | 0.8 | 1.2 | V |
| Dynamic ${ }^{\text {b }}$ |  |  |  |  |  |  |
| Total Gate Charge | $\mathrm{Q}_{\mathrm{g}}$ | $\mathrm{V}_{\mathrm{DS}}=30 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=6.0 \mathrm{~A}$ |  | 18 | 27 | $n \mathrm{C}$ |
| Gate－Source Charge | $\mathrm{Q}_{\mathrm{gs}}$ |  |  | 3.4 |  |  |
| Gate－Drain Charge | $\mathrm{Q}_{\mathrm{gd}}$ |  |  | 5.3 |  |  |
| Gate Resistance | $\mathrm{R}_{\mathrm{g}}$ | $\mathrm{V}_{\mathrm{GS}}=0.1 \mathrm{~V}, \mathrm{f}=5 \mathrm{MHz}$ | 0.5 | 1.4 | 2.4 | $\Omega$ |
| Turn－On Delay Time | $\mathrm{t}_{\mathrm{d}(\mathrm{on})}$ | $\begin{gathered} \mathrm{V}_{\mathrm{DD}}=30 \mathrm{~V}, \mathrm{R}_{\mathrm{L}}=30 \Omega \\ \mathrm{I}_{\mathrm{D}} \cong 1 \mathrm{~A}, \mathrm{~V}_{\mathrm{GEN}}=10 \mathrm{~V}, \mathrm{R}_{\mathrm{g}}=6 \Omega \end{gathered}$ |  | 10 | 20 | ns |
| Rise Time | $\mathrm{t}_{\mathrm{r}}$ |  |  | 10 | 20 |  |
| Turn－Off Delay Time | $\mathrm{t}_{\mathrm{d} \text {（off）}}$ |  |  | 25 | 50 |  |
| Fall Time | $\mathrm{t}_{\mathrm{f}}$ |  |  | 12 | 24 |  |
| Source－Drain Reverse Recovery Time | $\mathrm{trr}^{\text {r }}$ | $\mathrm{I}_{\mathrm{F}}=1.7 \mathrm{~A}, \mathrm{~d} / \mathrm{dtt}=100 \mathrm{~A} / \mu \mathrm{s}$ |  | 50 | 80 |  |

## Notes：

a．Pulse test；pulse width $\leq 300 \mu \mathrm{~s}$ ，duty cycle $\leq 2 \%$ ．
b．Guaranteed by design，not subject to production testing．

TYPICAL CHARACTERISTICS $25^{\circ} \mathrm{C}$ ，unless otherwise noted


TYPICAL CHARACTERISTICS $25^{\circ} \mathrm{C}$ ，unless otherwise noted


On－Resistance vs．Drain Current



Source－Drain Diode Forward Voltage

$\mathrm{V}_{\mathrm{DS}}$－Drain－to－Source Voltage（V）
Capacitance


On－Resistance vs．Junction Temperature


On－Resistance vs．Gate－to－Source Voltage

TYPICAL CHARACTERISTICS $25^{\circ} \mathrm{C}$ ，unless otherwise noted


## SOT－223（HIGH VOLTAGE）



|  | MILLIMETERS |  | INCHES |  |
| :---: | :---: | :---: | :---: | :---: |
| DIM． | MIN． | MAX． | MIN． | MAX． |
| A | 1.55 | 1.80 | 0.061 | 0.071 |
| B | 0.65 | 0.85 | 0.026 | 0.033 |
| B1 | 2.95 | 3.15 | 0.116 | 0.124 |
| C | 0.25 | 0.35 | 0.010 | 0.014 |
| D | 6.30 | 6.70 | 0.248 | 0.264 |
| E | 3.30 | 3.70 | 0.130 | 0.146 |
| e | 2.30 BSC |  | 0.0905 BSC |  |
| e1 | 4.60 BSC |  | 0.181 BSC |  |
| H | 6.71 | 7.29 | 0.264 | 0.287 |
| L | 0.91 | － | 0.036 | － |
| L1 | 0.061 BSC |  | 0.0024 BSC |  |
| $\theta$ | － | 10＇ | － | 10＇ |
| $\begin{aligned} & \hline 82109 . \\ & 969 \end{aligned}$ |  |  |  |  |

Notes
1．Dimensioning and tolerancing per ASME Y14．5M－1994．
2．Dimensions are shown in millimeters（inches）．
3．Dimension do not include mold flash．
4．Outline conforms to JEDEC outline TO－261AA．

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