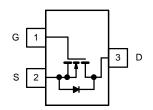


N-Channel 100 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)	
	0.240 at V _{GS} = 10 V	2.0		
100	0.250 at V _{GS} = 6 V	1.8	2.9 nC	
	0.260 at V _{GS} = 4.5 V	1.7		



FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g Tested
 100 % UIS Tested
- · Material categorization:



APPLICATIONS

- DC/DC Converters
- Load Switch
- · LED Backlighting in LCD TVs

ABSOLUTE MAXIMUM RATINGS (T	$_{A}$ = 25 °C, unless oth	nerwise noted)			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V_{DS}	100	V	
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current (T _{.1} = 150 °C)	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$	I-	2 1.8	A	
Continuous Brain Current (1) = 100 O)	$T_A = 25 \degree C$ $T_A = 70 \degree C$	I _D	1.6 ^{b, c} 1.3 ^{b, c}		
Pulsed Drain Current (t = 300 μs)		I _{DM}	7	A	
Continuous Source-Drain Diode Current	$T_C = 25 \degree C$ $T_A = 25 \degree C$	I _S	2.1 1.0 ^{b, c}]	
Single Pulse Avalanche Current	1 0.1 ml l	I _{AS}	5		
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	1.25	mJ	
Maximum Power Dissipation	$T_{C} = 25 ^{\circ}\text{C}$ $T_{C} = 70 ^{\circ}\text{C}$ $T_{A} = 25 ^{\circ}\text{C}$ $T_{A} = 70 ^{\circ}\text{C}$	P _D	2.5 1.6 1.25 ^{b, c} 0.8 ^{b, c}	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R_{thJA}	75	100	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	40	50	C/VV	

Notes:

- a. Based on T_C = 25 °C.
 b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 166 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{DS} = 0 \text{ V, } I_{D} = 250 \mu\text{A}$	100			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	S/T _J		105		\//00
V _{GS(th)} Temperature Coefficient				- 5.2		mV/°C
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250 \mu A$	1.2		2.8	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zana Oata Valtana Busin Oussant	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			- 1	μА
Zero Gate Voltage Drain Current		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	5			Α
	` ,	V _{GS} = 10 V, I _D = 1.5 A		0.240		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 6 V, I _D = 1 A		0.250		Ω
		V _{GS} = 4.5 V, I _D = 0.5 A		0.260		1
Forward Transconductance ^a	9 _{fs}	V _{DS} = 20 V, I _D = 1.5 A		2.0		S
Dynamic ^b						
Input Capacitance	C _{iss}			190		
Output Capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		22		pF
Reverse Transfer Capacitance	C _{rss}	23 7 63 - 7		13		-
Treverse Harrison Capacitance		V _{DS} = 50 V, V _{GS} = 10 V, I _D = 1.6 A		5.2	10.4	
Total Gate Charge	Q_g	103 11 1, 103 11 1, 10 11 11		2.9	5.8	nC
Gate-Source Charge	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 1.6 \text{ A}$		0.75		
Gate-Drain Charge	Q _{gd}	20 / 60 / 5		1.4		
Gate Resistance	R _g	f = 1 MHz	0.3	1.4	2.8	Ω
Turn-On Delay Time	t _{d(on)}			30	45	
Rise Time	t _r	$V_{DD} = 50 \text{ V}, R_1 = 39 \Omega$		26	39	
Turn-Off Delay Time	t _{d(off)}	$I_D = 1.3 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		17	26	
Fall Time	t _f			12	20	1
Turn-On Delay Time	t _{d(on)}			6	12	ns
Rise Time	t _r	$V_{DD} = 50 \text{ V, R}_{1} = 39 \Omega$		10	20	
Turn-Off Delay Time	t _{d(off)}	$I_D = 1.3 \text{ A}, V_{GEN} = 10 \text{ V}, R_q = 1 \Omega$		10	20	
Fall Time	t _f	·· 9		6	12	1
Drain-Source Body Diode Characteristi						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 2.1	
Pulse Diode Forward Current ^a		-			- 20	A
Body Diode Voltage	V _{SD}	I _S = 1.3 A		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time	t _{rr}	<u> </u>		22	33	ns
Body Diode Reverse Recovery Charge	Q _{rr}			21	32	nC
Reverse Recovery Fall Time	t _a	$I_F = 1.3 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		16		
Reverse Recovery Rise Time	t _b			6		ns

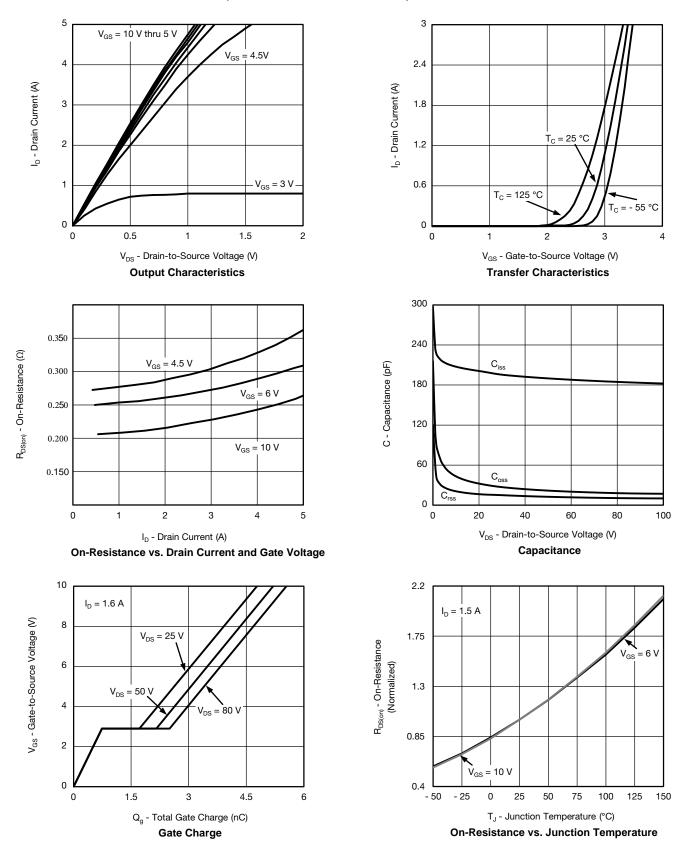
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

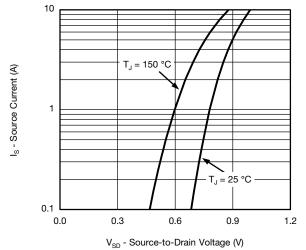


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

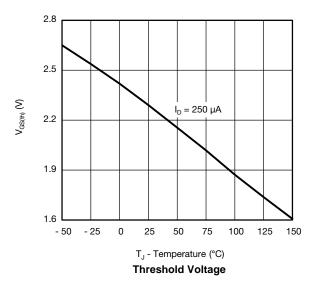


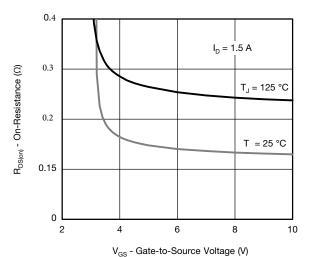


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

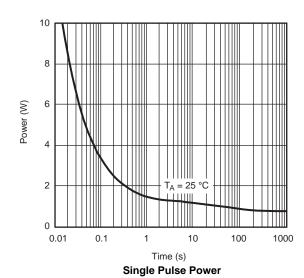


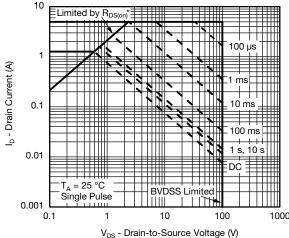
Source-Drain Diode Forward Voltage





On-Resistance vs. Gate-to-Source Voltage



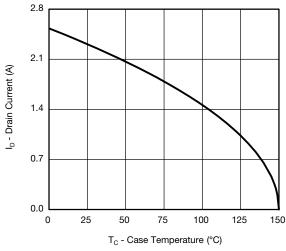


* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

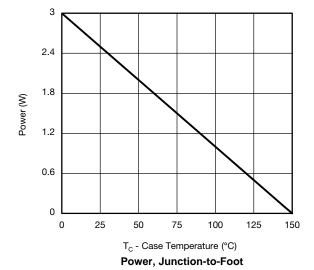
Safe Operating Area

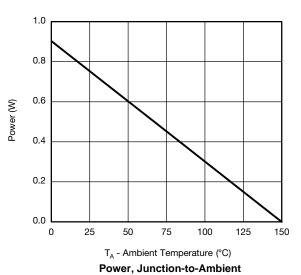


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*

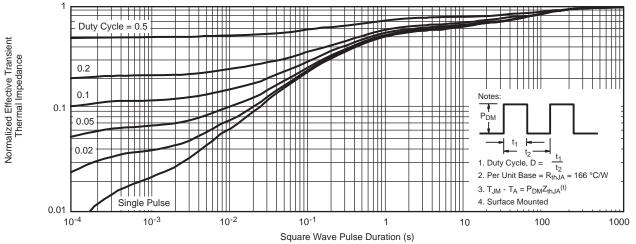




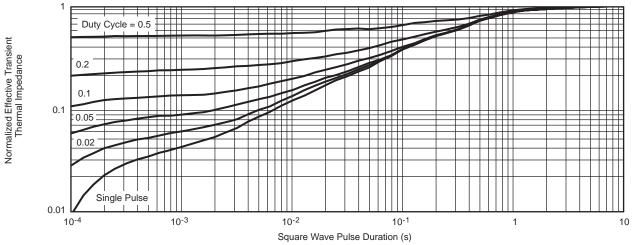
^{*} The power dissipation P_D is based on $T_{J(max)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

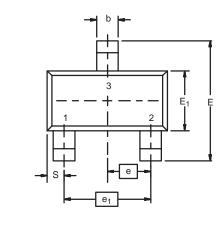
Note

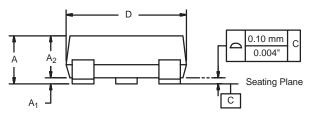
- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



SOT-23 (TO-236): 3-LEAD





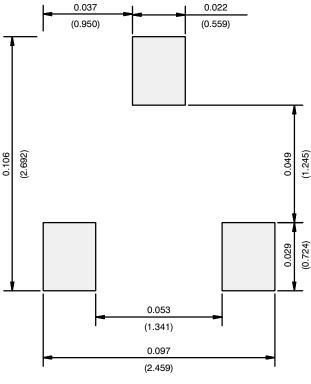


Dim	MILLIMETERS		INCHES			
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e ₁	1.90	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L ₁	0.64	0.64 Ref		S Ref		
S	0.50 Ref		0.020 Ref			
q	3°	8°	3°	8°		

DWG: 5479



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)



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