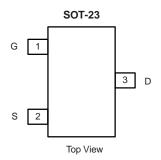


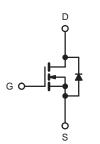
COMPLIANT

HALOGEN **FREE**

N-Channel 60-V (D-S) MOSFET

PRODUCT S	PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (mA)			
60	2.8 at V _{GS} = 10 V	250			





N-Channel MOSFET

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Low Threshold: 2 V (typ.)
- Low Input Capacitance: 25 pF
- Fast Switching Speed: 25 ns
- Low Input and Output Leakage
- TrenchFET® Power MOSFET
- 1200V ESD Protection
- Compliant to RoHS Directive 2002/95/EC



- · Low Offset Voltage
- Low-Voltage Operation
- Easily Driven Without Buffer
- **High-Speed Circuits**
- Low Error Voltage

APPLICATIONS

- Direct Logic-Level Interface: TTL/CMOS
- Drivers: Relays, Solenoids, Lamps, Hammers, Display, Memories, Transistors, etc.
- **Battery Operated Systems**
- Solid-State Relays

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted					
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	60		
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Drain Current (T _{.I} = 150 °C) ^b	T _A = 25 °C	I _D	250	mA	
Continuous Diain Current (1) = 150 °C)	T _A = 100 °C		150		
Pulsed Drain Current ^a		I _{DM}	800		
Devices Displie extremb	T _A = 25 °C	- P _D	0.30	W	
Power Dissipation ^b	T _A = 100 °C		0.13		
Maximum Junction-to-Ambient ^b		R _{thJA}	350	°C/W	
Operating Junction and Storage Temperature Range		T _{J,} T _{stg}	- 55 to 150	°C	

Notes:

- a. Pulse width limited by maximum junction temperature.b. Surface Mounted on FR4 board.

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply.



			Limits				
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static						•	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 10 \mu\text{A}$	60			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		2.5	V	
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 10		
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 15 \text{ V}$			1	μA	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}$			± 150	nA	
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 10 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			± 1000		
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 5 \text{ V}$			± 100		
Zone Onto Valta na Busin Oursent	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}$			1			
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 60 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$			500	μA	
	I _{D(on)}	V _{GS} = 10 V, V _{DS} = 7.5 V	500				
On-State Drain Current ^a		$V_{GS} = 4.5 \text{ V}, V_{DS} = 10 \text{ V}$	300			- mA	
	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 200 \text{ mA}$		2.8 3.3			
Drain-Source On-Resistance ^a		$V_{GS} = 4.5 \text{ V}, I_D = 150 \text{ mA}$		3.1	3.8	Ω	
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 100 \text{ mA}$	100			mS	
Diode Forward Voltage	V _{SD}	$I_S = 100 \text{ mA}, V_{GS} = 0 \text{ V}$			1.3	V	
Dynamic ^a			II.			•	
Total Gate Charge	Qg	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}$ $I_{D} \cong 150 \text{ mA}$		0.4	0.6	1	
Input Capacitance	C _{iss}			25			
Output Capacitance	C _{oss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}$		5		pF	
Reverse Transfer Capacitance	C _{rss}			2.0			
Switching ^{a, b, c}	•						
Turn-On Time	t _{d(on)}	$V_{DD} = 30 \text{ V}, R_{L} = 150 \Omega$			20		
Turn-Off Time	t _{d(off)}	$I_D \cong 200 \text{ mA}, V_{GEN} = 10 \text{ V}, R_G = 10 \Omega$			30	ns	

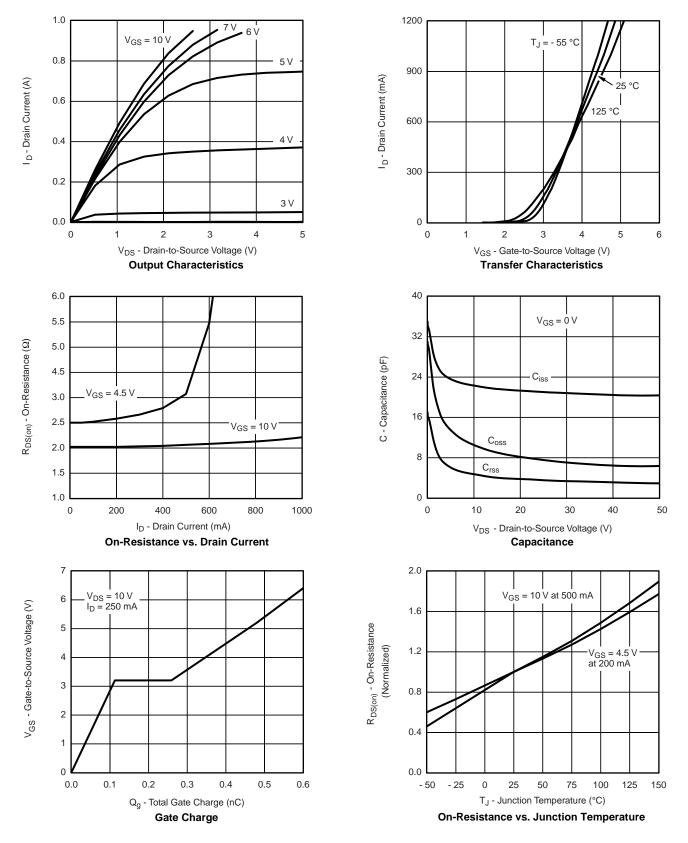
Notes:

- a. For DESIGN AID ONLY, not subject to production testing.
- b. Pulse test: PW \leq 300 μ s duty cycle \leq 2 %.
- c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

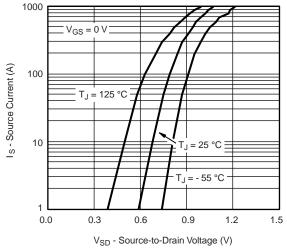


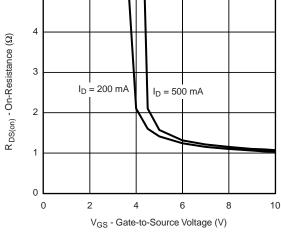
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



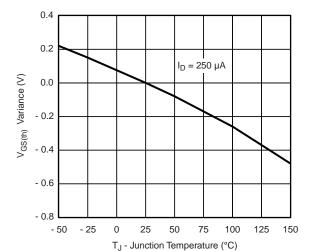


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

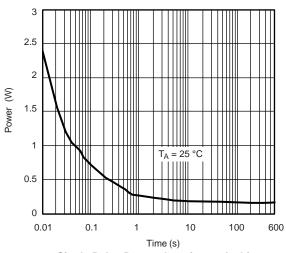




Source-Drain Diode Forward Voltage

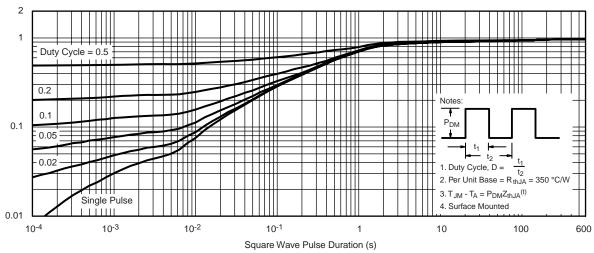


On-Resistance vs. Gate-Source Voltage



Threshold Voltage Variance Over Temperature



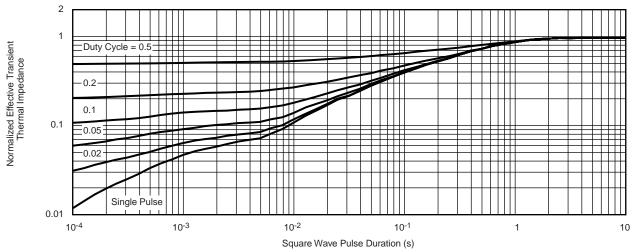


Normalized Thermal Transient Impedance, Junction-to-Ambient

Normalized Effective Transient Thermal Impedance



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



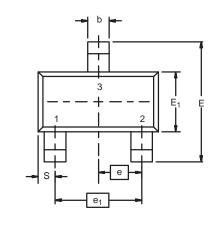
Normalized Thermal Transient Impedance, Junction-to-Foot

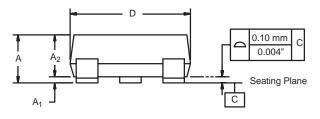
Note

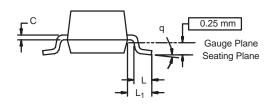
- · The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C) are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	

DWG: 5479



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)



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