

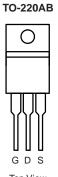
N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	100			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0. 036			
I _D (A) ^a	55			
Configuration	Single			

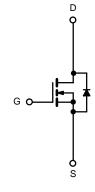
FEATURES

- TrenchFET[®] Power MOSFETS
- 175 °C Junction Temperature
- Low Thermal Resistance Package





Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T _C = 25 °C, unless oth	erwise noted		
Parameter		Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage		V _{GS}	± 20	
Continuous Drain Current ($T_1 = 175 ^{\circ}C$)	T _C = 25 °C	1-	55	
Continuous Drain Current $(1j = 175 C)$	T _C = 125 °C	I _D	40	
Pulsed Drain Current	·	I _{DM}	135	- A
Avalanche Current		I _{AR}	35	
Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AR}	61	mJ
	T _C = 25 °C	P	127 ^b	14/
Maximum Power Dissipation ^a	T _A = 25 °C ^c	– P _D –	3.75	W
Operating Junction and Storage Temperature Ra	inge	T _J , T _{stg}	- 55 to 175	°C

THERMAL RESISTANCE RATINGS				
Parameter		Symbol	Limit	Unit
Junction-to-Ambient	(PCB Mount) ^c	R _{thJA}	40	°C/W
Junction-to-Case (Drain)		R _{thJC}	1.4	0/11

Notes:

a. Duty cycle \leq 1 %.

b. See SOA curve for voltage derating.

c. When Mounted on 1" square PCB (FR-4 material).

* Pb containing terminations are not RoHS compliant, exemptions may apply.

SPECIFICATIONS $T_J = 25^{\circ}$	C, unless of	therwise noted				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	•		•			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{SS} = 0 V, I_D = 250 \mu A$	100			V
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1		3	v
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA
		$V_{DS} = 100 \ W_{GS} = 0 \ V$			1	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 \text{ °C}$			50	μA
		$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 175 \text{ °C}$			250	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	75			А
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.036		
	_	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		0.038		Ω
Drain-Source On-State Resistance ^a	r _{DS(on)}	V_{GS} = 10 V, I _D = 5 A, T _J = 125 °C		0.050		
		V _{GS} = 10 V, I _D = 3 A, T _J = 175 °C		0.065		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	10			S
Dynamic ^b	4					
Input Capacitance	C _{iss}			4500		
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1 MHz		270		pF
Reverse Transfer Capacitance	C _{rss}			90		
Total Gate Charge ^c	Qg			35	60	nC
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 40 \text{ A}$		11		
Gate-Drain Charge ^c	Q _{gd}			9		
Gate Resistance	R _G			1.7		Ω
Turn-On Delay Time ^c	t _{d(on)}			11	20	
Rise Time ^c	t _r	V_{DD} = 50 V, R _L = 1.25 Ω		12	20	ns
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ 40 A, V_{GEN} = 10 V, R_G = 2.5 Ω		30	45	
Fall Time ^c	t _f			12	20	
Source-Drain Diode Ratings and Cha	aracteristics 7	_C = 25 °C ^b				
Continuous Current	ا _S	-			40	^
Pulsed Current	I _{SM}				120	A
Forward Voltage ^a	V _{SD}	$I_{F} = 30 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		1.0	1.5	V
Reverse Recovery Time	t _{rr}			60	100	ns
Peak Reverse Recovery Current	I _{RM(REC)}	I _F = 30 A, di/dt = 100 A/µs		5	8	А
Reverse Recovery Charge	Q _{rr}			0.15	0.4	μC

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

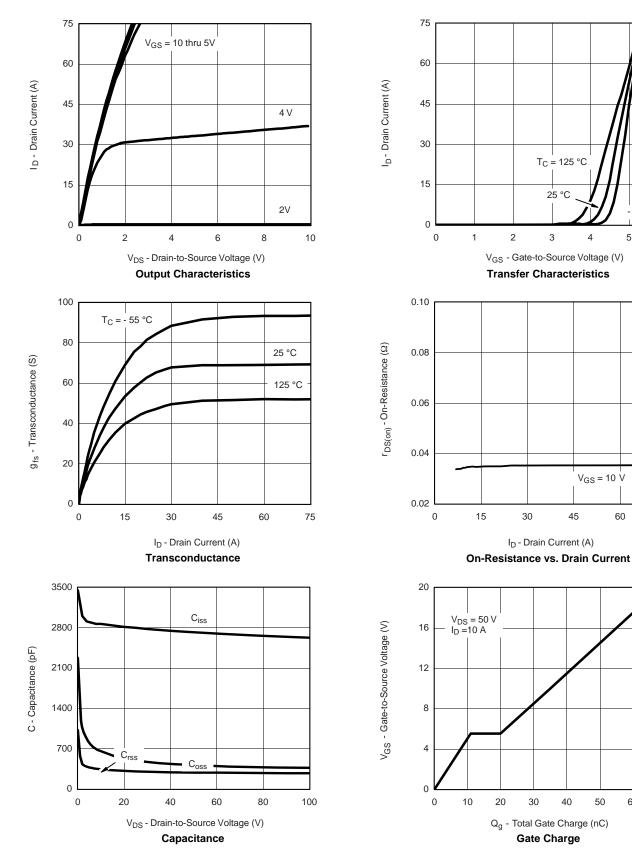
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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55 °C

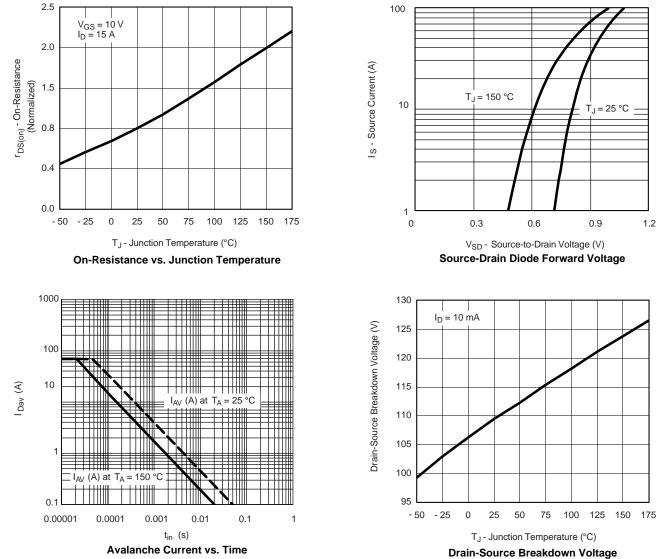


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



1.2

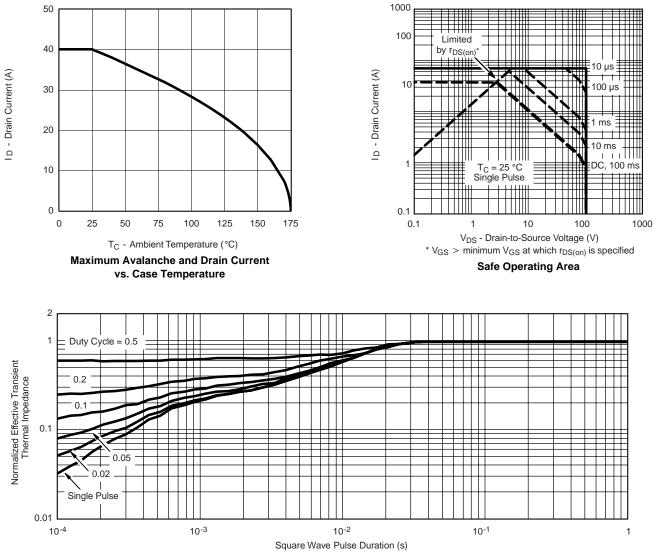
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



vs. Junction Temperature



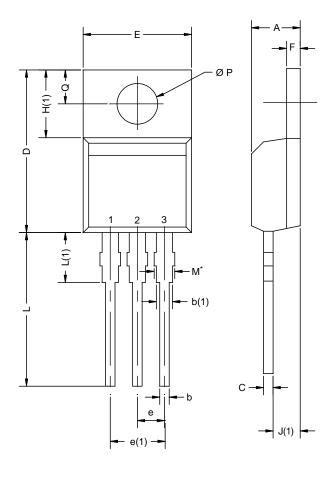
THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Case



TO-220AB



IIN. .25 .69 .20 .36 .36 4.85 .0.04 .41 .88	MAX. 4.65 1.01 1.73 0.61 15.49 10.51 2.67 5.28	MIN. 0.167 0.027 0.047 0.014 0.585 0.395 0.095	MAX. 0.183 0.040 0.068 0.024 0.610 0.414 0.105
.69 .20 .36 4.85 0.04 .41	1.01 1.73 0.61 15.49 10.51 2.67	0.027 0.047 0.014 0.585 0.395 0.095	0.040 0.068 0.024 0.610 0.414
.20 .36 4.85 0.04 .41	1.73 0.61 15.49 10.51 2.67	0.047 0.014 0.585 0.395 0.095	0.068 0.024 0.610 0.414
.36 4.85 0.04 .41	0.61 15.49 10.51 2.67	0.014 0.585 0.395 0.095	0.024 0.610 0.414
4.85 0.04 .41	15.49 10.51 2.67	0.585 0.395 0.095	0.610 0.414
0.04 .41	10.51 2.67	0.395 0.095	0.414
.41	2.67	0.095	-
	-		0.105
.88	5 28		
	0.20	0.192	0.208
.14	1.40	0.045	0.055
.09	6.48	0.240	0.255
.41	2.92	0.095	0.115
3.35	14.02	0.526	0.552
.32	3.82	0.131	0.150
.54	3.94	0.139	0.155
.60	3.00	0.102	0.118
	3.35 .32 .54 .60	3.35 14.02 .32 3.82 .54 3.94 .60 3.00	3.35 14.02 0.526 .32 3.82 0.131 .54 3.94 0.139

Notes

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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