

# N- and P- Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY							
	V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)				
		0.090 at V <sub>GS</sub> = 4.5 V	3.28				
N-Channel	20	0.110 at V <sub>GS</sub> = 2.5 V	2.13				
		0.130 at V <sub>GS</sub> = 1.8 V	1.50				
		0.155 at V <sub>GS</sub> = - 4.5 V	- 2.80				
P-Channel	- 20	0.190 at V <sub>GS</sub> = - 2.5 V	- 1.81				
		0.220 at V <sub>GS</sub> = - 1.8 V	- 1.15				

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET<sup>®</sup> Power MOSFETs: 1.8 V Rated

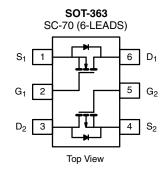
Compliant to RoHS Directive 2002/95/EC

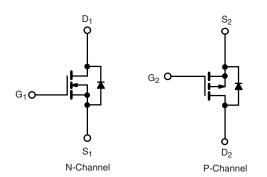
- Thermally Enhanced SC-70 Package
- Fast Switching



#### **APPLICATIONS**

• Load Switch for Portable Devices





<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted								
			N-C	Channel	P-Channel			
Parameter		Symbol	5 s	Steady State	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	20		- 20		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20		± 20		v	
Continuous Drain Current /T 150 °C\2	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	3.28	3.03	- 2.80	- 2.58		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 85 °C		2.12	1.81	- 1.72	- 1.53		
Pulsed Drain Current		I <sub>DM</sub>	9.5		- 8.5		Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	2.61	2.48	- 1.61	-1.48		
Mariana Barra Birata di ad	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	1.24	1.17	1.10	0.97	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 85 °C		0.88	0.75	0.66	0.5		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>sta</sub>	- 55 to 150				°C	

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	- R <sub>thJA</sub>	130	170	i		
	Steady State		170	220	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	80	100			

Notes:

a. Surface mounted on 1" x 1" FR4 board.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static	_							
Cata Threehold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 100 \mu A$	N-Ch	0.45		1	V	
Gate Threshold Voltage		$V_{DS} = V_{GS}, I_{D} = -100 \mu A$	P-Ch	- 0.45		1	V	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V	N-Ch			± 100	nA	
			P-Ch			± 100		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 16 V, V <sub>GS</sub> = 0 V	N-Ch			1		
		V <sub>DS</sub> = - 16 V, V <sub>GS</sub> = 0 V	P-Ch			- 1	μΑ	
		$V_{DS}$ = 16 V, $V_{GS}$ = 0 V, $T_{J}$ = 85 °C	N-Ch			5		
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$	= 85 °C P-Ch			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	N-Ch	2			А	
		$V_{DS} \le$ - 5 V, $V_{GS} =$ - 4.5 V	P-Ch - 2					
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 2.55 \text{ A}$	N-Ch	N-Ch 0.090				
		$V_{GS} = -4.5 \text{ V}, I_D = -1.85 \text{ A}$	P-Ch		0.155		Ω	
		$V_{GS} = 2.5 \text{ V}, I_D = 1.55 \text{ A}$	N-Ch		0.110			
		$V_{GS} = -2.5 \text{ V}, I_D = -1.35 \text{ A}$	P-Ch		0.190			
		$V_{GS} = 1.8 \text{ V}, I_D = 0.50 \text{ A}$	N-Ch		0.130			
		$V_{GS} = -1.8 \text{ V}, I_D = -0.50 \text{ A}$	P-Ch		0.220			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = 10 \text{ V}, I_{D} = 1.13 \text{ A}$	N-Ch		2.6		s	
		$V_{DS} = -10 \text{ V}, I_{D} = -0.88 \text{ A}$	P-Ch		1.5		3	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 0.48 A, V <sub>GS</sub> = 0 V	N-Ch	n 0.8 1.		1.2	V	
		$I_S = -0.48 \text{ A}, V_{GS} = 0 \text{ V}$	P-Ch		- 0.8	- 1.2		
Dynamic <sup>b</sup>	_							
Total Gate Charge	$Q_g$	N. Channel	N-Ch		1.25	2		
Total Gate Charge		N-Channel $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 2.55 \text{ A}$	P-Ch		1.2	1.8		
Gate-Source Charge	$Q_{gs}$		N-Ch		0.21		nC	
- Calle Course Charge		P-Channel	P-Ch		0.3		-	
Gate-Drain Charge	Q <sub>gd</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -0.88$	N-Ch		0.3			
date Brain Gridige			P-Ch		0.21			
Turn-On Delay Time	t <sub>d(on)</sub>		N-Ch		15	25		
		N-Channel	P-Ch		18	30		
Rise Time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, R_L = 20 \Omega$	N-Ch		22	35		
		$I_D \cong 0.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 6 \Omega$	P-Ch		25	40		
Turn-Off Delay Time	t <sub>d(off)</sub>	P-Channel	N-Ch		25	40	ns	
		$V_{DD} = -10 \text{ V}, R_{L} = 20 \Omega$	P-Ch		15	25	_	
Fall Time	t <sub>f</sub>	$I_D \cong -0.5 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 6 \Omega$	N-Ch		12	20		
ran inne			P-Ch		12	20		
Daviera Dassiera Tirri	t <sub>rr</sub>	1 0 40 4 41/44 400 4/4	N-Ch		30	60	1	
Reverse Recovery Time		I <sub>F</sub> = 0.48 A, dl/dt = 100 A/μs	P-Ch		30	60		

#### Notes:

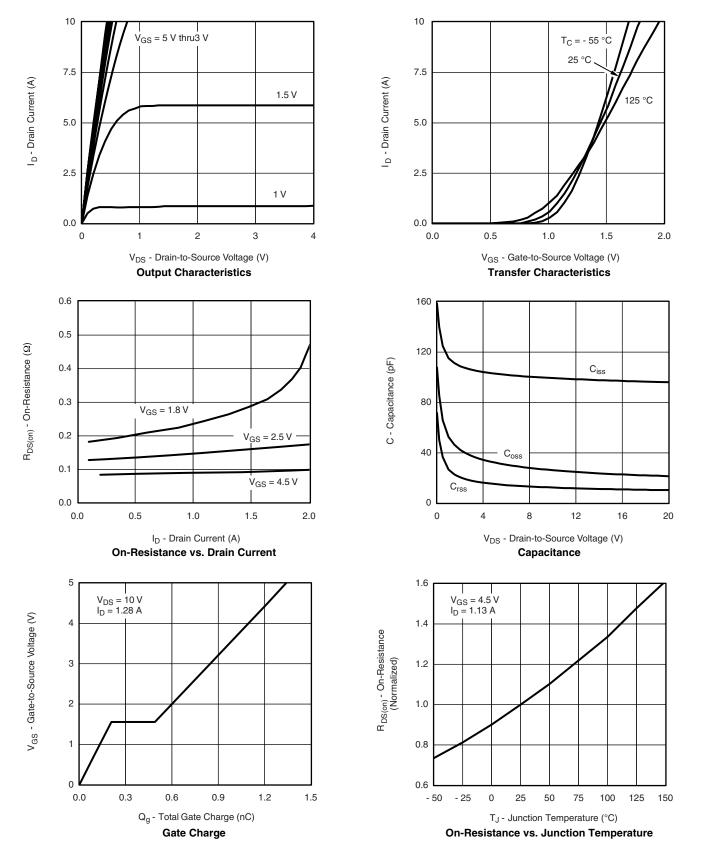
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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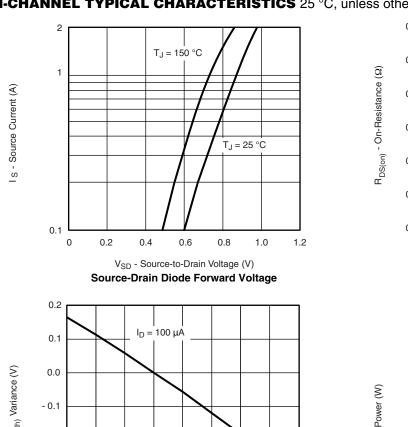


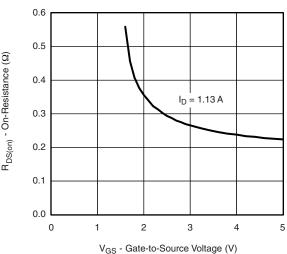
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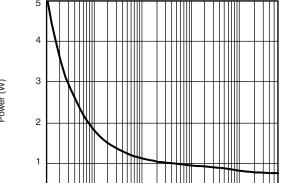




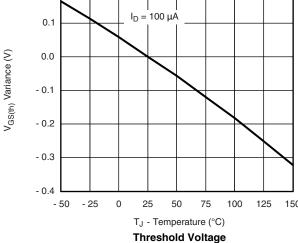
#### N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







On-Resistance vs. Gate-to-Source Voltage

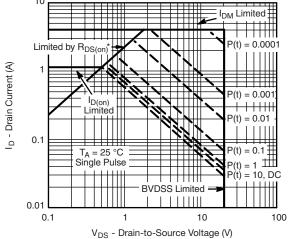


Time (s) Single Pulse Power, Junction-to-Ambient

10

100

600



0

0.01

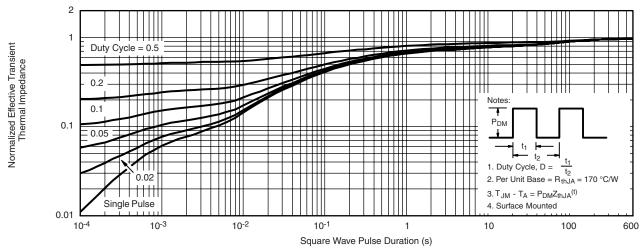
0.1

\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

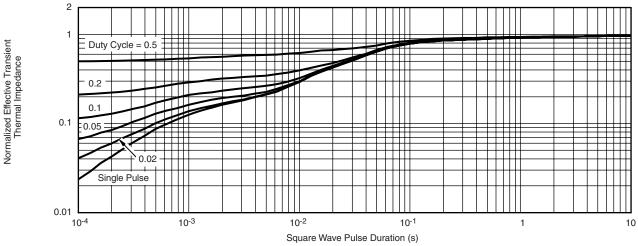
Safe Operating Area, Junction-to-Ambient



## N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



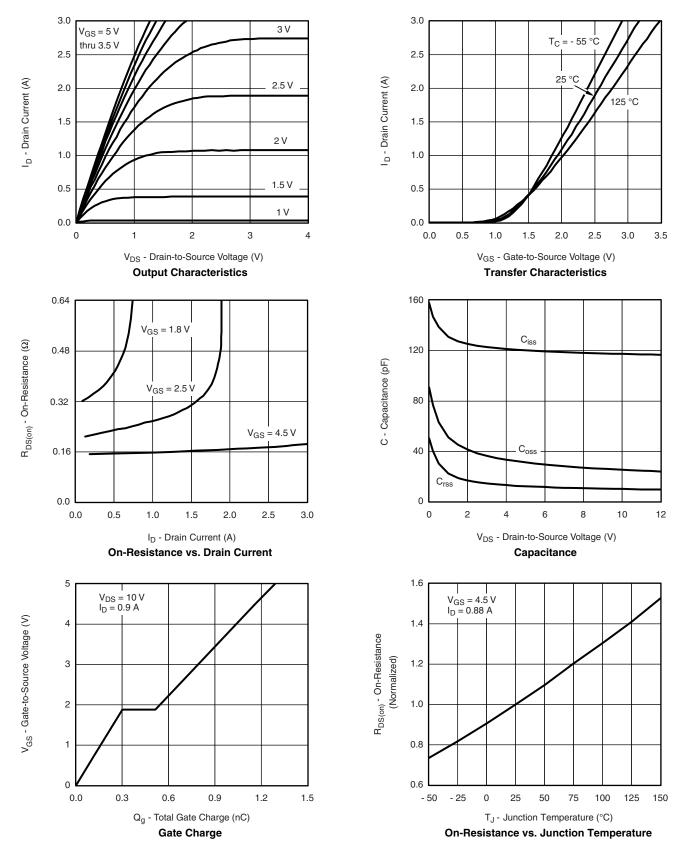
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

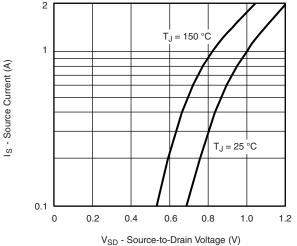


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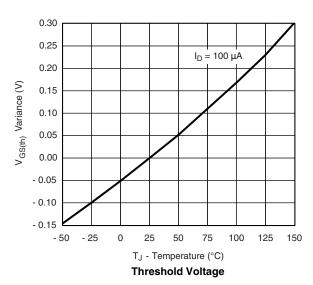




#### P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

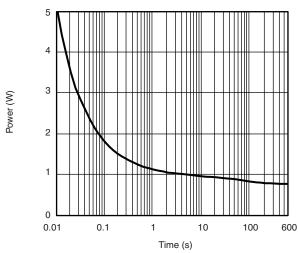


Source-Drain Diode Forward Voltage

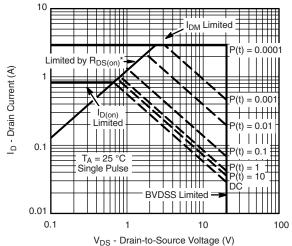


V<sub>GS</sub> - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

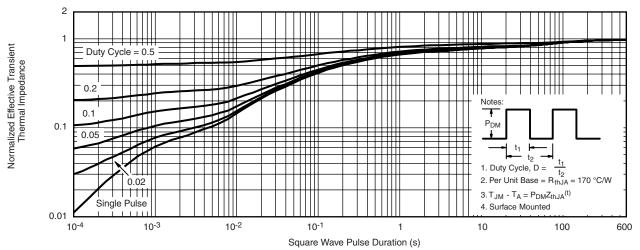


\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

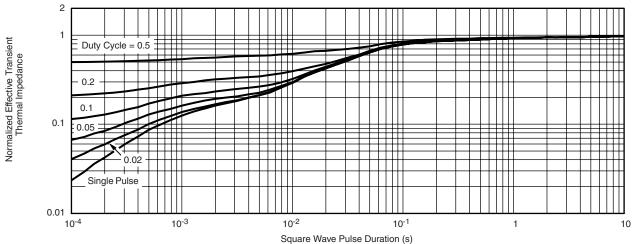
Safe Operating Area, Junction-to-Ambient



## P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



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