

## P-Channel 60 V (D-S) 175 °C MOSFET

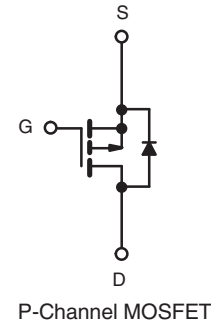
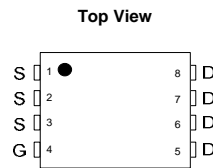
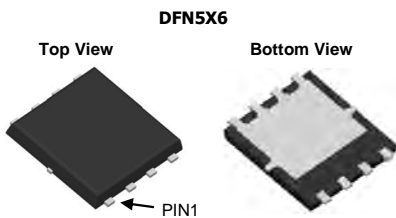
PRODUCT SUMMARY	
V <sub>DS</sub> (V)	-60
R <sub>DS(on)</sub> (Ω) at V <sub>GS</sub> = -10 V	0.0210
R <sub>DS(on)</sub> (Ω) at V <sub>GS</sub> = -4.5 V	0.0288
I <sub>D</sub> (A)	-36
Configuration	Single
Package	DFN 5X6

### FEATURES

- TrenchFET® power MOSFET
- 100 % R<sub>g</sub> and UIS tested



**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**



ABSOLUTE MAXIMUM RATINGS (T <sub>C</sub> = 25 °C, unless otherwise noted)				
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V <sub>DS</sub>	-60	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	
Continuous Drain Current	T <sub>C</sub> = 25 °C	I <sub>D</sub>	-36	A
	T <sub>C</sub> = 125 °C		-21	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	-60	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	-100	
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	-36	
Single Pulse Avalanche Energy		E <sub>AS</sub>	64.8	
Maximum Power Dissipation <sup>b</sup>	T <sub>C</sub> = 25 °C	P <sub>D</sub>	68	W
	T <sub>C</sub> = 125 °C		22	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C
Soldering Recommendations (Peak Temperature) <sup>d, e</sup>			260	

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-Ambient	PCB Mount <sup>c</sup>	R <sub>thJA</sub>	68	°C/W
Junction-to-Case (Drain)		R <sub>thJC</sub>	2.2	

### Notes

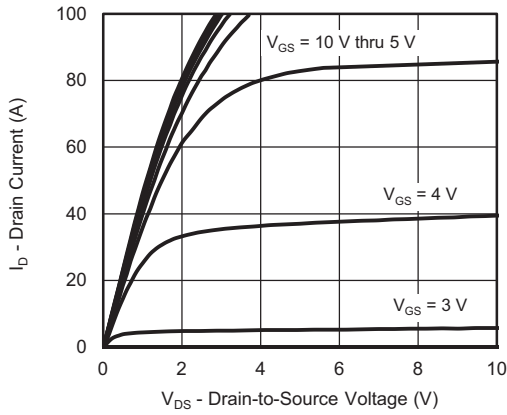
- Package limited.
- Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %.
- When mounted on 1" square PCB (FR4 material).

<b>SPECIFICATIONS</b> ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
<b>Static</b>							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0, I_D = -250\text{ }\mu\text{A}$		-60	-	-	V
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\text{ }\mu\text{A}$		-1.5	-2.0	-2.5	
Gate-Source Leakage	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 20\text{ V}$		-	-	$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{GS} = 0\text{ V}$	$V_{DS} = -60\text{ V}$	-	-	-1	$\mu\text{A}$
		$V_{GS} = 0\text{ V}$	$V_{DS} = -60\text{ V}, T_J = 125\text{ }^\circ\text{C}$	-	-	-50	
		$V_{GS} = 0\text{ V}$	$V_{DS} = -60\text{ V}, T_J = 175\text{ }^\circ\text{C}$	-	-	-150	
On-State Drain Current <sup>a</sup>	$I_{D(on)}$	$V_{GS} = -10\text{ V}$	$V_{DS} \geq -5\text{ V}$	-30	-	-	A
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	$V_{GS} = -10\text{ V}$	$I_D = -10\text{ A}$	-	0.0210	-	$\Omega$
		$V_{GS} = -10\text{ V}$	$I_D = -10\text{ A}, T_J = 125\text{ }^\circ\text{C}$	-	0.0409	-	
		$V_{GS} = -10\text{ V}$	$I_D = -10\text{ A}, T_J = 175\text{ }^\circ\text{C}$	-	0.0504	-	
		$V_{GS} = -4.5\text{ V}$	$I_D = -5\text{ A}$	-	0.0288	-	
Forward Transconductance <sup>b</sup>	$g_{fs}$	$V_{DS} = -15\text{ V}, I_D = -10\text{ A}$		-	26	-	S
<b>Dynamic <sup>b</sup></b>							
Input Capacitance	$C_{iss}$	$V_{GS} = 0\text{ V}$	$V_{DS} = -25\text{ V}, f = 1\text{ MHz}$	-	2600	3400	$\mu\text{F}$
Output Capacitance	$C_{oss}$			-	310	450	
Reverse Transfer Capacitance	$C_{rss}$			-	200	275	
Total Gate Charge <sup>c</sup>	$Q_g$	$V_{GS} = -10\text{ V}$	$V_{DS} = -30\text{ V}, I_D = -5\text{ A}$	-	65	100	nC
Gate-Source Charge <sup>c</sup>	$Q_{gs}$			-	9.5	-	
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$			-	19	-	
Gate Resistance	$R_g$	f = 1 MHz		0.50	1.19	1.80	$\Omega$
Turn-On Delay Time <sup>c</sup>	$t_{d(on)}$	$V_{DD} = -30\text{ V}, R_L = 6\text{ }\Omega$ $I_D \cong -5\text{ A}, V_{GEN} = -10\text{ V}, R_g = 1\text{ }\Omega$		-	15	25	ns
Rise Time <sup>c</sup>	$t_r$			-	5	10	
Turn-Off Delay Time <sup>c</sup>	$t_{d(off)}$			-	40	75	
Fall Time <sup>c</sup>	$t_f$			-	6	12	
<b>Source-Drain Diode Ratings and Characteristics <sup>b</sup></b>							
Pulsed Current <sup>a</sup>	$I_{SM}$			-	-	-100	A
Forward Voltage	$V_{SD}$	$I_F = -10\text{ A}, V_{GS} = 0\text{ V}$		-	-0.80	-1.2	V

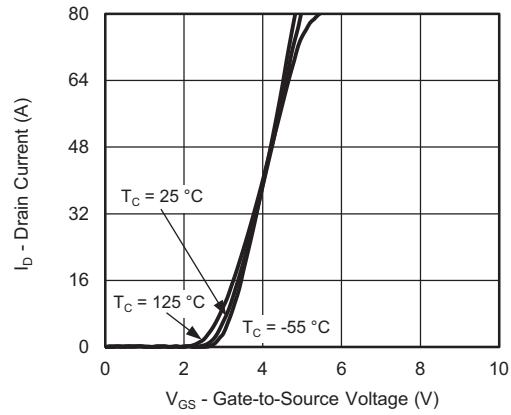
**Notes**

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
 b. Guaranteed by design, not subject to production testing.  
 c. Independent of operating temperature.

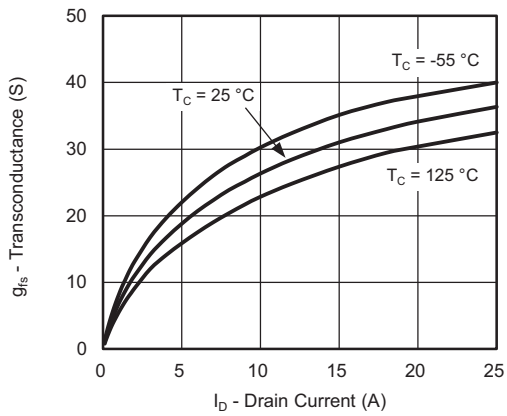
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



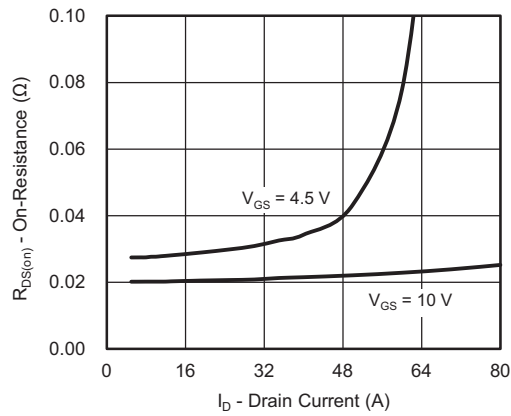
**Output Characteristics**



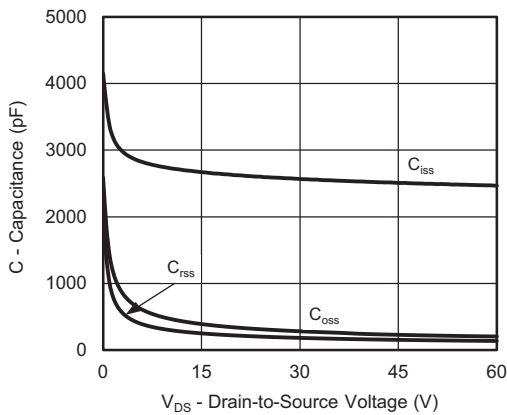
**Transfer Characteristics**



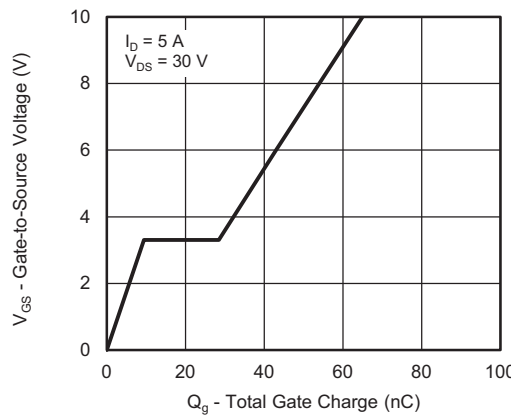
**Transconductance**



**On-Resistance vs. Drain Current**

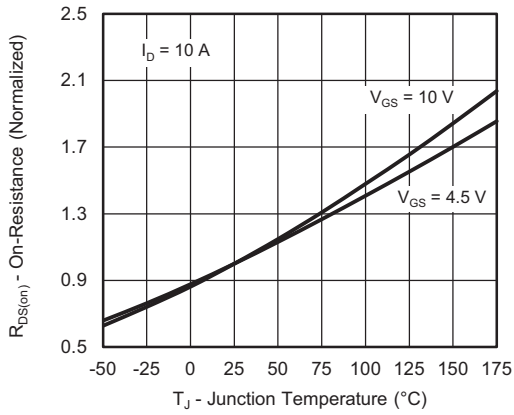


**Capacitance**

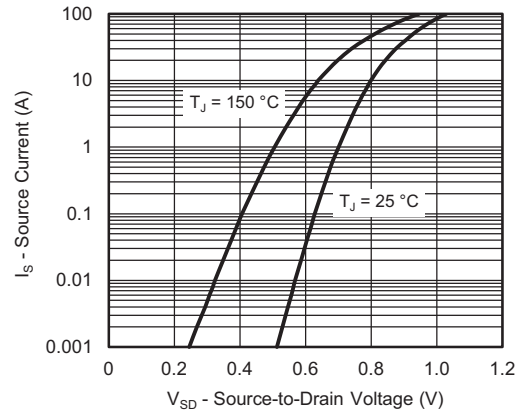


**Gate Charge**

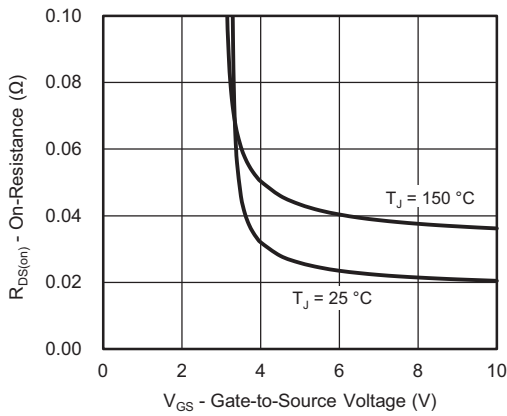
**TYPICAL CHARACTERISTICS** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



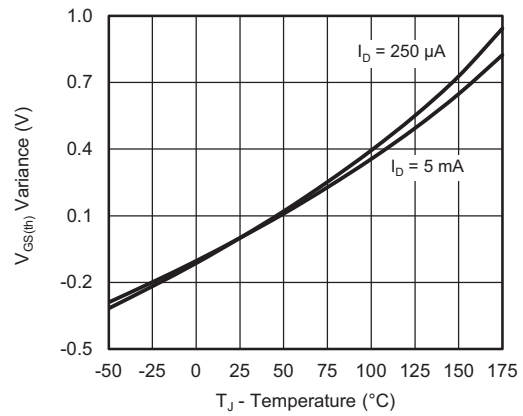
**On-Resistance vs. Junction Temperature**



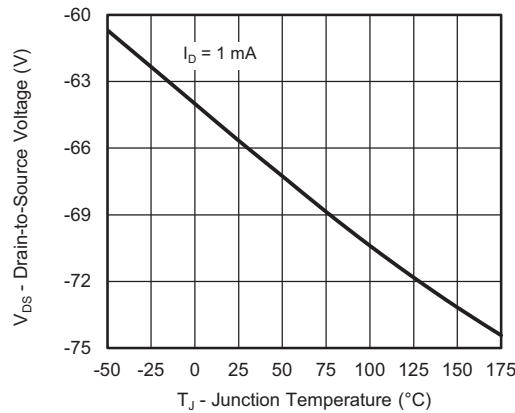
**Source Drain Diode Forward Voltage**



**On-Resistance vs. Gate-to-Source Voltage**

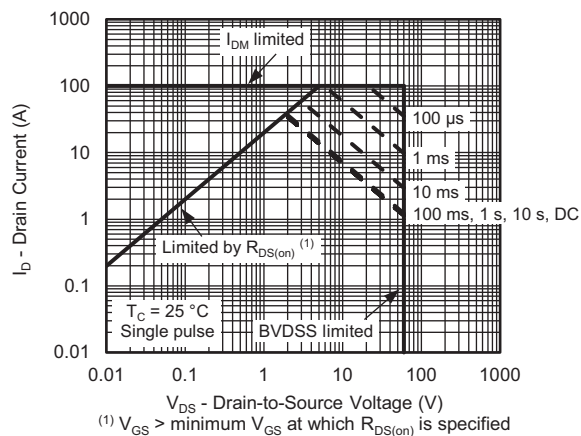


**Threshold Voltage**

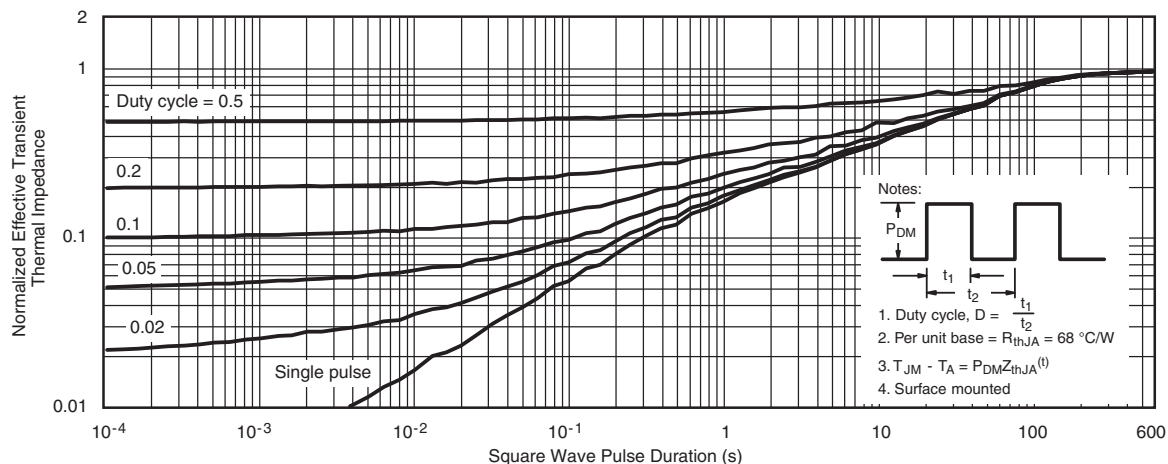


**Drain-Source Breakdown vs. Junction Temperature**

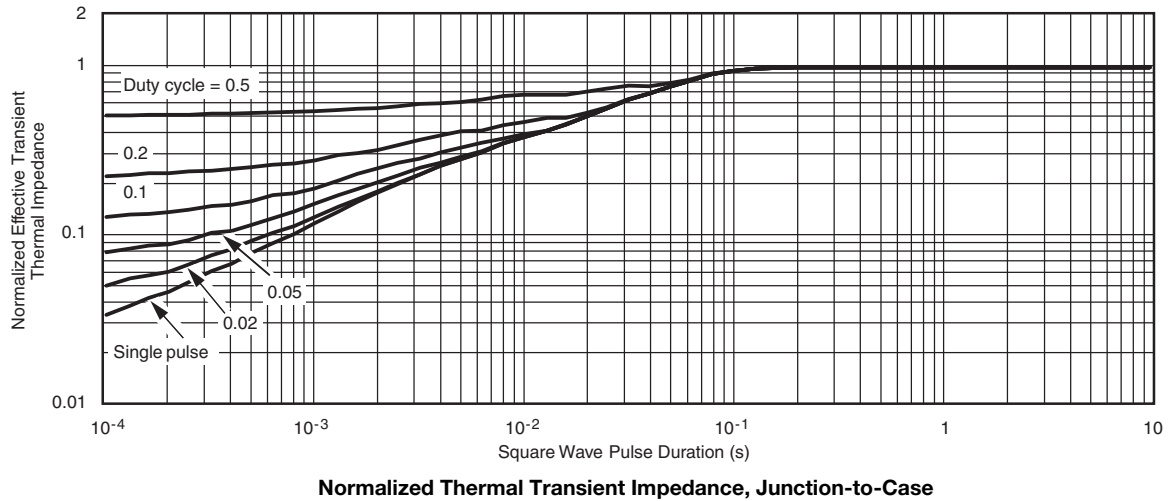
**THERMAL RATINGS** ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)



**Safe Operating Area**



**Normalized Thermal Transient Impedance, Junction-to-Ambient**

**THERMAL RATINGS** ( $T_C = 25\text{ }^\circ\text{C}$ , unless otherwise noted)

**Note**

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient ( $25\text{ }^\circ\text{C}$ )
  - Normalized Transient Thermal Impedance Junction-to-Case ( $25\text{ }^\circ\text{C}$ )
 are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

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