

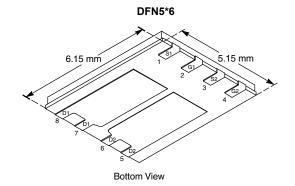
Dual N-Channel 30 V (D-S) MOSFET

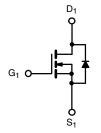
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
30	0.018 at V _{GS} = 10 V	22		

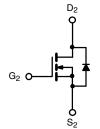
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	30		V	
Gate-Source Voltage		V _{GS}	± 20		, ,	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I-	22		A	
Continuous Diam Guirent (1) = 130 G)	T _A = 70 °C	l _D	15			
Pulsed Drain Current		I _{DM}	50		^	
Continuous Source Current (Diode Conduction) ^a		I _S	2.9			
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	3.5		W	
Maximum Fower Dissipation	T _A = 70 °C	T FD	2.2		VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	
Soldering Recommendations (Peak Temperature)			20	60	Ü	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maria	t ≤ 10 s	R _{thJA}	26	35	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		60	85		
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	3.9	5.5		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

服务热线:400-655-8788

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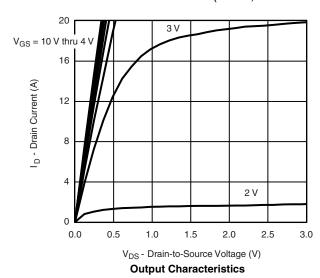
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.8		2.4	٧	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = 30 V, V _{GS} = 0 V			1		
	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Drain-Source On-State Resistance ^a	Б	V _{GS} = 10 V, I _D = 10 A		0.018		-	
	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 8.5 \text{ A}$		0.024		Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 10 A		22		S	
Diode Forward Voltage ^a	V_{SD}	I _S = 2.9 A, V _{GS} = 0 V		0.75	1.2	٧	
Dynamic ^b							
Total Gate Charge	Qg			13	20	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$		2			
Gate-Drain Charge	Q _{gd}			2.7			
Gate Resistance	R_g		0.5		3.2	Ω	
Turn-On Delay Time	t _{d(on)}			8	16		
Rise Time	t _r I _D	$V_{DD} = 15 \text{ V}, R_L = 15\Omega$		10	20	ns	
Turn-Off Delay Time		$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$		21	40		
Fall Time	t _f			10	20		
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 2.9 \text{ A}, dI/dt = 100 \text{ A}/\mu\text{s}$		40	80		

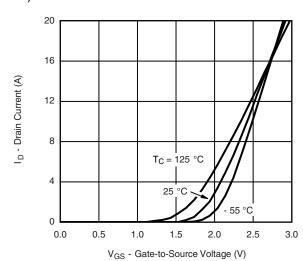
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

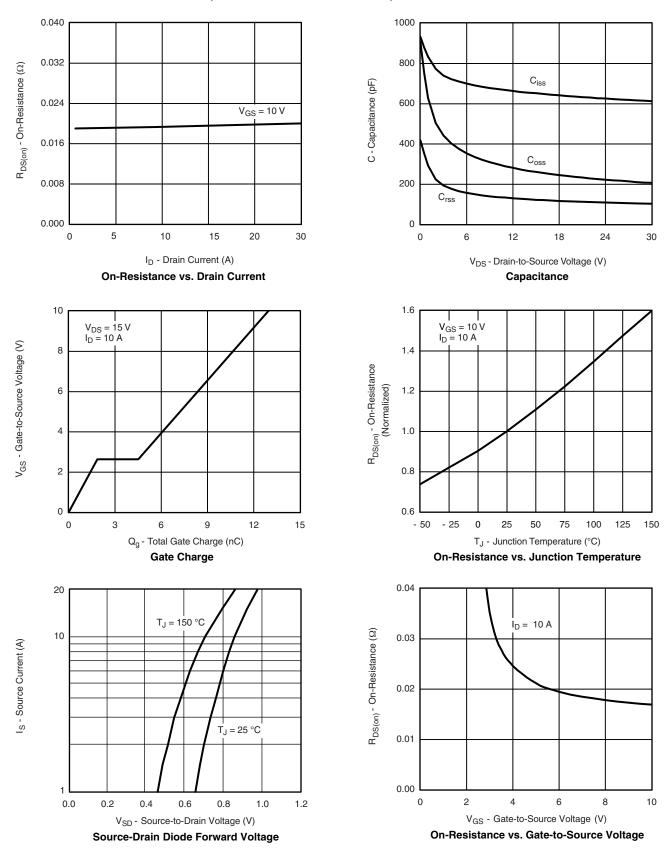




Transfer Characteristics

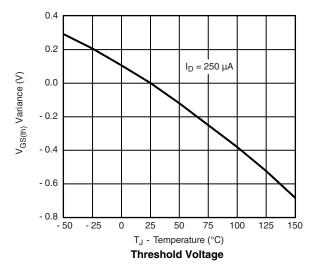


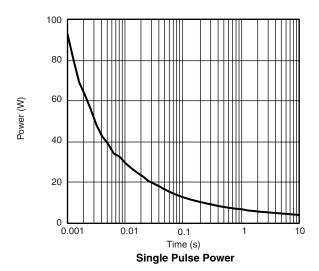
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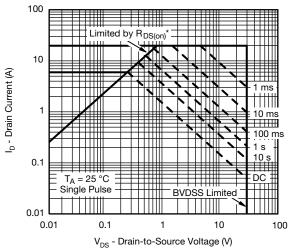


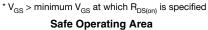


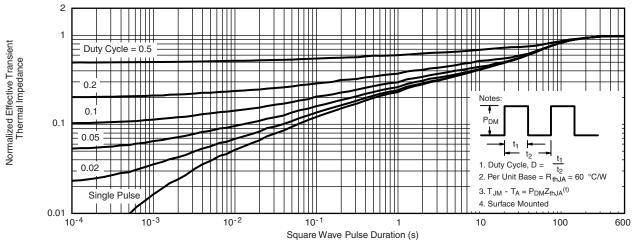
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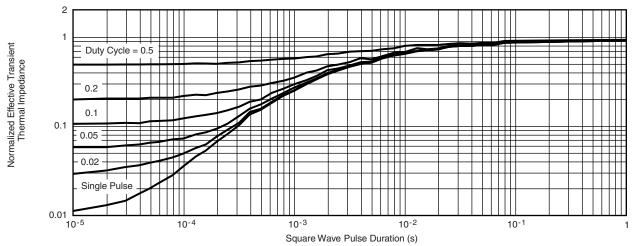




Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case



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