

P-Channel 60-V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)	Q _g (Typ)		
- 60	0.062 at V _{GS} = - 10 V	- 20	12.5		
- 60	0.074 at V _{GS} = - 4.5 V	- 15	12.3		

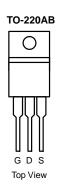
FEATURES

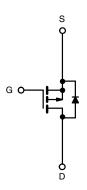
- TrenchFET® Power MOSFET
- 100 % UIS Tested



APPLICATIONS

Load Switch





P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted						
Parameter	Symbol	Limit	Unit			
Gate-Source Voltage	V _{GS}	± 20	V			
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 25 °C	I=	- 20			
Commudus Diam Guilent (1) = 173 O)	T _C = 100 °C	I _D	- 12			
Pulsed Drain Current	I _{DM}	- 60	Α			
Continuing Source Current (Diode Conduction)	I _S	- 12				
Avalanche Current	I _{AS}	- 12				
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	7.2	mJ		
Maximum Daway Dissination	T _C = 25 °C	P _D	60 ^a	w		
Maximum Power Dissipation	T _A = 25 °C	r D	2 ^b] vv		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 175	°C			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
hunding to Ambient	t ≤ 10 sec	R _{thJA}	20	25	°C/W	
Junction-to-Ambient ^D	Steady State	' 'thJA	62	75		
Junction-to-Case		R _{thJC}	5	6		

Notes:

- a. See SOA curve for voltage derating.
- b. Surface Mounted on 1" x 1" FR-4 boad.

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1



Parameter	Symbol	Test Conditions	Min	Typ ^a	Max	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 60			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0	- 2.0	- 3.0	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V _{DS} = - 60 V, V _{GS} = 0 V			- 1		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ	
		V _{DS} = - 60 V, V _{GS} = 0 V, T _J = 175 °C			- 150		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 10			Α	
		V _{GS} = - 10 V, I _D = - 5 A			0.100		
	_	V _{GS} = - 10 V, I _D = - 5 A, T _J = 125 °C			0.200	0	
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 5 A, T _J = 175 °C			0.300	Ω	
		V _{GS} = - 4.5 V, I _D = - 2 A			0.120		
Forward Transconductance ^b	9 _{fs}	V _{DS} = - 15 V, I _D = - 5 A		8		S	
Dynamic	•	'		•	•		
Input Capacitance	C _{iss}			850		pF	
Output Capacitance	C _{oss}	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		120			
Reverse Transfer Capacitance	C _{rss}			90			
Total Gate Charge	Q_g			13			
Gate-Source Charge	Q _{gs}	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -8.4 \text{ A}$		2.3		nC	
Gate-Drain Charge	Q_{gd}]		3.2			
Gate Resistance	R_{g}	f = 1 MHz	8.0			Ω	
Turn-On Delay Time ^c	t _{d(on)}			5	10		
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_{L} = 3.57 \Omega$		14	25		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ - 8.4 A, V_{GEN} = - 10 V, R_G = 2.5 Ω		15	25	ns	
Fall Time ^c	t _f	1		7	12		
Source-Drain Diode Ratings and Cha	racteristics	(T _C = 25 °C) ^b					
Pulsed Current	I _{SM}			- 20		Α	
Forward Voltage ^b	V_{SD}	I _F = - 2 A, V _{GS} = 0 V		- 0.9	- 1.3	V	
Reverse Recovery Time	t _{rr}	1 - 9 A di/d+ 100 A/::a		50	80	ns	
Reverse Recovery Time	Q _{rr}			80	120	nC	

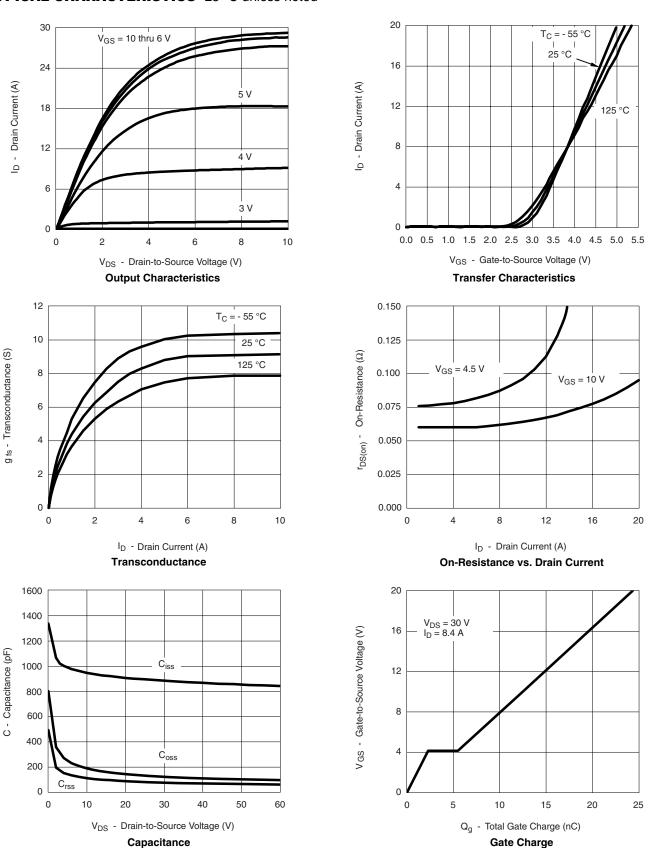
Notes:

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

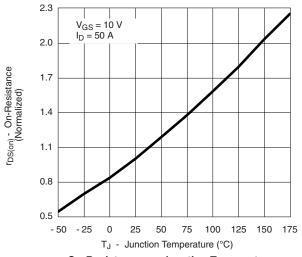


TYPICAL CHARACTERISTICS 25 °C unless noted

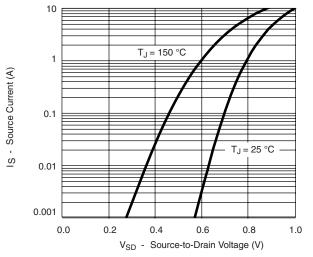




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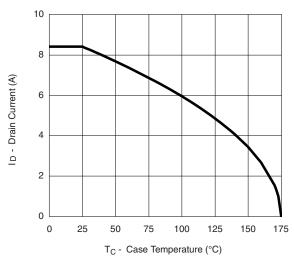




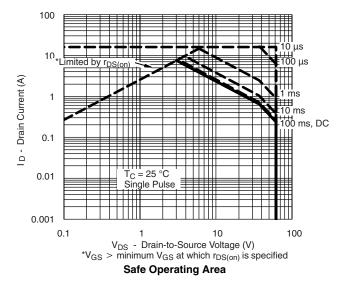


Source-Drain Diode Forward Voltage

THERMAL RATINGS

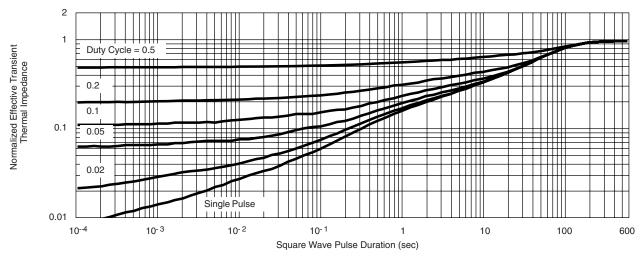


Drain Current vs. Case Temperature

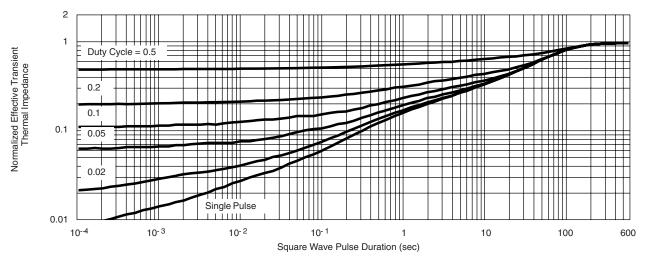




THERMAL RATINGS



Normalized Thermal Transient Impedance, Junction-to-Ambient



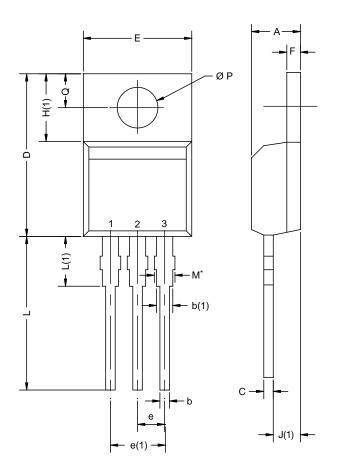
Normalized Thermal Transient Impedance, Junction-to-Case

E-mail: China@VBsemi TEL:86-755-83251052

5



TO-220AB



	MILLIM	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	4.25	4.65	0.167	0.183	
b	0.69	1.01	0.027	0.040	
b(1)	1.20	1.73	0.047	0.068	
С	0.36	0.61	0.014	0.024	
D	14.85	15.49	0.585	0.610	
Е	10.04	10.51	0.395	0.414	
е	2.41	2.67	0.095	0.105	
e(1)	4.88	5.28	0.192	0.208	
F	1.14	1.40	0.045	0.055	
H(1)	6.09	6.48	0.240	0.255	
J(1)	2.41	2.92	0.095	0.115	
L	13.35	14.02	0.526	0.552	
L(1)	3.32	3.82	0.131	0.150	
ØΡ	3.54	3.94	0.139	0.155	
Q	2.60	3.00	0.102	0.118	
ECN: X12-0208-Rev. N, 08-Oct-12					

ECN: X12-0208-Rev. N, 08-Oct-1: DWG: 5471

Notes

 * M = 1.32 mm to 1.62 mm (dimension including protrusion) Heatsink hole for HVM



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