STD65N55F3



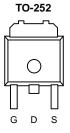
N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	60			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.0063			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 V$	0.0120			
I _D (A)	97			
Configuration	Single			

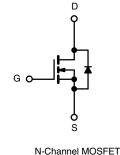
FEATURES

- TrenchFET[®] Power MOSFET
- Package with Low Thermal Resistance
- 100 % R_g and UIS Tested





Top View



ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted) PARAMETER SYMBOL LIMIT UNIT Drain-Source Voltage V_{DS} 60 V Gate-Source Voltage V_{GS} ± 20 $T_C = 25 \ ^\circ C$ 97 Continuous Drain Current I_D T_C = 125 °C 56 Continuous Source Current (Diode Conduction)^a I_S 100 А Pulsed Drain Current^b 290 I_{DM} Single Pulse Avalanche Current I_{AS} 45 L = 0.1 mHSingle Pulse Avalanche Energy 101 mJ E_{AS} $T_C = 25 \ ^\circ C$ 136 Maximum Power Dissipation^b P_D W T_C = 125 °C 45 T_J, T_{stg} °C Operating Junction and Storage Temperature Range - 55 to + 175

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R _{thJA}	50	°C/W	
Junction-to-Case (Drain)		R _{thJC}	1.1	0/10	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.

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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT		
Static								
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$		60	-	-	v	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$			4.0		
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 V, V_{GS} = \pm 20 V$		-	± 100	nA	
		$V_{GS} = 0 V$	V _{DS} = 60 V	-	-	1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 60 V, T _J = 125 °C	-	-	50	μA	
-		$V_{GS} = 0 V$	V _{DS} = 60 V, T _J = 175 °C	-	-	150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α	
		V _{GS} = 10 V	I _D = 25 A	-	0.0050	-	Ω	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V	I _D = 25 A, T _J = 125 °C	-	-	0.0117		
	R _{DS(on)}	V _{GS} = 10 V	I _D = 25 A, T _J = 175 °C	-	-	0.0149		
		V _{GS} = 4.5 V	I _D = 20 A	-	0.0120	-		
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 25 A		-	177	-	S	
Dynamic ^b					•			
Input Capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	-	4844	6060	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	441	555		
Reverse Transfer Capacitance	C _{rss}			-	200	250		
Total Gate Charge ^c	Qg			-	82	125		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V	$V_{DS} = 30 \text{ V}, \text{ I}_{D} = 50 \text{ A}$	-	14.5	-	nC	
Gate-Drain Charge ^c	Q _{gd}			-	13.5	-		
Gate Resistance	R _g	f = 1 MHz		1	2	3	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	14	21		
Rise Time ^c	tr	$\begin{array}{l} V_{\text{DD}}=30 \text{ V}, \ R_{\text{L}}=0.6 \ \Omega\\ I_{\text{D}}\cong50 \text{ A}, \ V_{\text{GEN}}=10 \text{ V}, \ R_{\text{g}}=1 \ \Omega \end{array}$		-	5	8	ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	41	62		
Fall Time ^c	t _f			-	7	11		
Source-Drain Diode Ratings and Char	acteristics ^b							
Pulsed Current ^a	I _{SM}			-	-	290	Α	
Forward Voltage	V _{SD}	I _F = 50 A, V _{GS} = 0 V		-	0.9	1.5	V	

Notes

a. Pulse test; pulse width $\leq 300~\mu\text{s},~\text{duty}~\text{cycle} \leq 2~\%.$

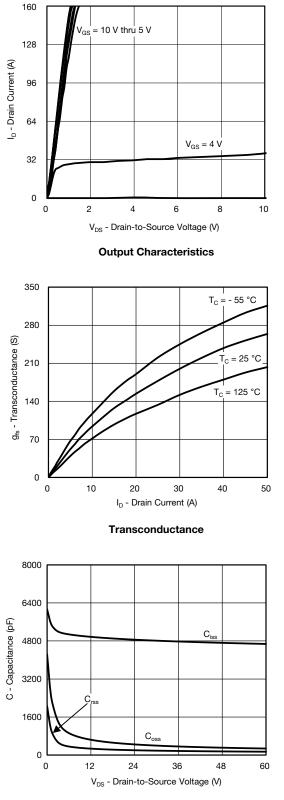
b. Guaranteed by design, not subject to production testing.

c. Independent of operating temperature.

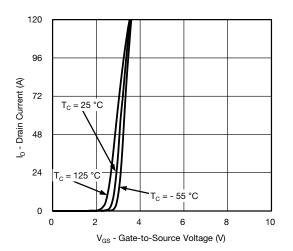
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



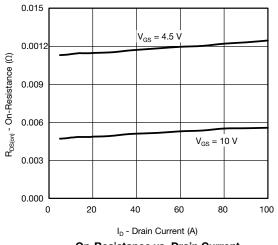
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



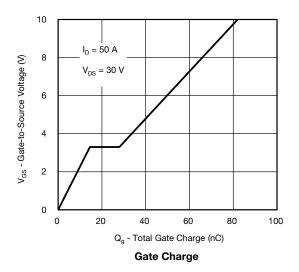
Capacitance



Transfer Characteristics

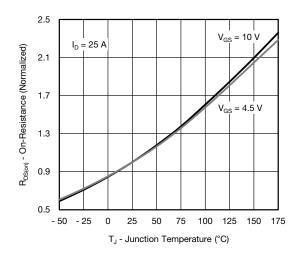


On-Resistance vs. Drain Current

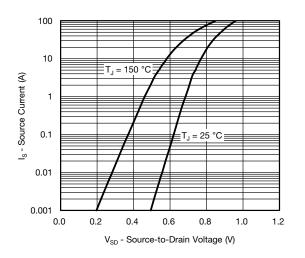




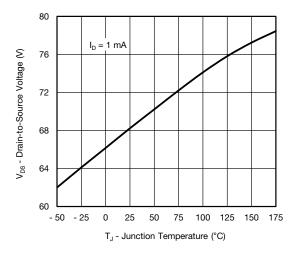
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



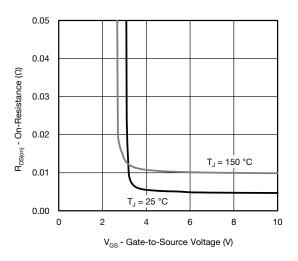
On-Resistance vs. Junction Temperature



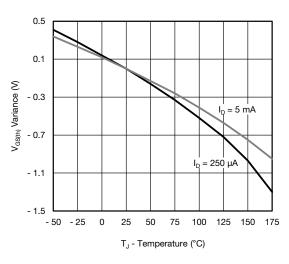
Source Drain Diode Forward Voltage



Drain Source Breakdown vs. Junction Temperature



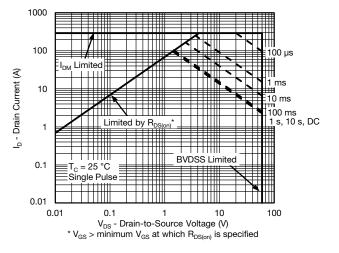
On-Resistance vs. Gate-to-Source Voltage



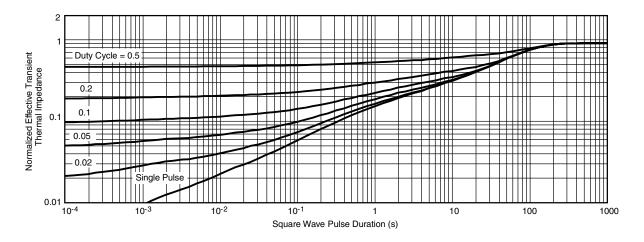
Threshold Voltage

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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



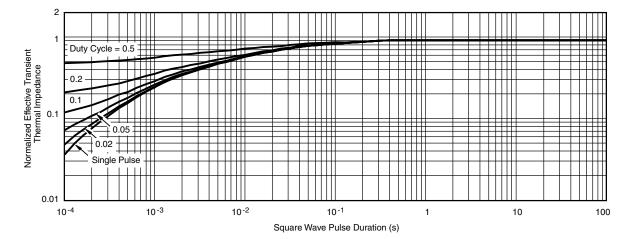
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

• The characteristics shown in the two graphs

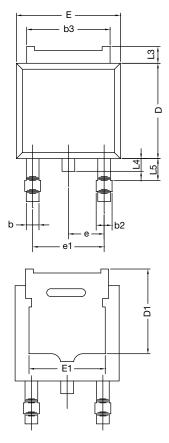
- Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)

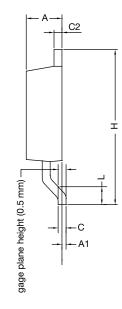
- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.



TO-252AA CASE OUTLINE





	MILLIN	IETERS	INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
А	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
E	6.35	6.73	0.250	0.265	
E1	4.32	-	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28	BSC	0.090 BSC		
e1	4.56	BSC	0.180 BSC		
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347					

Note

• Dimension L3 is for reference only.



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