

N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	100					
R _{DS(on)} (Ω)	V _{GS} = 10 V	0.086				
Q _g (Max.) (nC)	72					
Q _{gs} (nC)	11					
Q _{gd} (nC)	32					
Configuration	Single					

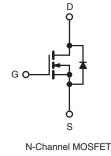
FEATURES

- Isolated Package
- High Voltage Isolation = 2.5 kV_{RMS} (t = 60 s; f = 60 Hz)
- Sink to Lead Creepage Distance = 4.8 mm
- 175 °C Operating Temperature
- · Dynamic dV/dt Rating
- · Low Thermal Resistance
- Lead (Pb)-free Available



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PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	100	V	
Gate-Source Voltage			V _{GS}	± 20	- v	
Continuous Drain Current	V _{GS} at 10 V	T _C = 25 °C	- I _D -	18		
		T _C = 100 °C		12	A	
Pulsed Drain Current ^a			I _{DM}	68	1	
Linear Derating Factor				0.32	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	720	mJ	
Repetitive Avalanche Current ^a			I _{AR}	17	A	
Repetitive Avalanche Energy ^a			E _{AR}	4.8	mJ	
Maximum Power Dissipation	T _C = 25 °C		PD	P _D 48		
Peak Diode Recovery dV/dt ^c	•		dV/dt	5.5	V/ns	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to + 175	°C		
Soldering Recommendations (Peak Temperature)	for 10 s		-	300 ^d		
Mounting Torque	6-32 or M3 screw			10	lbf ⋅ in	
				1.1	N · m	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. $V_{DD} = 25 \text{ V}$, starting $T_J = 25 \text{ °C}$, L = 3.7 mH, $R_G = 25 \Omega$, $I_{AS} = 17 \text{ A}$ (see fig. 12). c. $I_{SD} \le 17 \text{ A}$, dI/dt $\le 200 \text{ A}/\mu\text{s}$, $V_{DD} \le V_{DS}$, $T_J \le 175 \text{ °C}$.

d. 1.6 mm from case.

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THERMAL RESISTANCE RAT	TINGS								
PARAMETER	SYMBOL	TYP. MAX.			UNIT				
Maximum Junction-to-Ambient	R _{thJA}	- 65			2 0 AN				
Maximum Junction-to-Case (Drain)	R _{thJC}	- 3.1				- °C/W			
SPECIFICATIONS $T_J = 25 \ ^{\circ}C$,		vise noted			1	1	1	1	
PARAMETER	SYMBOL	TES	T CONDITI	ONS	MIN.	TYP.	MAX.	UNIT	
Static									
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} =	0 V, I _D = 2	50 μA	100	-	-	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C,	I _D = 1 mA	-	0.13	-	V/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} =$	V_{GS} , $I_D = 2$	250 μΑ	1.0	-	3.0	V	
Gate-Source Leakage	I _{GSS}	V	$I_{\rm GS} = \pm 20$	V	-	-	± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			-	-	25	μΑ	
	IDSS	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{\text{J}} = 150 ^{\circ}\text{C}$			-	-	250		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 10 V	I _D	= 10 A ^b	-	0.086	-	Ω	
Forward Transconductance	9 _{fs}	V _{DS} =	= 50 V, I _D =	10 A ^b	9.1	-	-	S	
Dynamic								•	
Input Capacitance	Ciss		V _{GS} = 0 V,		-	1700	-		
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1.0 MHz, see fig. 5		-	560	-	pF		
Reverse Transfer Capacitance	C _{rss}			-	120	-			
Drain to Sink Capacitance	С			-	12	-			
Total Gate Charge	Qg			-	-	72			
Gate-Source Charge	Q _{gs}	V _{GS} = 10 V	$I_D = 17$	A, V _{DS} = 80 V, g. 6 and 13 ^b	-	-	11	nC	
Gate-Drain Charge	Q _{gd}	see lig.		g. o and 15	-	-	32	1	
Turn-On Delay Time	t _{d(on)}	I			-	11	-		
Rise Time	t _r	V_{DD} = 50 V, I _D = 17 A, R _G = 9.1 Ω, R _D = 2.9 Ω, see fig. 10 ^b		-	44	-	- ns		
Turn-Off Delay Time	t _{d(off)}			-	53	-			
Fall Time	t _f			-	43	-			
Internal Drain Inductance	L _D	Between lead, 6 mm (0.25") from package and center of die contact		-	4.5	-	nH		
Internal Source Inductance	L _S			-	7.5	-			
Drain-Source Body Diode Characteristic	s					•	•		
Continuous Source-Drain Diode Current	١ _S	MOSFET symbol showing the		-	-	17	A		
Pulsed Diode Forward Currenta	I _{SM}	integral reverse p - n junction diode			-	-		68	
Body Diode Voltage	V_{SD}	$T_J = 25 \ ^\circ C, \ I_S = 17 \ A, \ V_{GS} = 0 \ V^b$		-	-	2.5	V		
Body Diode Reverse Recovery Time	t _{rr}	$T_J = 25 \text{ °C}, I_F = 17 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}^b$		-	180	360	ns		
Body Diode Reverse Recovery Charge	Q _{rr}			-	1.3	2.6	μC		
Forward Turn-On Time	t _{on}	Intrinsic tu	rn-on time i	s negligible (turn	-on is don	ninated by	y L _S and I	_D)	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11).

b. Pulse width \leq 300 $\mu s;$ duty cycle \leq 2 %.





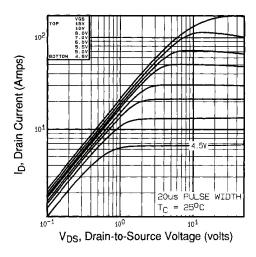


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

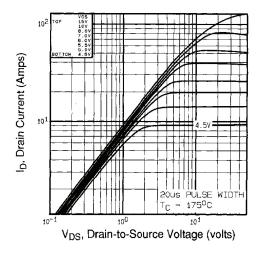


Fig. 2 - Typical Output Characteristics, $T_C = 175$ °C

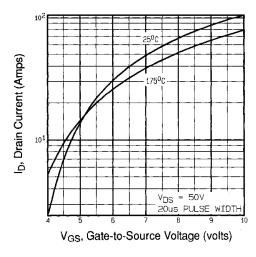


Fig. 3 - Typical Transfer Characteristics

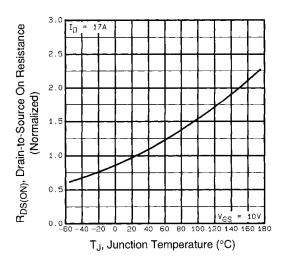


Fig. 4 - Normalized On-Resistance vs. Temperature

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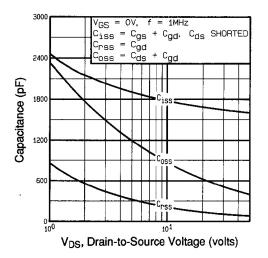
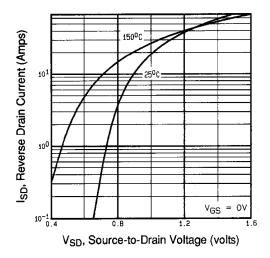


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage



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Fig. 7 - Typical Source-Drain Diode Forward Voltage

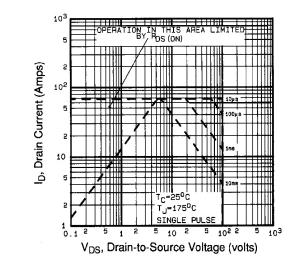


Fig. 8 - Maximum Safe Operating Area

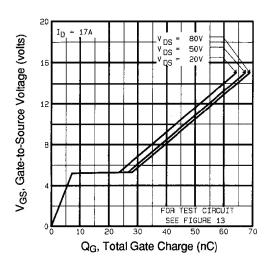


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

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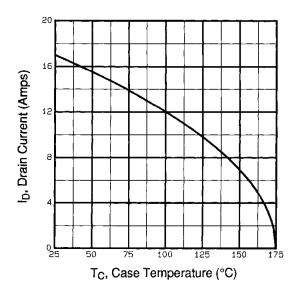


Fig. 9 - Maximum Drain Current vs. Case Temperature

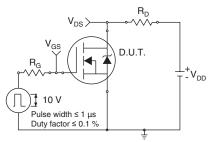


Fig. 10a - Switching Time Test Circuit

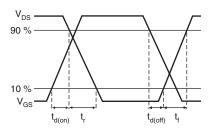
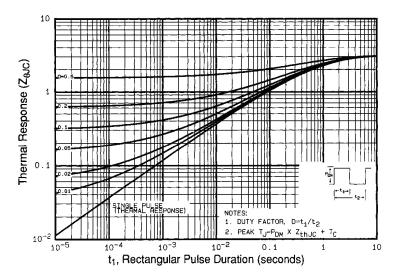


Fig. 10b - Switching Time Waveforms





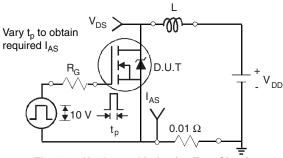


Fig. 12a - Unclamped Inductive Test Circuit

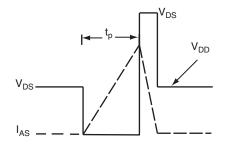


Fig. 12b - Unclamped Inductive Waveforms



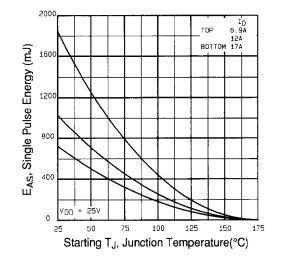


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

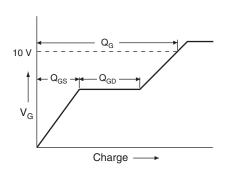
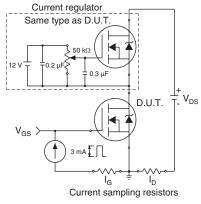
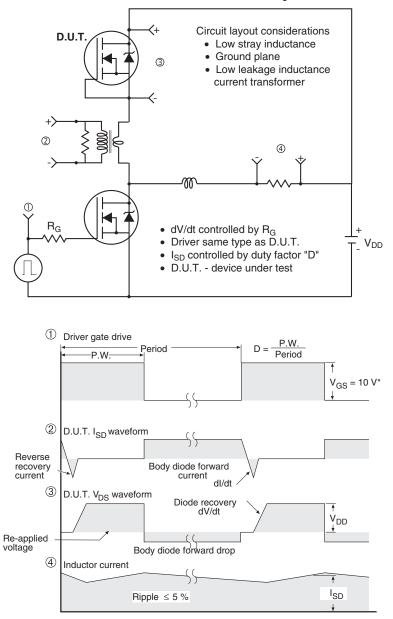


Fig. 13a - Basic Gate Charge Waveform









Peak Diode Recovery dV/dt Test Circuit

* V_{GS} = 5 V for logic level devices

Fig.14 - For N-Channel



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