

STW32NM50N-VB Datasheet

N-Channel 500V (D-S) Super Junction Power MOSFET

PRODUCT SUMMA	RY	
V _{DS} (V) at T _J max.	500)
R _{DS(on)} at 25 °C (Ω)	$V_{GS} = 10 V$	0.120

FEATURES

- Low figure-of-merit (FOM) Ron x Qa
- Low input capacitance (Ciss)
- Reduced switching and conduction losses
- Ultra low gate charge (Q_q)
- Avalanche energy rated (UIS)

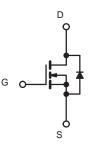
APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Lighting
 - High-intensity discharge (HID)
 - Fluorescent ballast lighting



TO-247

Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C :	= 25 °C, unl	less otherwis	se noted)			
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V _{DS}	500	v	
Gate-Source Voltage			V _{GS}	± 30		
Continuous Drain Current (T. 150 °C)	V at 10 V	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$ $T_{\rm C} = 100 \ ^{\circ}{\rm C}$	ID	30		
Continuous Drain Current (T _J = 150 °C)	V _{GS} at 10 V	T _C = 100 °C		18	А	
Pulsed Drain Current ^a			I _{DM}	90	7	
Linear Derating Factor				1.67	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	980	mJ	
Maximum Power Dissipation			PD	360	W	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +150	°C	
Drain-Source Voltage Slope $T_J = 125 \text{ °C}$		dV/dt	50			
Reverse Diode dV/dt ^d			15	V/ns		
Soldering Recommendations (Peak Temperature) ^c	for	10 s		260	°C	

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature. b. $V_{DD} = 100 \text{ V}$, starting $T_J = 25 \text{ °C}$, L = 30mH, $R_g = 25 \Omega$, $I_{AS} = 15A$.

c. 1.6 mm from case. d. $I_{SD} \le I_D$, dl/dt = 100 A/µs, starting T_J = 25 °C.

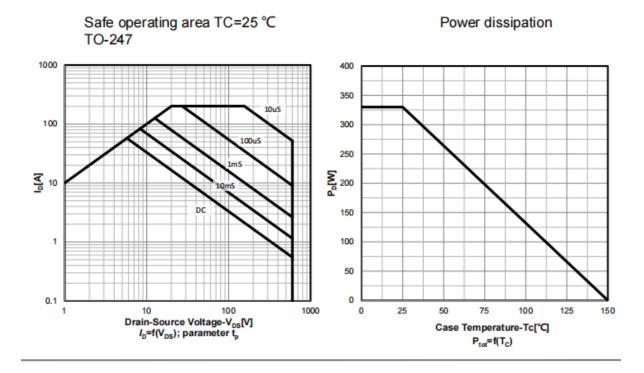


THERMAL RESISTANCE RATII		1							
PARAMETER	SYMBOL	TYP.		MAX.			UNIT		
Maximum Junction-to-Ambient	R _{thJA}	-		62			°C/W		
Maximum Junction-to-Case (Drain)	R _{thJC}	- 0.38			8	6/11			
SPECIFICATIONS (T _J = 25 °C, u	nless otherw	ise noted)							
PARAMETER	SYMBOL	-		IONS	MIN.	TYP.	MAX.	UNIT	
Static						ļ		ļ	
Drain-Source Breakdown Voltage	V _{DS}	Ves	= 0 V, I _D =	1 mA	500	-	_	V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$			$I_{\rm D} = 1 \rm{mA}$	-	0.70	-		
Gate-Source Threshold Voltage (N)	V _{GS(th)}			5	2.5	-	4.5		
	• GS(III)		$V_{DS} = V_{GS}, I_D = 250 \ \mu A$ $V_{GS} = \pm 20 \ V$		- 2.5	-	± 100	-	
Gate-Source Leakage	I _{GSS}		$V_{GS} = \pm 30$		-	_	± 100		
						_	1	V V/°C V/°C ΠΑ μΑ μΑ Ω S	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	_	$V_{\rm DS} = 500$ V, $V_{\rm GS} = 0$ V 00 V, $V_{\rm GS} = 0$ V, $T_{\rm J} = 125$ °C			_	100	μA	
Drain-Source On-State Resistance	R _{DS(on)}	$V_{GS} = 10 V$		$I_{\rm D} = 10A$	-	0.120	-	Ω	
Forward Transconductance		V _{DS}	= 30 V, I _D	= 10 A	-	5.6	-	S	
Dynamic	0.1				L				
Input Capacitance	C _{iss}		$V_{22} = 0$	1	-	3300	-	1	
Output Capacitance	Coss	V _{GS} = 0 V, V _{DS} = 100 V,		-	330	-	1		
Reverse Transfer Capacitance	C _{rss}		f = 1 MH	z	-	4	-		
Effective Output Capacitance, Energy Related ^a	C _{o(er)}				-	63	-	pF	
Effective Output Capacitance, Time Related ^b	C _{o(tr)}	$V_{DS} = 0 V \text{ to } 520 V, V_{GS} = 0 V$		-	213	-			
Total Gate Charge	Qg				-	160	-		
Gate-Source Charge	Q _{gs}	$V_{GS} = 10 V$	I _D = 20	0 A, V _{DS} = 520 V	-	39	-	nC	
Gate-Drain Charge	Q _{gd}				-	47	-		
Turn-On Delay Time	t _{d(on)}				-	18	25		
Rise Time	t _r	VDD	= 520 V, I _C) = 20A,	-	24	55	ns	
Turn-Off Delay Time	t _{d(off)}	$V_{GS} = 10 \text{ V}, \text{ R}_{g} = 9.1 \Omega$		-	80	-	110		
Fall Time	t _f	VGS -	= 10 v, ng	= 9.1 \22	-	12	-		
Gate Input Resistance	R _g	f = 1	MHz, ope	n drain	-	0.8	-	Ω	
Drain-Source Body Diode Characteristic	s								
Continuous Source-Drain Diode Current	I _S	MOSFET symbol		30					
Pulsed Diode Forward Current	I _{SM}	integral reverse p - n junction diode -		-	90	A			
Diode Forward Voltage	V _{SD}	T _J = 25 °C, I _S = 8 A, V _{GS} = 0 V		-	-	1.5	V		
Reverse Recovery Time	t _{rr}				-	520	-	ns	
Reverse Recovery Charge	Q _{rr}	T _J = 25 °C, I _F = I _S = 8 A, dl/dt = 100 A/ μ s, V _B = 400 V		-	5.8	-	μC		
Reverse Recovery Current	I _{RRM}	ai/at =	ου Α/μs, \	v _R = 400 V	-	4 5	-	A	

Notes

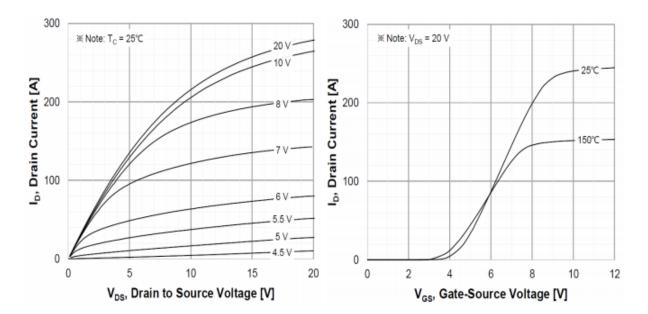
a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} . b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} .



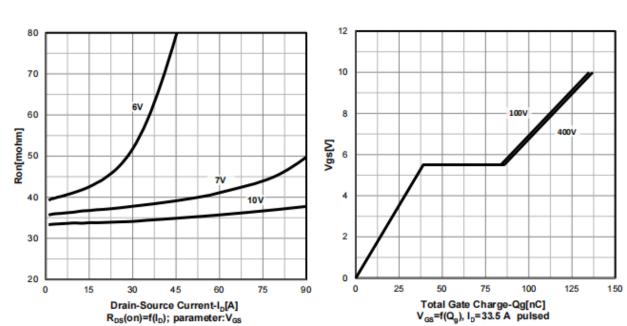


Typ. output characteristics T_i =25 $^{\circ}C$

Transfer characteristics





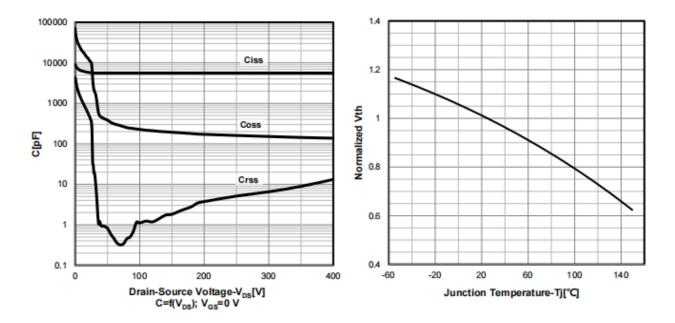


Typ. drain-source on-state resistance

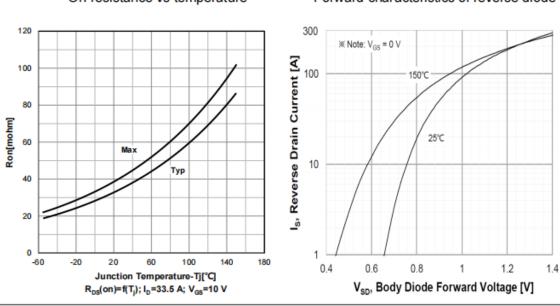
Typ. gate charge characteristics

Typ. capacitances





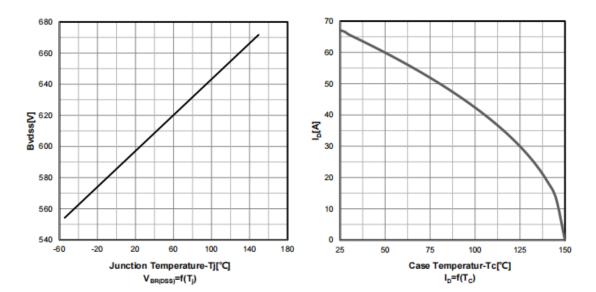




On-resistance vs temperature Forward characteristics of reverse diode

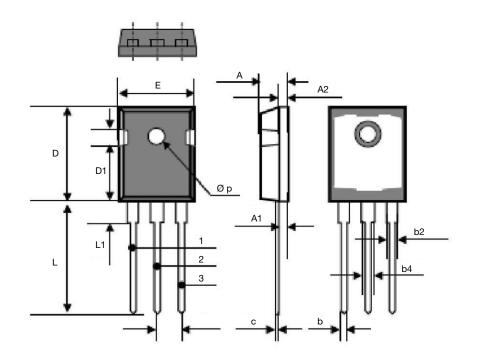
Drain-source breakdown voltage

Drain current vs temperature





TO-247



DIM	MILLIN	METERS	INCHES		
DIM.	MIN.	MAX.	MIN.	MAX.	
А	4.70	5.31	0.185	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b2	1.65	2.41	0.065	0.095	
b4	2.59	3.43	0.102	0.135	
С	0.61 BSC		0.024 BSC		
D	20.80	21.46	0.819	0.845	
D1	3.68	5.49	0.145	0.216	
(e)	5.46 BSC		0.215 BSC		
E	15.49	16.26	0.610	0.640	
L	19.81	20.32	0.780	0.800	
L1	4.06	4.50	0.160	0.177	
Øp	3.51	3.66	0.138	0.144	



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