

RoHS COMPLIANT HALOGEN

# N-Channel 200 V (D-S) MOSFET

**FEATURES** 

Definition

100 % R<sub>g</sub> and UIS Tested
TrenchFET<sup>®</sup> Power MOSFET

• Halogen-free According to IEC 61249-2-21

Compliant to RoHS Directive 2002/95/EC

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)		
200	1.4 at V <sub>GS</sub> = 10 V	0.6		

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#### Top View

# G C S N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_A = 25 \text{ °C}$ , unless otherwise noted)						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	200		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
Continuous Drain Current (T <sub>.I</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 25 °C	– I <sub>D</sub>	0.6	0.45		
Continuous Drain Current $(T_j = 150^{\circ} C)$	T <sub>A</sub> = 70 °C		0.5	0.35	А	
Pulsed Drain Current <sup>b</sup>		I <sub>DM</sub>	2.5		A	
Avalanche Current <sup>b</sup>	L = 0.1 mH	I <sub>AS</sub>	2.5			
Single Avalanche Energy		E <sub>AS</sub>	50		mJ	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	0.6		А	
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	Р	1.55	1.03	W	
	T <sub>A</sub> = 70 °C	P <sub>D</sub>	1.20	0.87	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 5 s	Р	80	100	
Maximum Sunction-to-Ambient	Steady State	R <sub>thJA</sub>	130	170	°C/W
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	45	55	

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.



				Limits			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 V, I_D = 1 mA$	200			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.5		3.5		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current		$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 100 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			75		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 15$ V, $V_{GS} = 10$ V	2.5			Α	
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 0.5 \text{ A}$		1.4		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.5 A		4		S	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = 1 A, V <sub>GS</sub> = 0 V		0.8	1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			3	5		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = 100 V, $V_{GS}$ = 10 V, $I_{D}$ = 0.5 A		0.37		nC	
Gate-Drain Charge	Q <sub>gd</sub>			1.45			
Gate Resistance	Rg		0.5	1.3	2.4	Ω	
Switching							
Turn-On Delay Time	t <sub>d(on)</sub>			7	11		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 100 V, $R_{L}$ = 33 $\Omega$		10	15	]	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 0.2$ A, $V_{GEN} = 10$ V, $R_g = 6 \Omega$		9	15	ns	
Fall Time	t <sub>f</sub>			11	15		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 0.5 A, dl/dt = 100 A/μs50100					

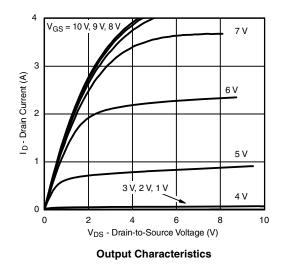
Notes:

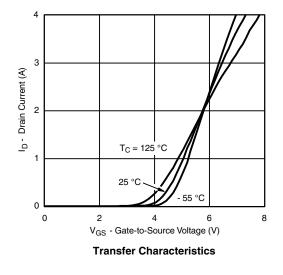
a. Pulse test: PW  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

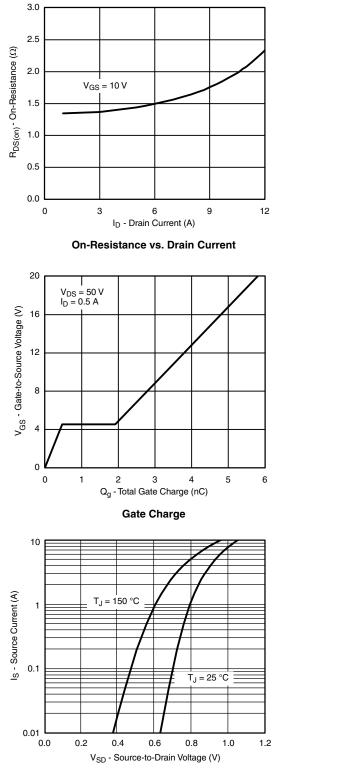
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

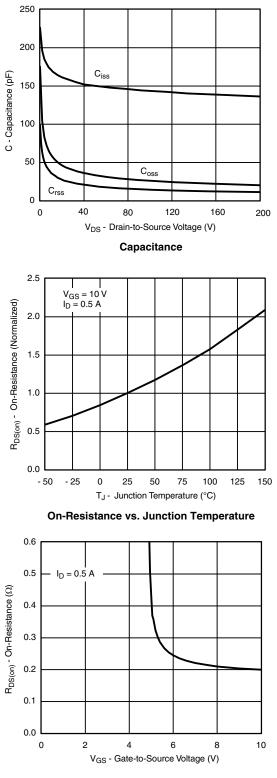






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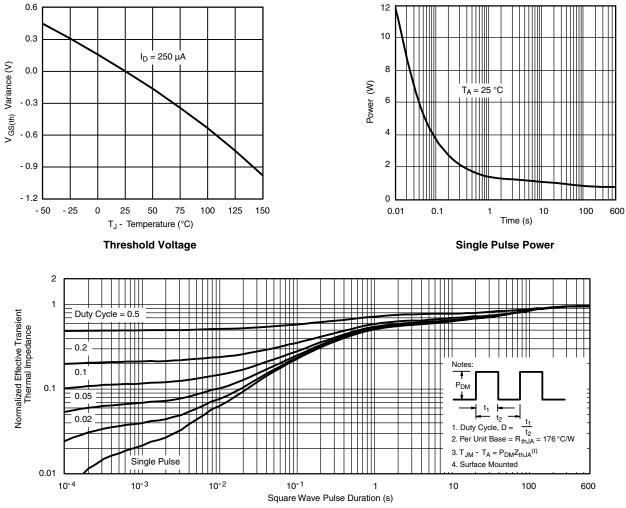




Source-Drain Diode Forward Voltage

On-Resistance vs. Gate-to-Source Voltage



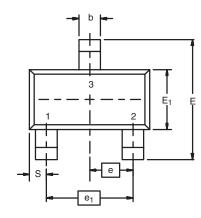


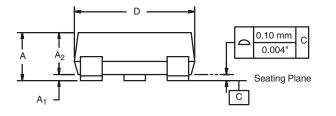
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

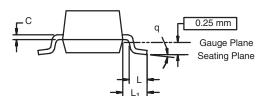
Normalized Thermal Transient Impedance, Junction-to-Ambient



#### SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES		
	Min	Мах	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
C	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025 Ref		
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K, 09- DWG: 5479	Jul-01				



#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)



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