

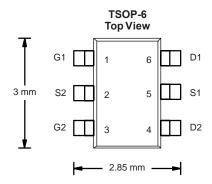
N- and P-Channel 20V (D-S) MOSFET

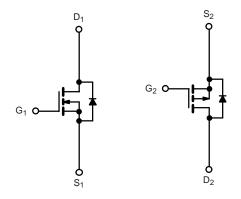
PRODUCT SUMMARY						
	V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)			
N-Channel	20	$0.024 \text{ at V}_{GS} = 10 \text{ V}$	5.5			
		0.036 at V _{GS} = 4.5 V	4.2			
P-Channel	- 20	0.069 at V _{GS} = - 10 V	- 3.4			
P-Channel		0.083 at V _{GS} = - 4.5 V	- 2.5			

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
 Compliant to RoHS Directive 2002/95/EC







N-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted							
Parameter	Symbol	N-Channel	P-Channel	Unit			
Drain-Source Voltage		V _{DS}	20	- 20	V		
Gate-Source Voltage		V _{GS}	± 20	± 20	V		
Continuous Dunis Comment /T 450 90\d.b	T _A = 25 °C	- I _D	5.5	- 3.4	A		
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		4.0	- 2.3			
Pulsed Drain Current		I _{DM}	15	10	A		
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	1.05	- 1.05			
M. D. D ah	T _A = 25 °C	P _D	1.15		W		
Maximum Power Dissipation ^{a, b}	T _A = 70 °C	' D	0.73				
Operating Junction and Storage Temperature Range		T _J , T _{stq}	- 55 to 150		°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Naccione le matica de Ambienta	t ≤ 5 s	R _{thJA}	93	110	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		130	150		
Maximum Junction-to-Lead	Steady State	R_{thJL}	75	90		

a. Surface Mounted on FR4 board.

b. $t \le 5$ s.



SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions		Min.	Тур.	Max.	Unit	
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	N-Ch	0.6			V	
		$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$ P-C		- 0.7			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	N-Ch			± 100	nA	
Cate Body Leakage	.655		P-Ch			± 100	11//	
	I _{DSS}	V _{DS} = 24 V, V _{GS} = 0 V	N-Ch			1	- μΑ	
Zero Gate Voltage Drain Current		V _{DS} = - 24 V, V _{GS} = 0 V				- 1		
	200	$V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	N-Ch			5	F	
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$	P-Ch			- 5		
On-State Drain Current ^a	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	N-Ch	3.7			A	
On State Brain Surrent	·D(011)	V _{DS} = - 5 V, V _{GS} = - 10 V	P-Ch	- 3			, ,	
		$V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$	N-Ch		0.022 0.024			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 1.8 A	P-Ch		0.055	0.069	Ω	
Diam-Source On-State Resistance	. (DS(on)	$V_{GS} = 4.5 \text{ V}, I_D = 2.0 \text{ A}$	N-Ch		0.030	0.036	32	
		$V_{GS} = -4.5 \text{ V}, I_D = -1.2 \text{ A}$	P-Ch		0.079	0.083		
Forward Transconductance ^a	α.	$V_{DS} = 10 \text{ V}, I_{D} = 2.5 \text{ A}$	N-Ch		4.3		S	
Forward Transconductance	9 _{fs}	V _{DS} = - 15 V, I _D = - 1.8 A	P-Ch		2.4		5	
Diada Farruard Valtage	V _{SD}	I _S = 1.05 A, V _{GS} = 0 V	N-Ch		0.81	1.10	V	
Diode Forward Voltage ^a		I _S = - 1.05 A, V _{GS} = 0 V	P-Ch		- 0.83 - 1.10		V	
Dynamic ^b								
Total Gate Charge	Q_{g}	N. Channal	N-Ch		2.1	3.2		
Total Gate Gridige	₩g	N-Channel $V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 1.8 \text{ A}$	P-Ch		2.4	3.6	nC	
Gate-Source Charge	Q_{gs}	V _{DS} = 10 V, V _{GS} = 0 V, I _D = 1.0 / V	N-Ch		0.7			
	9-	P-Channel	P-Ch N-Ch		0.9			
Gate-Drain Charge	Q_{gd}	$V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -1.8 \text{ A}$	P-Ch		0.7			
			N-Ch	0.5	0.0	2.4		
Gate Resistance	R_g		P-Ch	3		11	Ω	
Turn On Delay Time	t _{d(on)}		N-Ch		7	11		
Turn-On Delay Time		N-Channel	P-Ch		8	12		
Rise Time		$V_{DD} = 15 \text{ V}, R_L = 15 \Omega$ $I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_\alpha = 6 \Omega$	N-Ch		9	14		
Nise Time		$ID = IA$, $VGEN = IOV$, $N_g = 0.52$	P-Ch		12	18		
Turn-Off Delay Time	t _{d(off)}	P-Channel	N-Ch		13	20	ns	
		$V_{DD} = -15 \text{ V}, R_{L} = 15 \Omega$	P-Ch		12	18		
Fall Time		$I_D \cong$ - 1 A, V_{GEN} = - 10 V, R_g = 6 Ω	N-Ch		5	8		
		P-Ch I _F = 1.05 A, dl/dt = 100 A/μs N-Ch			7 35	11 60	-	
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = 1.05 \text{ A}, \text{ dl/dt} = 100 \text{ A/µs}$ $I_F = -1.05 \text{ A}, \text{ dl/dt} = 100 \text{ A/µs}$	N-Ch P-Ch		30	60		
		i _F = - 1.05 A, αί/αι = 100 A/μS	P-Cn		30	00		

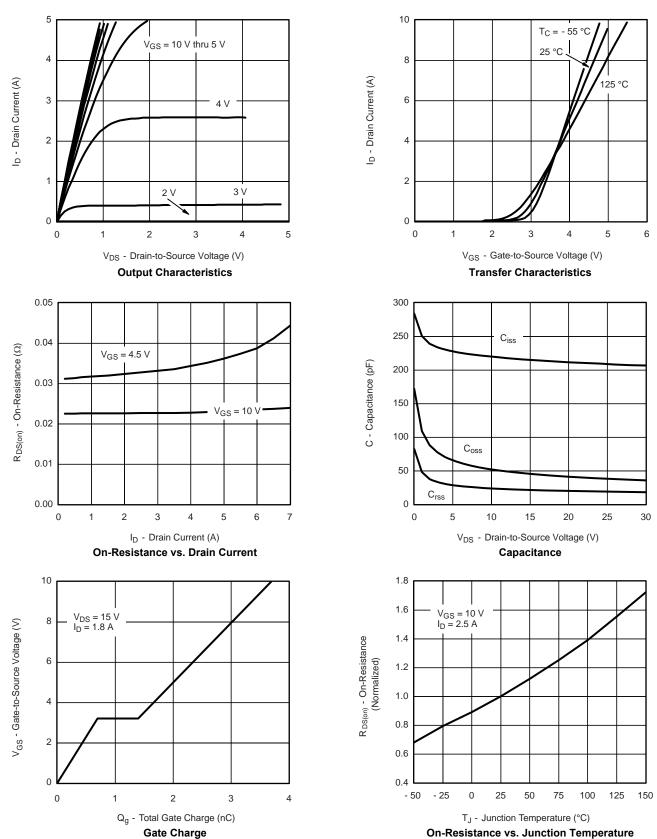
Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.

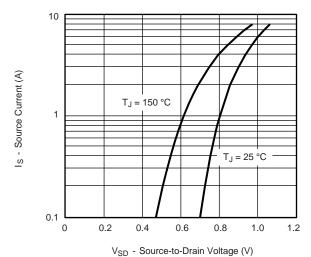


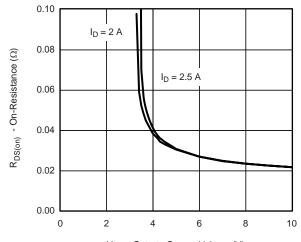
N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



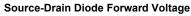


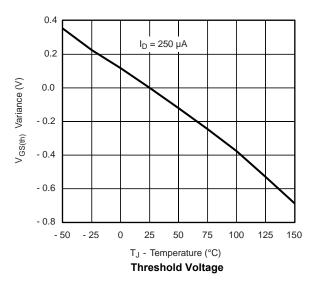
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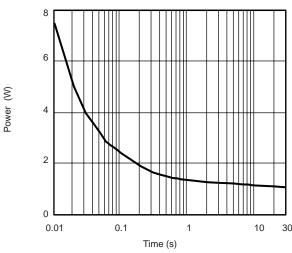


V_{GS} - Gate-to-Source Voltage (V)

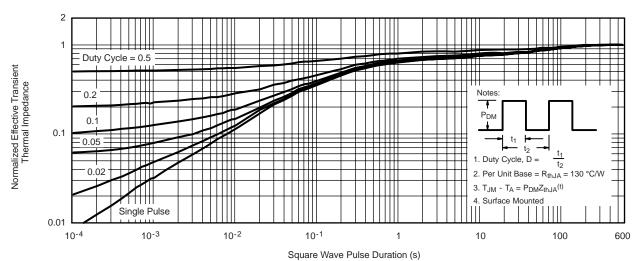




On-Resistance vs. Gate-to-Source Voltage



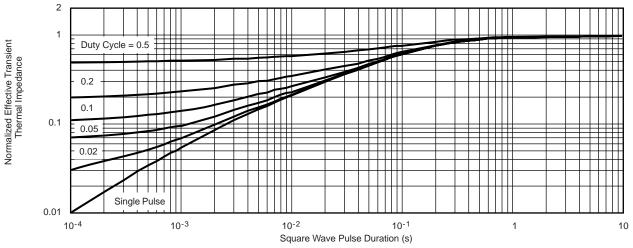
Single Pulse Power (Junction-to-Ambient)



Normalized Thermal Transient Impedance, Junction-to-Ambient

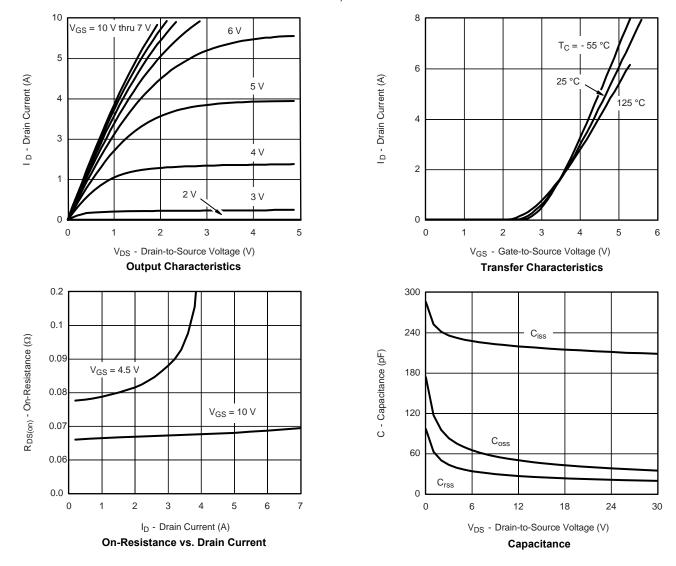


N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



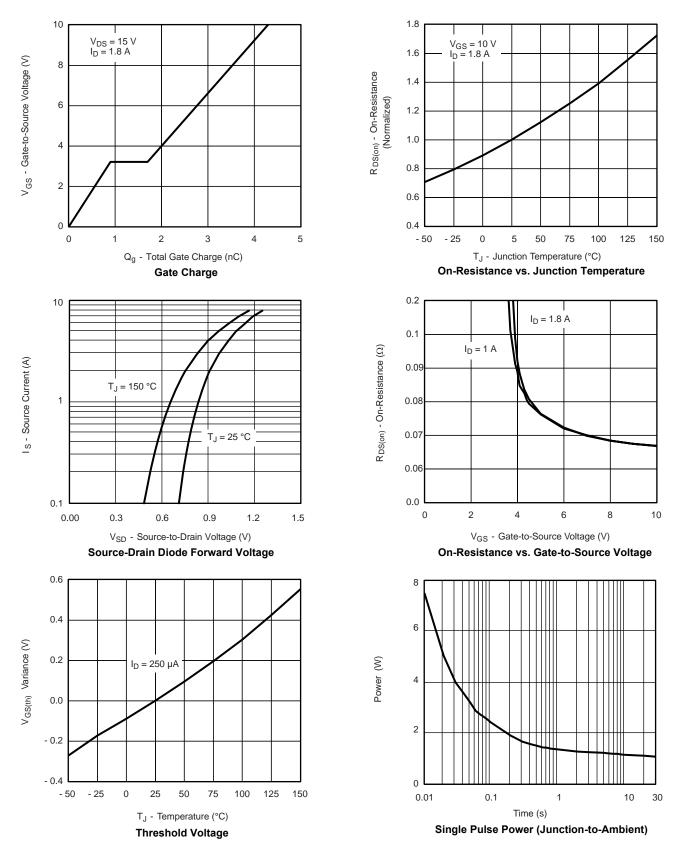
Normalized Thermal Transient Impedance, Junction-to-Foot

P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



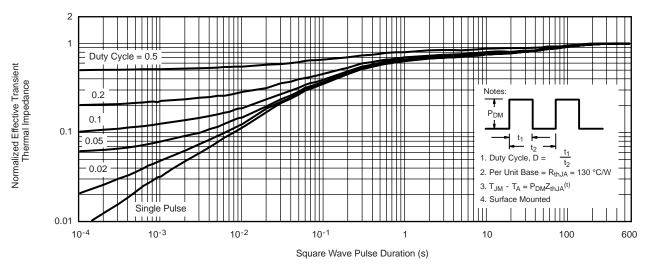


P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

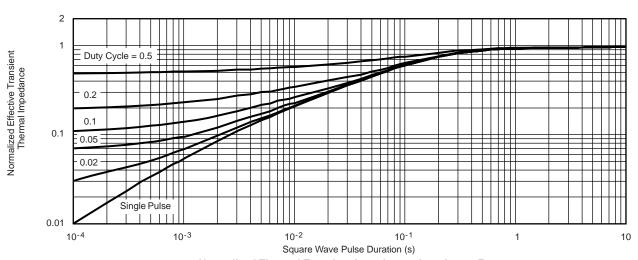




P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

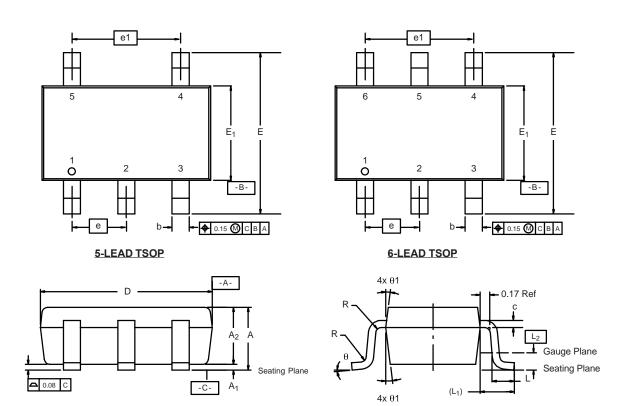


Normalized Thermal Transient Impedance, Junction-to-Foot



TSOP: 5/6-LEAD

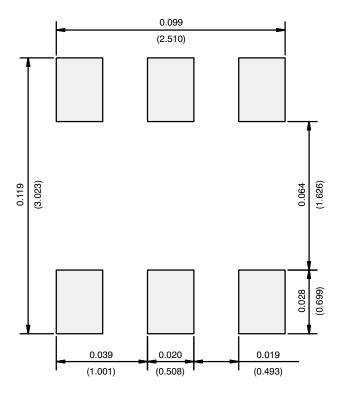
JEDEC Part Number: MO-193C



	MILLIMETERS			INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
Е	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁	0.60 Ref			0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							



RECOMMENDED MINIMUM PADS FOR TSOP-6



Recommended Minimum Pads Dimensions in Inches/(mm)

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