

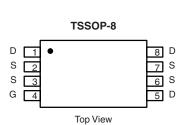
# P-Channel 20-V (G-S) MOSFET

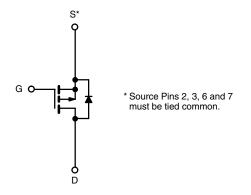
PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
	$0.012$ at $V_{GS} = -4.5 \text{ V}$	- 9.0			
-20	0.015 at V <sub>GS</sub> = - 2.5 V	- 7.8			
	0.020 at V <sub>GS</sub> = - 1.8 V	- 6.0			

#### **FEATURES**

- Halogen-free
- TrenchFET® Power MOSFETs







P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS	T <sub>A</sub> = 25 °C, unles	s otherwise n	oted			
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	-20			
Gate-Source Voltage		$V_{GS}$	± 12		V	
Continuous Dusin Comment /T 150 °C\2	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 9.0	-7.8	^	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 6.8	-5.8		
Pulsed Drain Current (10 μs Pulse Width)		I <sub>DM</sub>	- 30		Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.35	- 0.95		
M	T <sub>A</sub> = 25 °C	В	1.5	1.05	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C	$P_{D}$	1.0	0.67	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian and Lucation to Ambient	t ≤ 10 s	R <sub>thJA</sub>	65	83	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State	' 'thJA	100	120	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	43	52	

Notes: a. Surface Mounted on 1" x 1" FR4 board.

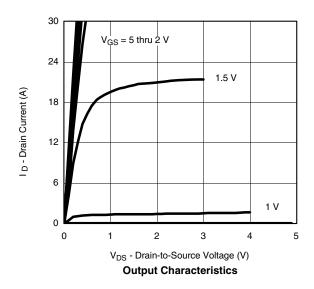


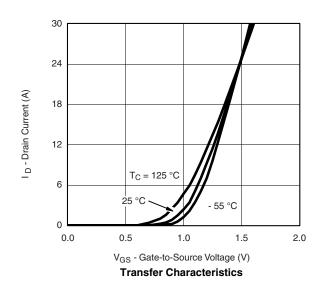
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol			Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -450 \mu A$		-	1.0	V	
Gate-Body Leakage	I <sub>GSS</sub>				± 100	nA	
Zara Cata Valtaga Drain Current	1	V <sub>DS</sub> = - 20 V, V <sub>GS</sub> = 0 V			- 1	- 1 - 25 μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C			- 25		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 4.5 V	- 20			Α	
		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8.0 A		0.010		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 7.0 A		0.012			
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 5.8 A		0.016			
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 5 V, I <sub>D</sub> = - 8.0 A		44		S	
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.5 A, V <sub>GS</sub> = 0 V		- 0.56	- 1.1	V	
Dynamic <sup>b</sup>							
Total Gate Charge	$Q_g$			46	70		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -8.0 \text{ A}$		5		nC	
Gate-Drain Charge	$Q_{gd}$			15.5		1	
Turn-On Delay Time	t <sub>d(on)</sub>			45	70		
Rise Time	t <sub>r</sub>	V <sub>DD</sub> = - 10 V, R= 6 Ω		85	130	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$		220	400		
Fall Time	t <sub>f</sub>			155	235		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.5 A, di/dt = 100 A/μs		140	210		

- Notes: a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %. b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





0.1 -

0.2

0.4

0.6

 $V_{\mbox{SD}}$  - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

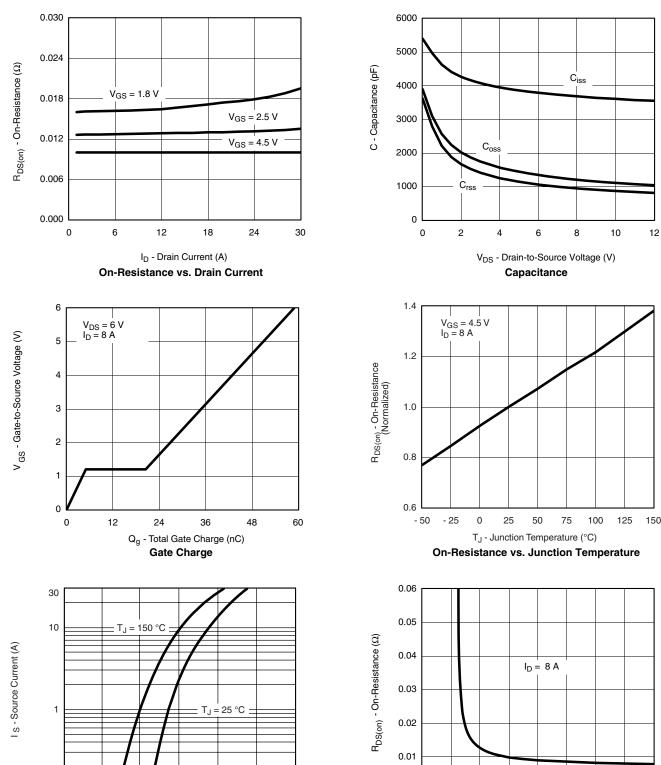
0.8

1.0

1.2



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



 $\label{eq:VGS} \mbox{V}_{GS} \mbox{ - Gate-to-Source Voltage} \mbox{ (V)} \\ \mbox{On-Resistance vs. Gate-to-Source Voltage}$ 

6

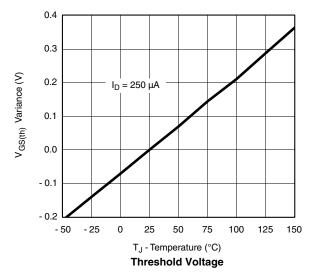
0.00

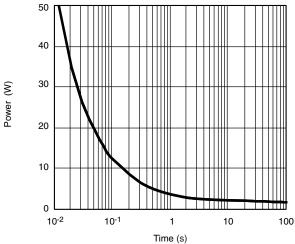
0

2

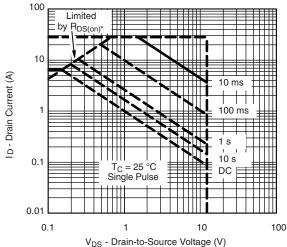


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



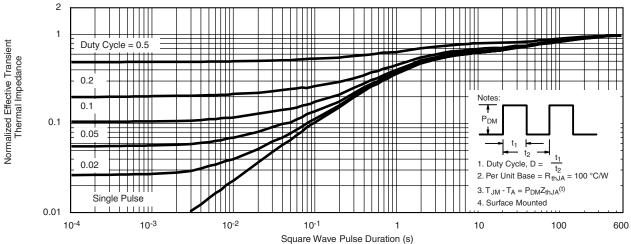


Single Pulse Power, Junction-to-Ambient



\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

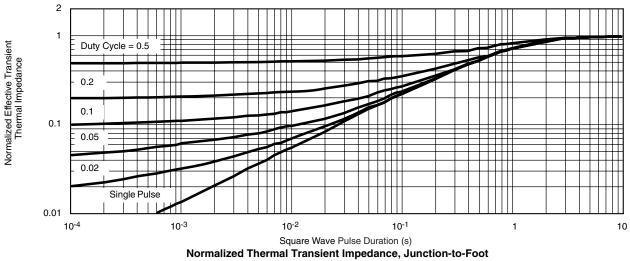
#### Safe Operating Area, Junction-to-Case



Normalized Thermal Transient Impedance, Junction-to-Ambient

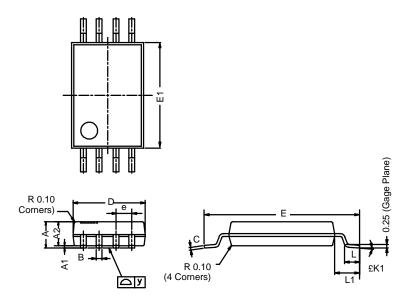


### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



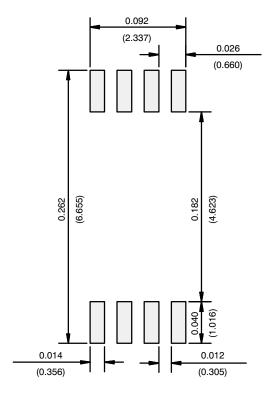
TSSOP: 8-LEAD

JEDEC Part Number: MO-153



	MILLIMETERS					
Dim	Min Nom		Max			
Α	-	-	1.20			
A <sub>1</sub>	0.05	0.10	0.15			
A <sub>2</sub>	0.80	1.00	1.05			
В	0.19	0.28	0.30			
С	-	0.127	-			
D	2.90	3.00	3.10			
E	6.20	6.40	6.60			
E <sub>1</sub>	4.30	4.40	4.50			
е	_	0.65	_			
L	0.45	0.60	0.75			
L <sub>1</sub>	0.90	1.00	1.10			
Υ	-	-	0.10			
£ <b>K1</b>	0°	3°	6°			
ECN: S-03946—Rev. G, 09-Jul-01 DWG: 5844						

### **RECOMMENDED MINIMUM PADS FOR TSSOP-8**



Recommended Minimum Pads Dimensions in Inches/(mm)



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