

## P-Channel 150-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}$ ( $\Omega$ )	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
- 150	$0.065 \text{ at V}_{GS} = -10 \text{ V}$	- 22	7.6 nC			
	$0.07 \ 0 \ \text{at V}_{GS} = -4.5 \ \text{V}$	- 20	7.0110			

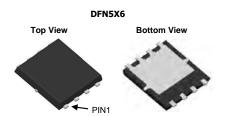
#### **FEATURES**

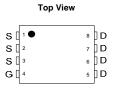
- TrenchFET® Power MOSFET
- 100 % UIS Tested

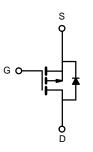
#### **APPLICATIONS**

Load Switch









P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATING</b>	<b>S</b> (T <sub>A</sub> = 25 °C, unle	ess otherwise no	ted)		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V <sub>DS</sub>	- 150	V		
Gate-Source Voltage	$V_{GS}$	± 20	v		
	T <sub>C</sub> = 25 °C		- 22 <sup>a</sup>		
Continuous Proin Current (T. – 150 °C)	T <sub>C</sub> = 70 °C		- 18		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	20		
	T <sub>A</sub> = 70 °C		- 18.1 <sup>b</sup>	A	
Pulsed Drain Current	I <sub>DM</sub>	- 45			
Avalanche Current Pulse	L = 0.1 mH	I <sub>AS</sub>	- 12		
Single Pulse Avalanche Energy	L = 0.1 mn	E <sub>AS</sub>	12	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	I.	7.5 <sup>a</sup>	^	
Continuous Source-Diain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	2.4 <sup>b</sup>	_ A	
	T <sub>C</sub> = 25 °C		10 <sup>a</sup>		
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	7.0 <sup>a</sup>	w	
	T <sub>A</sub> = 25 °C	-D	1.3 <sup>b</sup>	VV	
	T <sub>A</sub> = 70 °C		2 <sup>b</sup>		
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C		

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	$R_{thJA}$	33	40	°C/W		
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.98	1.2	- C/VV		

#### Notes:

- a. Based on  $T_C$  = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.



Parameter	Symbol	nbol Test Conditions		Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$				V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Γ <sub>D</sub> = - 250 μA		68		mV/°C
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	1 <sub>D</sub> = - 250 μΑ		- 5.2		IIIV/
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.5		- 3	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zara Cata Valtaga Prain Current	1	V <sub>DS</sub> = - 100 V, V <sub>GS</sub> = 0 V			- 1	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = - 120 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 10	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 10 V	- 20			Α
D : 0	В	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 5 A		0.065		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 5 A		0.070		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 15 V, I <sub>D</sub> = - 5 A	20			S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			5000		pF
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		390		
Reverse Transfer Capacitance	C <sub>rss</sub>			290		
Total Cata Charge	Qg	$V_{DS} = -30 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -5 \text{ A}$		76	115	nC
Total Gate Charge				38	60	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -30 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5 \text{ A}$		16		
Gate-Drain Charge	$Q_{gd}$			19		
Gate Resistance	$R_g$	f = 1 MHz		5.2		Ω
Turn-On Delay Time	t <sub>d(on)</sub>			10	15	
Rise Time	t <sub>r</sub>	$V_{DD} = -2 V, R_L = 2 \Omega$		7	15	- ns
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong -5 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		70	110	
Fall Time	t <sub>f</sub>			40	60	
<b>Drain-Source Body Diode Characteristics</b>	s					
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 6	А
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 15	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A		- 1	- 1.5	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			45	68	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I = 5 A di/dt = 10 A/up T = 25 °C		59	120	nC
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = -5 \text{ A}, \text{ di/dt} = 10 \text{ A/}\mu\text{s}, T_J = 25 °\text{C}$		29		ns
Reverse Recovery Rise Time	t <sub>b</sub>			16		

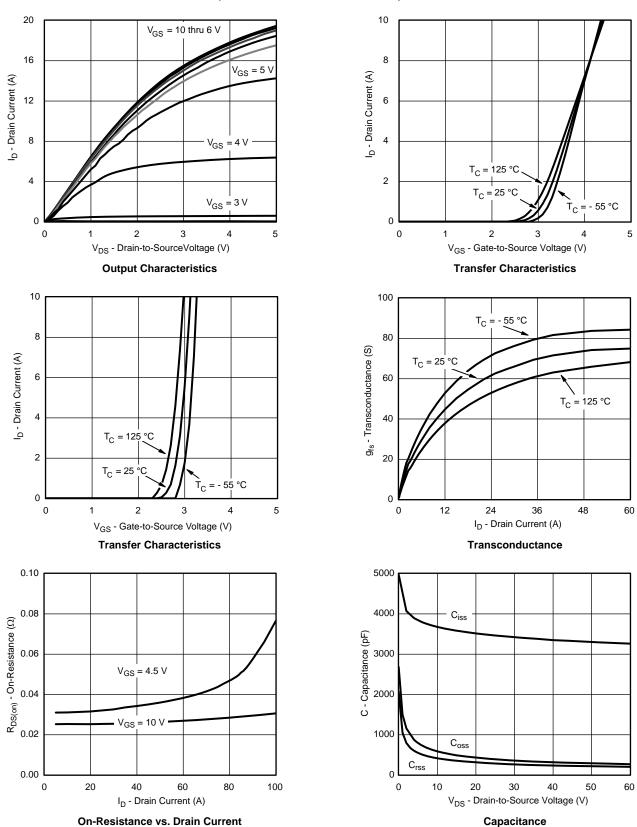
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

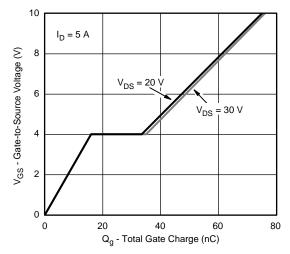


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

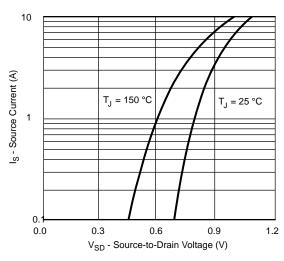




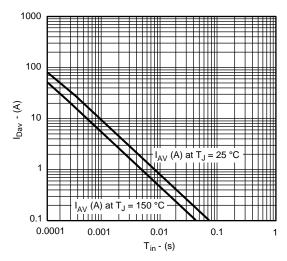
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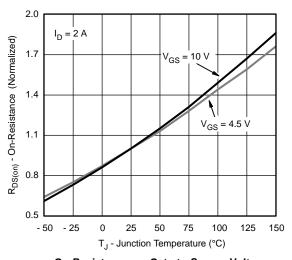




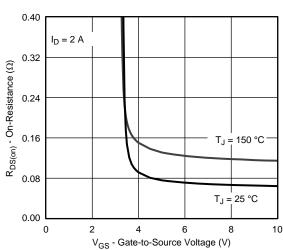
Source-Drain Diode Forward Voltage



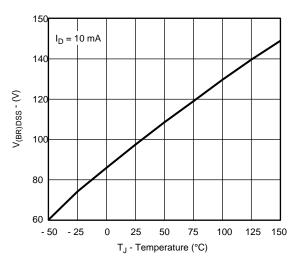
Single Pulse Avalanche Current Capability vs. Time



On-Resistance vs. Gate-to-Source Voltage



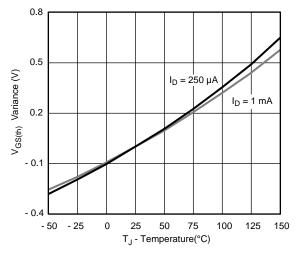
On-Resistance vs. Gate-to-Source Voltage

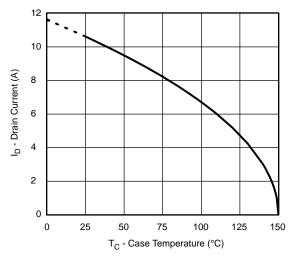


Drain-Source Breakdown Voltage vs. Junction Temperature

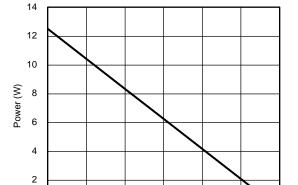


#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

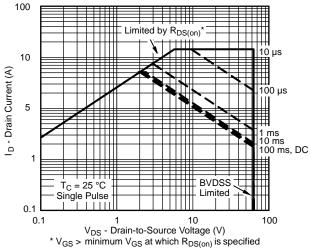




#### Threshold Voltage



Max. Drain Current vs. Case Temperature



# $\label{eq:TJ-Temperature condition} T_J \text{ - Temperature (°C)}$ Power Derating, Junction-to-Case

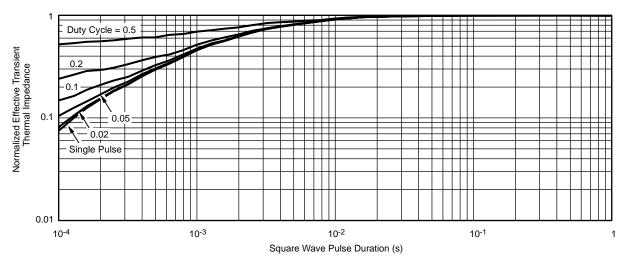
75

100

125

150





Normalized Thermal Transient Impedance, Junction-to-Case

服务热线:400-655-8788

0

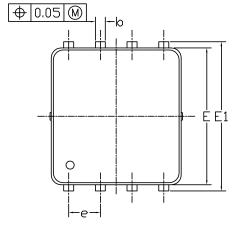
0

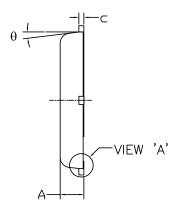
25

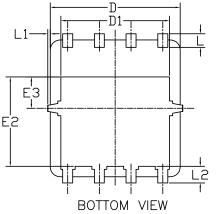
50

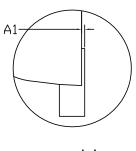


DFN5x6\_8L\_EP1\_P PACKAGE OUTLIN



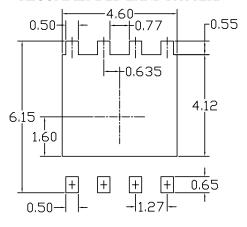






<u>VIEW 'A'</u> (SCALE 5:1)

#### RECOMMENDED LAND PATTERN



GVA (DOLG	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES			
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX	
A	0.85	0. 95	1.00	0.033	0.037	0.039	
A1	0.00		0.05	0.000		0.002	
b	0.30	0.40	0.50	0.012	0.016	0.020	
c	0. 15	0. 20	0. 25	0.006	0.008	0.010	
D	5. 10	5. 20	5. 30	0. 201	0. 205	0. 209	
D1	4. 25	4. 35	4. 45	0. 167	0.171	0. 175	
Е	5. 45	5. 55	5. 65	0. 215	0. 219	0. 222	
E1	5. 95	6.05	6. 15	0. 234	0. 238	0. 242	
E2	3. 525	3.625	3. 725	0.139	0. 143	0. 147	
E3	1. 175	1.275	1.375	0.046	0.050	0.054	
e	1. 27 BSC			0.050 BSC			
L	0.45	0. 55	0.65	0.018	0.022	0.026	
L1	0		0. 15	0		0.006	
L2	0.68 REF			0. 027 REF			
θ	0°		10°	0°		10°	

#### **NOTE**

- UNIT: mm
- 1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS. MOLD FLASH AT THE NON-LEAD SIDES SHOULD BE LESS THAN 6 MILS EACH.
- 2. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSIONS ARE NOT NECESSARILY EXACT.



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TK16J60W,S1VQ(O 2SK2614(TE16L1,Q) DMN1017UCP3-7 DMN1053UCP4-7 SQJ469EP-T1-GE3 NTE2384 DMC2700UDMQ-7
DMN2080UCB4-7 DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 DMN2990UFB-7B
IPB80P04P405ATMA2 2N7002W-G MCAC30N06Y-TP MCQ7328-TP NTMC083NP10M5L BXP7N65D BXP4N65F AOL1454G
WMJ80N60C4 BXP2N20L BXP2N65D BXT1150N10J BXT1700P06M TSM60NB380CP ROG RQ7L055BGTCR DMNH15H110SK3-13
SLF10N65ABV2 BSO203SP BSO211P IPA60R230P6