

N- and P-Channel 20 V (D-S) MOSFET

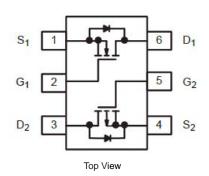
PRODUCT SUMMARY					
	V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
N-Channel	20	0.270 at V _{GS} = 4.5 V	0.60		
N-Channel		0.410 at V _{GS} = 2.5 V	0.55		
P-Channel	- 20	0.840 at V _{GS} = - 4.5 V	- 0.30		
F-Channel	- 20	0.660 at V _{GS} = - 2.5 V	- 0.25		

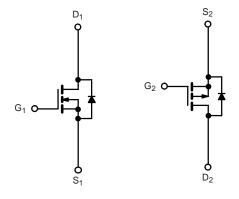
FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g Tested
 Compliant to RoHS Directive 2002/95/EC









N-Channel MOSFET

P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	N-Channel	P-Channel	Unit	
Drain-Source Voltage		V _{DS}	20	- 20	V	
Gate-Source Voltage		V_{GS}	± 20	± 20		
0 // D : 0 //T // // // h	T _A = 25 °C	- I _D	0.6	- 0.3		
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C		0.55	- 0.25		
Pulsed Drain Current		I _{DM}	3	- 2	A	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	1.05	- 1.05		
	T _A = 25 °C	P _D	1.15		W	
Maximum Power Dissipation ^{a, b}	T _A = 70 °C	r D	0.73			
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maniana Indiana da Andria da	t ≤ 5 s	R _{thJA}	93	110	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		130	150		
Maximum Junction-to-Lead	Steady State	R _{thJL}	75	90		

a. Surface Mounted on FR4 board.

b. $t \le 5$ s.



Parameter	Symbol	Test Conditions		Min.	Тур.	Max.	Unit	
Static	•				l			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	N-Ch	0.7			V	
		V _{DS} = V _{GS} , I _D = - 250 μA	P-Ch	- 0.8			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	N-Ch			± 100	nA	
			P-Ch			± 100		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24 V, V _{GS} = 0 V	N-Ch			1		
		$V_{DS} = -24 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 24 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 55 \text{ °C}$ N:				- 1	μA	
						5		
		V _{DS} = - 24 V, V _{GS} = 0 V, T _J = 55 °C	P-Ch			- 5		
On-State Drain Current ^a	1	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	N-Ch	3.7			А	
	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -10 \text{ V}$	P-Ch	- 3				
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 0.6 \text{ A}$	N-Ch			0.410		
		V _{GS} = - 2.5 V, I _D = - 0.3A				0.840		
		$V_{GS} = 4.5 \text{ V}, I_D = 0.6 \text{A}$	N-Ch			0.270	Ω	
		V _{GS} = - 4.5 V, I _D = - 0.3 A	P-Ch			0.660		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 10 V, I _D = 2.5 A	N-Ch		4.3			
		V _{DS} = - 15 V, I _D = - 1.8 A	P-Ch		2.4		S	
Diode Forward Voltage ^a	V _{SD}	I _S = 1.05 A, V _{GS} = 0 V	N-Ch		0.81	1.10	.,	
		I _S = - 1.05 A, V _{GS} = 0 V	P-Ch		- 0.83	- 1.10	V	
Dynamic ^b								
Total Gate Charge	0		N-Ch		2.1	3.2		
Total Gate Charge	Qg	N-Channel	P-Ch		2.4	3.6	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 15 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 1.8 \text{ A}$	N-Ch		0.7			
Cate Course Charge	₩gs	P-Channel	P-Ch		0.9			
Gate-Drain Charge	Q _{qd}	$V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_{D} = -1.8 \text{ A}$			0.7			
	94		P-Ch	0.5	0.8	0.4		
Gate Resistance	R_g		N-Ch P-Ch	0.5 3		2.4 11	Ω	
			N-Ch	<u> </u>	7	11	 	
Turn-On Delay Time Rise Time	t _{d(on)}	N-Channel	P-Ch		8	12		
		$V_{DD} = 15 \text{ V}, R_{L} = 15 \Omega$	N-Ch		9	14		
		$I_D \cong 1 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 6 \Omega$	P-Ch		12	18		
Turn-Off Delay Time	t _{d(off)}	- P-Channel	N-Ch		13	20	1	
		$V_{DD} = -15 \text{ V}, R_L = 15 \Omega$	P-Ch		12	18	ns	
Fall Time	t _f	$I_D \cong -1$ A, $V_{GEN} = -10$ V, $R_g = 6$ Ω	N-Ch		5	8		
		-	P-Ch		7	11		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 1.05 A, dI/dt = 100 A/μs N-Ch		<u> </u>	35	60		
		I _F = - 1.05 A, dI/dt = 100 A/μs	P-Ch		30	60		

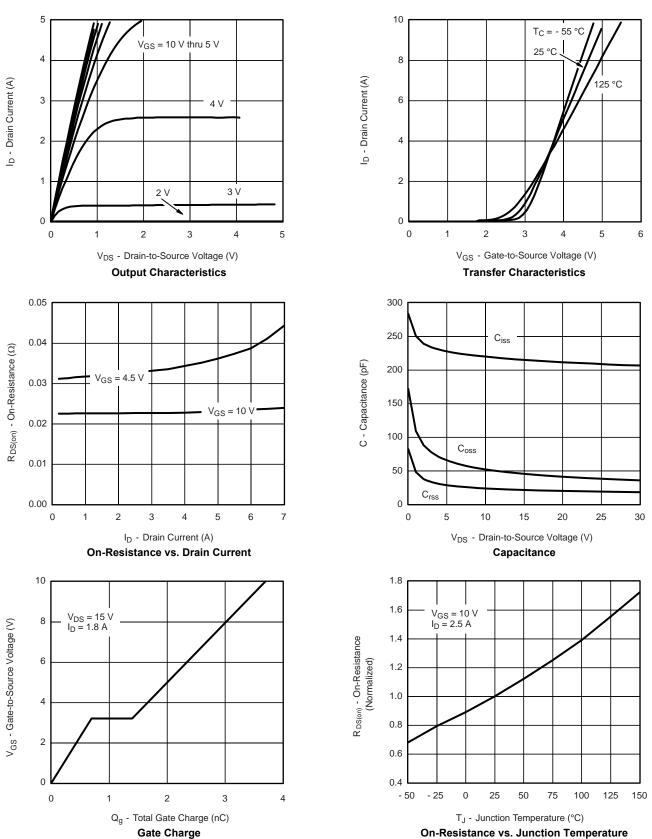
Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.



N-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

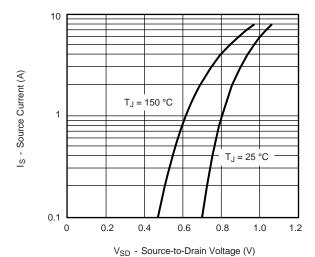




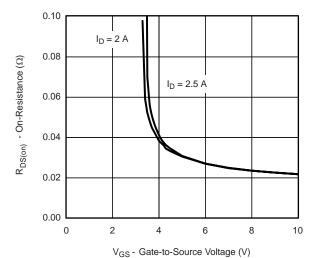
600

100

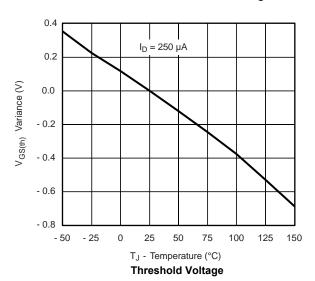
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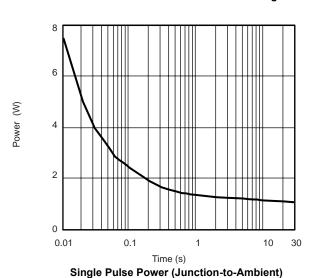


Source-Drain Diode Forward Voltage

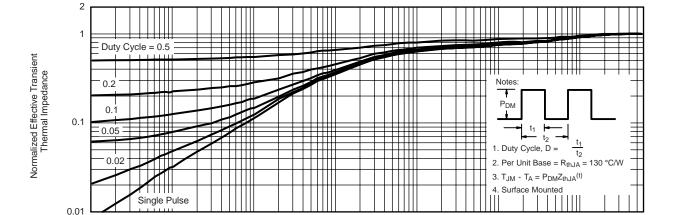


On-Resistance vs. Gate-to-Source Voltage





10



10-1

10-2

Square Wave Pulse Duration (s)

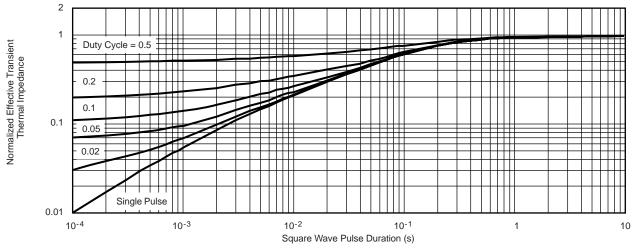
Normalized Thermal Transient Impedance, Junction-to-Ambient

10-4

10-3

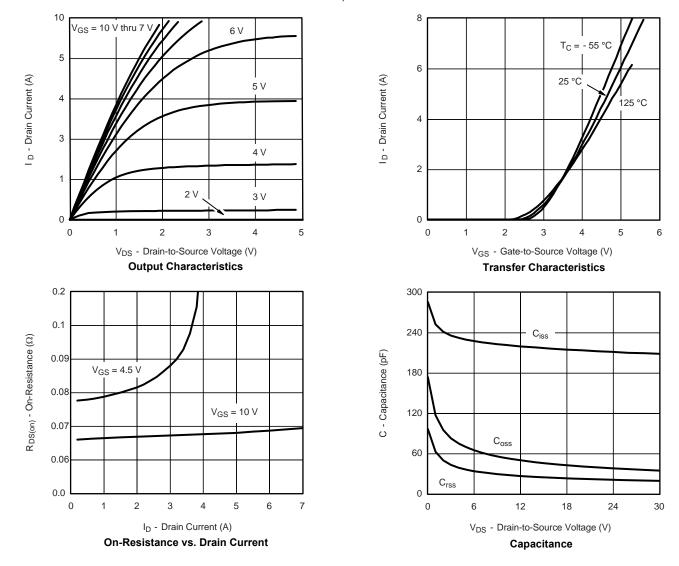


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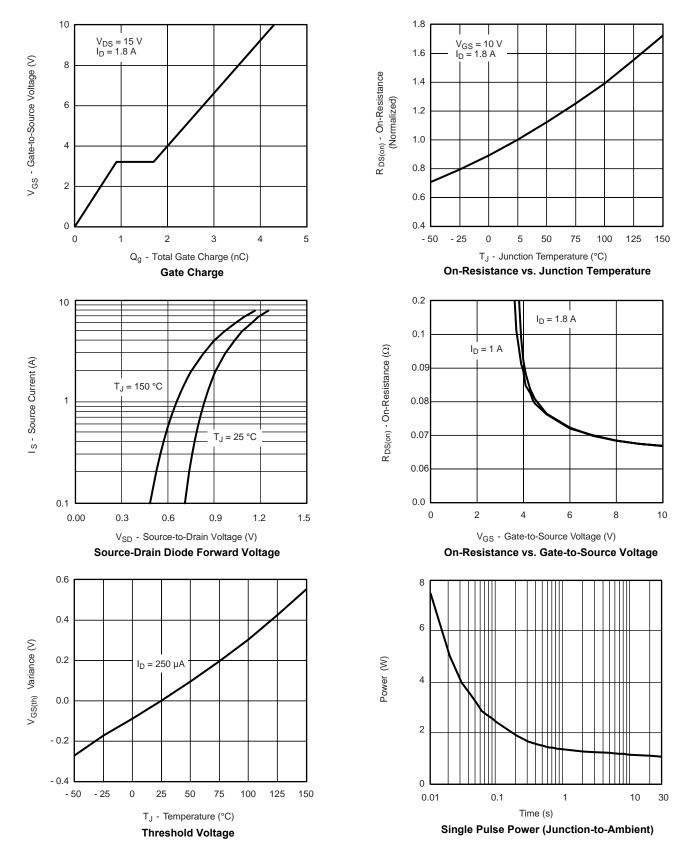
Normalized Thermal Transient Impedance, Junction-to-Foot

P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



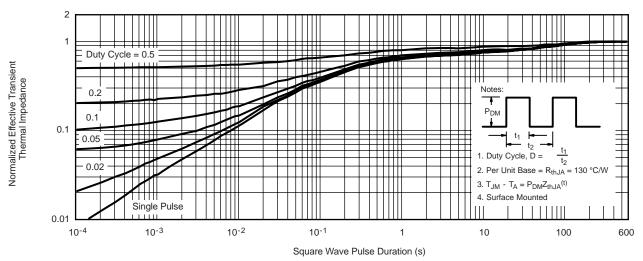


P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

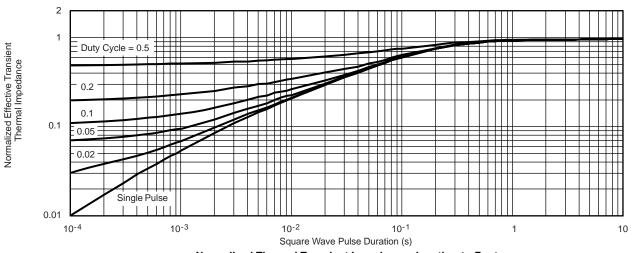




P-CHANNEL TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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