

Low-Noise Bottom Port Piezoelectric MEMS Microphone With Wake on Sound Feature

# Data Sheet

### VM1010

The VM1010 is the world's first ZeroPower Listening<sup>™</sup> piezoelectric MEMS microphone. It provides an ultra-low power always listening solution, bringing voice activation to battery powered consumer devices.

#### Features:

- ZeroPower Listening Technology
- Full audio output upon waking
- Capable of voice detection with just 18  $\mu$ W operation
- Extends battery life up to 10x
- Dust resistant to IP5x

The VM1010 is a low-noise single-ended analog MEMS microphone with a Wake on Sound mode that allows detection of voice activity while consuming only 10  $\mu$ A of supply current (or 18 μW of power). In this *Wake on Sound* mode, the microphone detects sounds in the voice band above a configurable acoustic threshold level. When the microphone detects a sound above the threshold, it instantly alerts the system of the acoustic event. The system can then switch the VM1010 to Normal mode with full audio output within 200 µSec. This is fast enough for the VM1010 to capture the sound that exceeds the threshold and send it to the system for processing. This creates the system architecture for ZeroPower Listening.



The VM1010's *Wake on Sound* mode brings voice activation to battery-powered consumer devices — while drawing nearly zero power, making it ideal for voice-interface devices such as smart speakers, TV remotes, smart earbuds, and smart home products.

The VM1010 has a small 3.76 mm X 2.95 mm X 1.3 mm package and is reflow solder-compatible with no sensitivity degradation. It operates in environmentally harsh surroundings because it is dust- and moisture-resistant.

## Specifications

All specifications are at 25°C,  $V_{DD}$  = 1.8 V unless otherwise noted. Valid for Date Code 1811 and higher.

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units

Sensitivity		1 kHz. 94 dB	-41	-38	-35	dBV
,		SPL				
Signal-to-Noise	SNR	94 dB SPL at 1		60		dB(A)
Ratio		kHz signal				
		20 Hz – 20				
		kHz, A-				
		weighted				
		Noise				
Signal-to Noise	SNR	94 dB SPL at 1		62		dB(A)
Ratio Voice		kHz signal				
Band		20 Hz – 8 kHz				
		A-weighted				
Total Harmonic	THD	94 dB SPL		0.1		%
Distortion						
Acoustic	AOP	10.0% THD		125		dB SPL
Overload Point						

#### Normal Mode Acoustic Specifications

Roll Off Frequency	-3db at 1KHz	100	Hz
Directivity		Omni	

## Wake on Sound Acoustic Specifications

Specifications valid on material Date Code 1811 (lid marking A811..) and higher.

			1	0	/	0
Max	PaTH	Rg= Open		89		SPL
Acoustic						
Threshold						
(default)						
Mid	PaTH	Rg= 90kOhm		77		SPL
Threshold						
Min Acoustic	PaTH	Rg= 18kOhm	2	65		SPL
Threshold						

Note: Rg is the resistance configured by user between GA1 and GA2 pins. A 1% tolerance resistor is recommended.

Previous material 1810 and lower have a different range and resistor values and are now obsolete.

	Поп	Har Mode Electric	ai opeci	neations		
Supply	VDD		1.6	1.8	3.6	V
Voltage						
Supply		$V_{DD} \le 1.8 V$ , Mode		83	100	μA
Current		Normal				
Power	PSRR	VDD = 1.8, 1kHz,		56		dB
Supply		200mV <sub>PP</sub> Sine				
Rejection		wave				
Ratio						
Power	PSR	VDD = 1.8, 217Hz,		-87		dB(A)
Supply		$100 \text{mV}_{PP}$ square				
Rejection		wave, 20 Hz –				
		20kHz. A-				
		weighted				
		Weighted				
					1	

# Normal Mode Electrical Specifications

Output Impedance	Zout		1	kΩ
Output DC Offset			0.75	V

# Wake on Sound Mode Electrical Specifications

Supply Voltage	VDD		1.6	1.8	3.6	V
Supply Current		V <sub>DD</sub> ≤ 1.8 V, Mode WoS	7	10	14	μΑ

# Digital Electrical Interface Specifications

Logic Input High		0.65*VDD		3.6	V
Logic Input Low		-0.3		0.35*VDD	V
Logic Output High	I <sub>Load</sub> = 2mA	0.7*VDD	VDD		V
Logic Output Low	I <sub>Load</sub> = 2mA		0	0.3*VDD	>
Driving Capability		Μ		100	pF

# Absolute Maximum Ratings

Parameter	Maximum	Units
Supply Voltage	3.6	V
Sound Pressure Level	160	dB re 20 µPa
Operating Temperature Range	-40 to +85	°C
Storage Temperature Range	-50 to +150	°C

# Typical Performance Characteristics



Figure 1. Frequency Response (Normal Mode)



Figure 3. THD+N vs Amplitude (Normal Mode) (Normal Mode)









## Environmental Robustness

IP adherence is evaluated by 1kHz Sensitivity spec post stress

Ingress Protection Type	Description
Dust Resistance	IP5X
Water Immersion	IPX7; 2hrs drying time, dry environment

#### Microphone Modes

mode pin	Mic mode	I <sub>dd</sub> , typ. (μΑ)	V <sub>out</sub> pin	d <sub>out</sub> pin
high	Wake on Sound	10	GND	low, then latches high
				after first wake-up event
low or	Normal	83	audio	Tied to GND through low
floating			output	impedance

Table 2: Effect of mode pin on microphone mode, supply current & output pins

## Microphone Operation

The VM1010 is a user-controlled multi-mode microphone. Through the single-bit digital input pin *mode*, the user provides a signal that will put the microphone in the *Wake on Sound* or *Normal* mode of operation. When in the *Wake on Sound* mode, should a sound be detected, the microphone will output a digital high through the *dout* pin. The user can then switch the microphone to *Normal* mode by providing a digital low to the *mode* pin. The user can at some later point provide a digital high to the *mode* pin to return the microphone to *Wake on Sound* mode. Figure 5 shows the states of various pins that are either user-provided (*mode, Vdd*) or VM1010-provided (*dout, Vout*) at three points of operation.



Figure 5: Timing diagrams showing turn-on time (left), transition time into Wake on Sound mode (center) & Normal mode (right)

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We recommend powering up the microphone in *Normal* mode (*mode* pin low), and then entering *Wake on Sound* mode as needed.

## System Architecture

Control loop for Wake on Sound:



Figure 6 above shows a full-featured system built around the VM1010. The VM1010 wakes up the system when triggered by sound. The DSP or voice processor can be kept in a low-power state when there is no sound to process.

## Solder Reflow Profile



#### Handling Instructions

Vesper's piezoelectric MEMS microphones are very resistant to harsh environments such as dust and moisture. However, to avoid mechanical damage to the microphone, we recommend using appropriate handling procedures when manually handling the parts or when using pick and place equipment. The following guidelines will avoid damage:

- Do not apply a vacuum to the bottom side of the microphone. A vacuum pen may be used with care on the top side only.
- Do not apply very high air pressure over the port hole.
- Do not insert any large particles or objects in the port hole. The microphone is resistant to small particles per IP5x specification.
- Do not board wash or clean after the reflow process or expose the acoustic port to harsh chemicals.

# Dimensions and Pin Layout

#### All dimensions are in mm



Figure 8: Dimensions and Pin layout

Pin Number	Pin Name	Description		
1	$V_{out}$	Analog Output Voltage		
2	GA2	Wake-on-Sound Acoustic Threshold Adjust pin 2		
3	GA1	Wake-on-Sound Acoustic Threshold Adjust pin 1		
4	GND	Ground		
5	Mode	Mode control (hi=Wake-on-Sound, lo=Normal-		
		Power)		
6	V <sub>dd</sub>	Power Supply (1.6V to 3.6V)		
7	d <sub>out</sub>	Digital output for Wake-on-Sound trigger		
8	GND	Ground		

#### PCB Design Layout

All dimensions are in mm



Figure 11: Tape and Reel Specification

# Ordering Guide

Product	Package Description	Ordering Quantity
VM1010-U7-R13	13" Tape and Reel	5,000

## Lid Marking



Figure 11. Lid Marking Description

### Engineering Samples

Engineering samples may vary from the technical specifications contained in this data sheet. They are not intended for use in end products and are intended for client evaluation and testing of the product. The test of an engineering sample will not reflect the testing of a final product. Material identified as engineering samples are provided without any guarantees or warranty. In association with engineering samples, Vesper makes no warranties of any kind, either express or implied, including but not limited to warranties of merchantability, fitness for a particular purpose, of title, or of noninfringement of third-party rights implied.

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