

Hyperfast Rectifier, 8 A FRED Pt®





VS-8ETH06FP-N3

PRIMARY CHARACTERISTICS					
I _{F(AV)}	8 A				
V _R	600 V				
V _F at I _F	1.3 V				
t _{rr} typ.	18 ns				
T _J max.	175 °C				
Package	2L TO-220 FullPAK				
Circuit configuration	Single				

FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- UL pending
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Repetitive peak reverse voltage	V _{RRM}		600	V
Average rectified forward current	I _{F(AV)}	T _C = 108 °C	8	
Non-repetitive peak surge current	I _{FSM}		100	А
Repetitive peak forward current	I _{FM}		16	
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	N	
Forward voltage	¥-	I _F = 8 A	-	2.0	2.4	v	
Torward voltage	۷F	I _F = 8 A, T _J = 150 °C	-	1.3	1.8		
Poverse leakage aurrent	I_	$V_R = V_R$ rated	-	0.3	50		
neverse leakage current	١R	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	55	500	μΑ	
Junction capacitance	CT	V _R = 600 V	-	17	-	pF	
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH	

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RoHS COMPLIANT HALOGEN FREE



DYNAMIC RECOVERY CHARACTERISTICS ($T_c = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST	CONDITIONS	MIN.	TYP.	MAX.	UNITS	
		I _F = 1 A, dI _F /dt = 100 A/µs, V _R = 30 V		-	18	22		
Bayerse recovery time	+	$I_F = 8 \text{ A}, dI_F/dt = 100 \text{ A}/\mu \text{s}, V_R = 30 \text{ V}$		-	20	25		
neverse recovery time	۲r	T _J = 25 °C		-	25	-	115	
	T _J = 125 °C		-	40	-	I		
Poak rocovory ourrent	1	T _J = 25 °C T _J = 125 °C	I _F = 8 A dI _F /dt = 200 A/μs V _R = 390 V	-	2.4	-	^	
Feak recovery current	IRRM			-	4.8	-	~	
Poverse recevent charge	0	T _J = 25 °C		-	25	-	nC	
Reverse recovery charge Q _{rr}	T _J = 125 °C		-	120	-	10		
Reverse recovery time	t _{rr}		I _F = 8 A	-	33	-	ns	
Peak recovery current	I _{RRM}	T _J = 125 °C	dI _F /dt = 600 A/µs	-	12	-	A	
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	220	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction-to-case	R _{thJC}		-	3.4	4.3	°C/W	
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70		
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-		
Weight			-	2.0	-	g	
weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking device		Case style 2L TO-220 FullPAK		8ETH	106FP		









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VS-8ETH06FP-N3

Vishay Semiconductors



100

200

10 0



400

500

600

Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage











Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt







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ORDERING INFORMATION TABLE

Device code	VS-	8	Е	т	Н	06	FP	-N3
		2	3	4	5	6	7	8
	1	- Visł	nay Sem	iconduc	ctors pro	oduct		
	2	- Cur	rent rati	ng (8 = 8	3 A)			
	3	- E=	single					
	4	- T=	TO-220	, D ² PAk	(TO-26	63AB)		
	5	- H=	hyperfa	st reco	/ery			
	6	- Volt	age rati	ng (06 =	600 V)			
	7	- FP	= 2L TC	-220 Fu	IIPAK			
	8	- Env	ironmer	ntal digit	:			
		-N3	= halog	en-free,	RoHS-	complia	ant, and	totally

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-8ETH06FP-N3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?96157				
Part marking information	www.vishay.com/doc?95392				



2L TO-220 FullPAK

DIMENSIONS in millimeters







Bottom view





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