

# VS-MURB1620CTHM3, VS-MURB1620CT-1HM3

**Vishay Semiconductors** 

# Ultrafast Rectifier, 2 x 8 A FRED Pt<sup>®</sup>



TO-263AB (D<sup>2</sup>PAK)



Base common cathode Q 2 2

TO-262AA

Anode Anode 1 cathode VS-MURB1620CTHM3

Common cathode VS-MURB1620CT-1HM3

Anode

Anode

PRODUCT SUMMARY						
Package	TO-263AB (D <sup>2</sup> PAK), TO-262AA					
I <sub>F(AV)</sub>	2 x 8 A					
V <sub>R</sub>	200 V					
V <sub>F</sub> at I <sub>F</sub>	0.895 V					
t <sub>rr</sub> (typ)	19 ns					
T <sub>J</sub> max.	175 °C					
Diode variation	Common cathode					

### **FEATURES**

- · Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Meets JESD 201 class 1 whisker test
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### **DESCRIPTION / APPLICATIONS**

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS				
Peak repetitive reverse voltage	V <sub>RRM</sub>		200	V				
Average restified forward surrent	eg laun		8.0	_				
total devi	e <sup>IF(AV)</sup>	Rated V <sub>R</sub> , T <sub>C</sub> = 150 °C	16					
Non-repetitive peak surge current per leg	I <sub>FSM</sub>		100	A				
Peak repetitive forward current per leg	I <sub>FM</sub>	Rated V <sub>R</sub> , square wave, 20 kHz, T <sub>C</sub> = 150 °C	16					
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C				

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	200	-	-			
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A - ·		-	0.975	V		
		I <sub>F</sub> = 8 A, T <sub>J</sub> = 150 °C	-	-	0.895			
		$V_{R} = V_{R}$ rated	-	-	5			
Reverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250			
		V <sub>R</sub> = 200 V	-	25	-	μΑ		
Junction capacitance	CT	Measured lead to lead 5 mm from package body	-	8.0	-	pF		
Series inductance	L <sub>S</sub>	I <sub>R</sub> = 100 μA	200	-	-	nH		

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RoHS COMPLIANT HALOGEN

FREE



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### **Vishay Semiconductors**

<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 10$	-	19	-		
	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	20	-	ns
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 8 A dI <sub>F</sub> /dt = 200 A/μs V <sub>P</sub> = 160 V	-	34	-	
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		-	1.7	-	A nC
		T <sub>J</sub> = 125 °C		-	4.2	-	
Reverse recovery charge	Q <sub>rr</sub>	$T_J = 25 \ ^{\circ}C$		-	23	-	
		T <sub>J</sub> = 125 °C		-	75	-	

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C		
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	-	3.0			
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>		-	-	50	°C/W		
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	-	0.5	-			
			-	2.0	-	g		
weight			-	0.07	-	oz.		
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)		
Maultine device		Case style TO-263AB (D <sup>2</sup> PAK) MURB162						
		Case style TO-262AA		MURB16	20CT-1H			







Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage



Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

Average Power Loss (W)







Fig. 6 - Forward Power Loss Characteristics

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#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mbox{Pd} = \mbox{Forward power loss} = \mbox{I}_{F(AV)} \times \mbox{V}_{FM} \mbox{ at } (\mbox{I}_{F(AV)}/\mbox{D}) \mbox{ (see fig. 6);} \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = \mbox{V}_{R1} \times \mbox{I}_{R} \mbox{ (1 - D); } \mbox{I}_{R} \mbox{ at } \mbox{V}_{R1} = \mbox{Rated V}_{R} \end{array}$ 



Fig. 9 - Reverse Recovery Waveform and Definitions

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### **ORDERING INFORMATION TABLE**



ORDERING INFORMATION (Example)								
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION					
VS-MURB1620CTHM3	50	1000	Antistatic plastic tube					
VS-MURB1620CT-1HM3	50	1000	Antistatic plastic tube					
VS-MURB1620CTLHM3	800	800	13" diameter reel					
VS-MURB1620CTRHM3	800	800	13" diameter reel					

LINKS TO RELATED DOCUMENTS					
Dimonsions	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95046			
Dimensions	TO-262AA	www.vishay.com/doc?95419			
Part marking information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95444			
Part marking mormation	TO-262AA	www.vishay.com/doc?95443			
Packaging information	TO-263AB (D <sup>2</sup> PAK)	www.vishay.com/doc?95032			

# **Outline Dimensions**



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D<sup>2</sup>PAK

### **DIMENSIONS** in millimeters and inches

SHA



SVMBOL	MILLIMETERS		INC	INCHES		NOTES	NOTES		MILLIM	ETERS	INC	HES	NOTES
STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STINDUL	MIN.	MAX.	MIN.	MAX.	NOTES	
А	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3	
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3	
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3	
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC		
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625		
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110		
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3	
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070		
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC		
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208		

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5 M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

- <sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1
- <sup>(4)</sup> Dimension b1 and c1 apply to base metal only
- <sup>(5)</sup> Datum A and B to be determined at datum plane H
- <sup>(6)</sup> Controlling dimension: inch
- <sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-263AB

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## **Outline Dimensions**

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**TO-262** 

### **DIMENSIONS** in millimeters and inches





Lead assignments



**Diodes** 1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode



(3) E1 Section A - A Base (4) Plating b1, b3 metal Ā ///// (4)<--(b, b2)-►

Е

D1(3)

Section B - B and C - C Scale: None

MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. 0.160 0.190 А 4.06 4.83 0.080 A1 2.03 3.02 0.119 0.51 0.99 0.020 0.039 b b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 b3 1.14 1.73 0.045 0.068 4 0.38 0.74 0.015 0.029 с 0.38 0.015 0.023 4 c1 0.58 0.045 0.065 c2 1.14 1.65 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2.3 E1 7.90 8.80 0.311 0.346 3 2.54 BSC 0.100 BSC е L 13.46 0.530 0.555 14.10 L1 1.65 0.065 3 3.56 L2 3.71 0.140 0.146

#### Notes

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<sup>(1)</sup> Dimensioning and tolerancing as per ASME Y14.5M-1994

(2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

(4) Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline

<sup>(3)</sup> Thermal pad contour optional within dimension E, L1, D1 and E1

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### **Vishay Semiconductors**

**TO-262** 



#### Note

<sup>(1)</sup> If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
А	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
М	Halogen-free, RoHS-compliant and termination lead (Pb)-free
N	Halogen-free, RoHS-compliant and totally lead (Pb)-free
G	Green



### **Vishay Semiconductors**

D<sup>2</sup>PAK



#### Note

<sup>(1)</sup> If part number contain "H" as last digit, product is AEC-Q101 qualified

ENVIRONMENTAL NAMING CODE (Z)	PRODUCT DEFINITION
А	Termination lead (Pb)-free
В	Totally lead (Pb)-free
E	RoHS-compliant and termination lead (Pb)-free
F	RoHS-compliant and totally lead (Pb)-free
М	Halogen-free, RoHS-compliant, and termination lead (Pb)-free
Ν	Halogen-free, RoHS-compliant, and totally lead (Pb)-free
G	Green



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