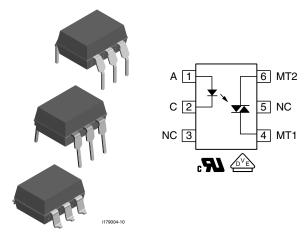
BRT11, BRT12, BRT13

Vishay Semiconductors

Optocoupler, Phototriac Output, Non-Zero Crossing



www.vishay.com

DESCRIPTION

The BRT11, BRT12, and BRT13 product family consists of AC optocouplers non-zero voltage detectors consisting of two electrically insulated lateral power ICs which integrate a thyristor system, a photo detector and noise suppression at the output and an IR GaAs diode input.

FEATURES

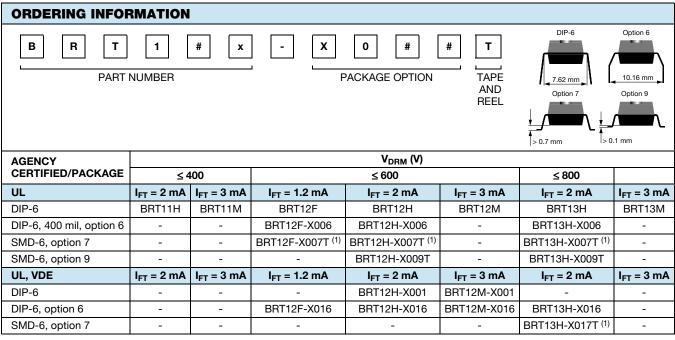
- I_{TBMS} = 300 mA
- High static dV/dt < 10 000 V/µs
- · Electrically insulated between input and output circuit
- RoHS Microcomputer compatible - very low trigger COMPLIANT current
- Trigger current
 - (I_{FT} < 1.2 mA) BRT12**F**
 - (I_{FT} < 2 mA) BRT11H, BRT12H, BRT13H
 - (I_{FT} < 3 mA) BRT11**M**, BRT12**M**, BRT13**M**
- Non-zero voltage detectors high input sensitivity
- · Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

APPLICATIONS

- Industrial controls
- Office equipment
- Consumer appliances

AGENCY APPROVALS

- UL 1577, file no. E52744 system code H
- DIN EN 60747-5-2 (VDE 0844)/DIN EN 60747-5-5 (pending) available with option 1
- CQC



Note

⁽¹⁾ Also available in tube, do not put T on the end.





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PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT		•			
Reverse voltage			V _R	6 V	
Forward current			I _F	20	mA
Surge forward current			I _{FSM}	1.5	А
Power dissipation	t ≤ 10 µs		P _{diss}	30	mW
OUTPUT					
Repetitive peak off-state voltage		BRT11	V _{DRM}	400	V
		BRT12	V _{DRM}	600	V
		BRT13	V _{DRM}	800	V
RMS on-state current			I _{TRMS}	300	mA
Single cycle surge current	50 Hz		I _{TSM}	3	А
Power dissipation			P _{diss}	600	mW
COUPLER					
Maximum power dissipation			P _{tot}	630	mW
Isolation test voltage (between emitter and detector, climate per DIN 500414, part 2, Nov. 74) ⁽¹⁾			V _{ISO}	5300	V _{RMS}
Reference voltage in accordance with VDE 0110 b			V _{ref}	500	V _{RMS}
Reference voltage in accordance with VDE 0110 b (insulation group C)			V _{ref}	600	V_{DC}
Creepage distance				≥ 7.2	mm
Clearance distance				≥ 7.2	mm
Comparative tracking index per DIN IEC 112/VDE 0303 part 1	group Illa according to DIN VDE 0109		СТІ	≥ 175	
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$		R _{IO}	≥ 10 ¹²	Ω
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$		R _{IO}	≥ 10 ¹¹	Ω
Storage temperature range			T _{stg}	- 40 to + 150	°C
Ambient temperature range			T _{amb}	- 40 to + 100	°C

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not • implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽¹⁾ Test AC voltage in accordance with DIN 57883, June 1980.



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PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT		•			•		
Forward voltage	I _F = 10 mA		V _F		1.1	1.35	V
Reverse current	V _R = 6 V		I _R			10	μA
Thermal resistance, junction to ambient ⁽¹⁾			R _{thJA}			750	°C/W
OUTPUT						-	
Peak off-state voltage	I _{D(RMS)} = 100 μΑ	BRT11			400		μA
		BRT12	V _{DM}		600		μA
		BRT13			800		μA
Off-state current	$T_{C} = 80 \ ^{\circ}C, V_{DRM}$		I _D		0.5	100	μA
On-state voltage	I _T = 300 mA		VT			2.3	V
Pulse current	$t_p \le 5 \ \mu s, f = 100 \ Hz, \ dI_{tp}/dt \le 8 \ A/\mu s$		I _{tp}			2	А
Critical rate of rise of off-state voltage	$V_D = 0.67 V_{DRM}, T_j = 25 \ ^{\circ}C$		dV/dt _{cr}	10			kV/μs
	$V_D = 0.67 V_{DRM}, T_j = 80 \ ^{\circ}C$		dV/dt _{cr}	5			kV/μs
Critical rate of rise of voltage at current commutation	V_D = 0.67 V_{DRM} , T_j = 25 °C, dI/dt _{crq} ≤ 15 A/ms		dV/dt _{crq}	10			kV/µs
	V _D = 0.67 V _{DRM} , T _j = 80 °C, dl/dt _{crq} ≤ 15 A/ms		dV/dt _{crq}	5			kV/µs
Critical rate of rise of on-state at current			dl/dt _{cr}	8			A∕µs
Holding current	$V_D = 10 V$		Ι _Η		80	500	μA
Thermal resistance, junction to ambient			R _{thJA}			125	°C/W
COUPLER							
Trigger current	$V_D = 10 V$, F - versions		I _{FT}			1.2	mA
	$V_D = 10 V, H - versions$		I _{FT}	0.4		2	mA
	$V_D = 10 V, M - versions$		I _{FT}	0.8		3	mA
Trigger current temperature gradient			$\Delta I_{FT} / \Delta T_j$		7	14	µA/°C
Capacitance (input to output)	f = 1 MHz, V _B = 0 V		C _{IO}			2	pF

Notes

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.
(1) Static air, SITAC soldered in PCB or base plate.

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

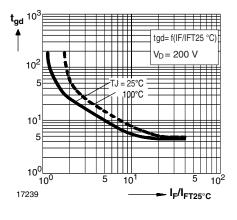


Fig. 1 - Typical Trigger Delay Time

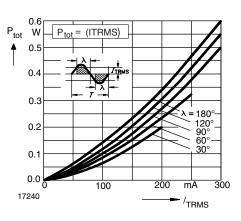


Fig. 2 - Power Dissipation 60 Hz to 60 Hz Line Operation

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Document Number: 83689

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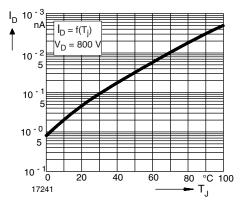


Fig. 3 - Typical Off-State Current

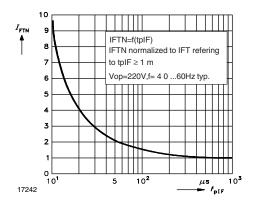


Fig. 4 - Pulse Trigger Current

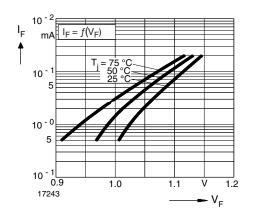


Fig. 5 - Typical Input Characteristics

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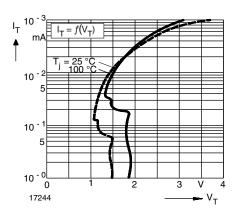


Fig. 6 - Typical Output Characteristics

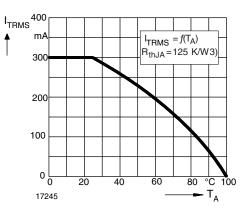


Fig. 7 - Current Reduction

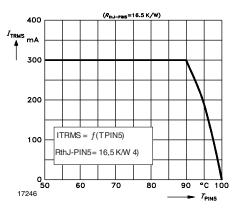


Fig. 8 - Current Reduction

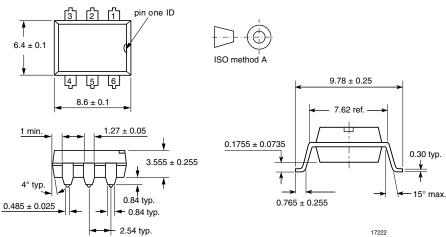
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BRT11, BRT12, BRT13



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PACKAGE DIMENSIONS in millimeters



Option 6



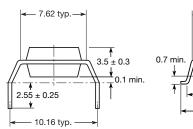
 3.5 ± 0.3

7.62 typ.-

9.27 min.

12.1 max.







– 7.62 typ. →

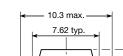
8 min. 10.3 max.



 0.25 ± 0.1

 4.3 ± 0.3

0.6 min.





 0.1 ± 0.1



 3.6 ± 0.3

PACKAGE MARKING (example)



Notes

- Only options 1, and 7 are reflected in the package marking. ٠
- The VDE logo is only marked on option 1 parts. .
- Tape and reel suffix (T) is not part of the package marking. ٠



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