**HALOGEN** 

FREE



## Vishay General Semiconductor

# **Surface Mount Schottky Barrier Rectifier**



**SMA (DO-214AC)** 

#### **DESIGN SUPPORT TOOLS AVAILABLE**



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	1.5 A			
V <sub>RRM</sub>	90 V			
I <sub>FSM</sub>	40 A			
V <sub>F</sub>	0.75 V			
T <sub>J</sub> max.	150 °C			
Package	SMA (DO-214AC)			
Circuit configuration	Single			

#### **FEATURES**

- Low profile package
- Ideal for automated placement
- · Guardring for overvoltage protection
- Low power losses, high efficiency
- Very low switching losses
- · High surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **TYPICAL APPLICATIONS**

For use in high frequency inverters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

#### **MECHANICAL DATA**

Case: SMA (DO-214AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test **Polarity:** color band denotes the cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	BYS11-90	UNIT
Device marking code			BYS109	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	90	V
Maximum average forward rectified current		I <sub>F(AV)</sub>	1.5	Α
Peak forward surge current single half sine-wave superimposed on rated load	8.3 ms	ı	40	^
	10 ms	I <sub>FSM</sub>	30	A
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs
Junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		TEST CONDITIONS		SYMBOL	BYS11-90	UNIT
Maximum instantaneous forward voltage	1.0 A		1.0 A		V <sub>F</sub> <sup>(1)</sup>	750	mV
Maximum DC reverse current	V	T <sub>J</sub> = 25 °C	I <sub>R</sub> <sup>(1)</sup>	100	μA		
	V <sub>RRM</sub>	T <sub>J</sub> = 100 °C		1	mA		

#### Note

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		BYS11-90	UNIT	
Maximum thermal resistance, junction to lead		25	°C/W	
	R <sub>0JA</sub> (1)	150		
Maximum thermal resistance, junction to ambient	R <sub>0JA</sub> (2)	125	°C/W	
	$R_{\theta JA}$ (3)	100		

#### **Notes**

- (1) Mounted on epoxy-glass hard tissue
- (2) Mounted on epoxy-glass hard tissue, 50 mm<sup>2</sup> 35 μm Cu
- (3) Mounted on Al-oxide-ceramic (Al<sub>2</sub>O<sub>3</sub>), 50 mm<sup>2</sup> 35 μm Cu

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
BYS11-90-M3/TR	0.064	TR	1800	7" diameter plastic tape and reel	
BYS11-90-M3/TR3	0.064	TR3	7500	13" diameter plastic tape and reel	

### RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

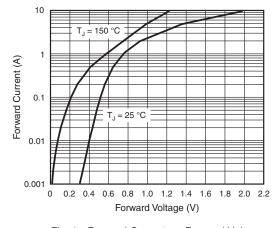


Fig. 1 - Forward Current vs. Forward Voltage

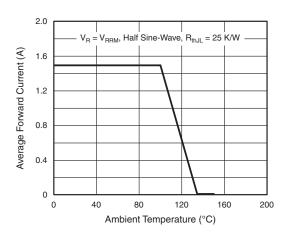


Fig. 2 - Max. Average Forward Current vs. Ambient Temperature



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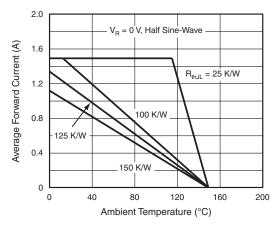


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

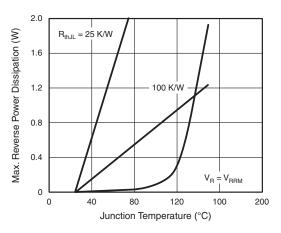


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

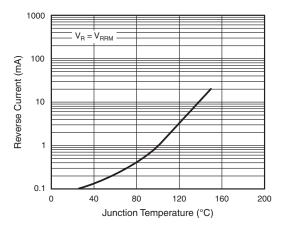


Fig. 4 - Reverse Current vs. Junction Temperature

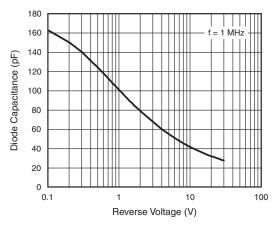
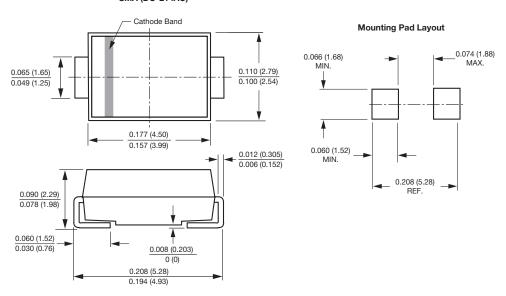


Fig. 6 - Diode Capacitance vs. Reverse Voltage

# PACKAGE OUTLINE DIMENSIONS in inches (millimeters) SMA (DO-214AC)





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