BYT52A, BYT52B, BYT52D, BYT52G, BYT52J, BYT52K, BYT52M



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Vishay Semiconductors

Fast Avalanche Sinterglass Diode



DESIGN SUPPORT TOOLS



MECHANICAL DATA

Case: SOD-57 Terminals: plated axial leads, solderable per MIL-STD-750, method 2026 Polarity: color band denotes cathode end Mounting position: any Weight: approx. 369 mg

FEATURES

- Glass passivated junction
- · Hermetically sealed package
- Low reverse current
- · Soft recovery characteristics
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

· Fast rectification and switching diode

ORDERING INFORMATION (Example)					
DEVICE NAME	ORDERING CODE	TAPED UNITS	MINIMUM ORDER QUANTITY		
BYT52M	BYT52M-TR	5000 per 10" tape and reel	25 000		
BYT52M	BYT52M-TAP	5000 per ammopack	25 000		

PARTS TABLE					
PART	TYPE DIFFERENTIATION	PACKAGE			
BYT52A	V _R = 50 V; I _{F(AV)} = 1.4 A	SOD-57			
BYT52B	V _R = 100 V; I _{F(AV)} = 1.4 A	SOD-57			
BYT52D	V _R = 200 V; I _{F(AV)} = 1.4 A	SOD-57			
BYT52G	V _R = 400 V; I _{F(AV)} = 1.4 A	SOD-57			
BYT52J	V _R = 600 V; I _{F(AV)} = 1.4 A	SOD-57			
BYT52K	V _R = 800 V; I _{F(AV)} = 1.4 A	SOD-57			
BYT52M	V _R = 1000 V; I _{F(AV)} = 1.4 A	SOD-57			

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT			
		BYT52A	$V_R = V_{RRM}$	50	V			
	See electrical characteristics	BYT52B	$V_R = V_{RRM}$	100	V			
		BYT52D	$V_R = V_{RRM}$	200	V			
Reverse voltage = repetitive peak reverse voltage		BYT52G	$V_R = V_{RRM}$	400	V			
		BYT52J	$V_R = V_{RRM}$	600	V			
		BYT52K	$V_R = V_{RRM}$	800	V			
		BYT52M	$V_R = V_{RRM}$	1000	V			
Peak forward surge current	t _p = 10 ms, half sine wave		I _{FSM}	50	А			
Average forward eurrent	l = 10 mm		I _{F(AV)}	1.4	А			
Average forward current	On PC board		I _{F(AV)}	0.85	А			
		BYT52J	E _R	10	mJ			
Non repetitive reverse avalanche energy	$I_{(BR)R} = 0.4 A$	BYT52K	E _R	10	mJ			
		BYT52M	E _R	10	mJ			
Junction and storage temperature range			$T_j = T_{stg}$	-55 to +175	°C			

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MAXIMUM THERMAL RESISTANCE (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
lunction ambient	Lead length I = 10 mm, T _L = constant	R _{thJA}	45	K/W		
	On PC board with spacing 25 mm	R _{thJA}	100	K/W		

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I _F = 1 A		V _F	-	-	1.3	V
Reverse current	$V_{R} = V_{RRM}$		I _R	-	-	5	μA
neverse current	$V_{R} = V_{RRM}, T_{j} = 150 \text{ °C}$		I _R	-	-	150	μA
Reverse recovery time	I _F = 0.5 A, I _R = 1 A, i _R = 0.25 A		t _{rr}	-	-	200	ns

TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)



Fig. 1 - Max. Thermal Resistance vs. Lead Length



Fig. 2 - Max. Forward Current vs. Forward Voltage



Fig. 3 - Max. Average Forward Current vs. Ambient Temperature



Fig. 4 - Max. Reverse Current vs. Junction Temperature

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T_i - Junction Temperature (°C) Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

100

150

175

125

0

16331

25

50

75



Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-57



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