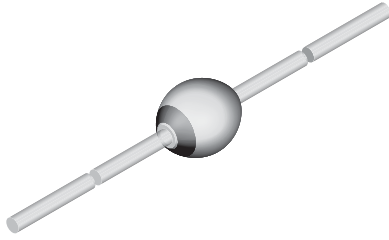




Ultra-Fast Avalanche Sinterglass Diode



949539

FEATURES

- Glass passivated junction
• Hermetically sealed package
• Low reverse current
• Soft recovery characteristics
• Material categorization:
For definitions of compliance please see
www.vishay.com/doc?99912



RoHS COMPLIANT HALOGEN FREE

MECHANICAL DATA

Case: SOD-57

Terminals: plated axial leads, solderable per MIL-STD-750, method 2026

Polarity: color band denotes cathode end

Mounting position: any

Weight: approx. 369 mg

APPLICATIONS

- Very fast rectification and switches
• Switched mode power supplies
• High-frequency inverter circuits

Table with 4 columns: DEVICE NAME, ORDERING CODE, TAPED UNITS, MINIMUM ORDER QUANTITY. Includes rows for BYT53G (TR and TAP).

Table with 3 columns: PART, TYPE DIFFERENTIATION, PACKAGE. Lists parts BYT53A through BYT53G with their respective V_R and I_F(AV) values.

Table with 6 columns: PARAMETER, TEST CONDITION, PART, SYMBOL, VALUE, UNIT. Lists absolute maximum ratings for reverse voltage, surge current, average current, etc.

Table with 5 columns: PARAMETER, TEST CONDITION, SYMBOL, VALUE, UNIT. Lists maximum thermal resistance for junction ambient.

ELECTRICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	$I_F = 1\text{ A}$		V_F	-	-	1.1	V
	$I_F = 1\text{ A}, T_j = 175\text{ }^{\circ}\text{C}$		V_F	-	-	0.9	V
Reverse current	$V_R = V_{RRM}$		I_R	-	-	5	μA
	$V_R = V_{RRM}, T_j = 150\text{ }^{\circ}\text{C}$		I_R	-	-	200	μA
Reverse recovery time	$I_F = 0.5\text{ A}, I_R = 1\text{ A}, t_R = 0.25\text{ A}$		t_{rr}	-	-	50	ns

TYPICAL CHARACTERISTICS ($T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

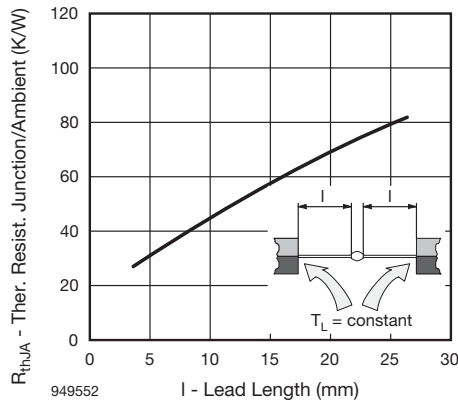


Fig. 1 - Max. Thermal Resistance vs. Lead Length

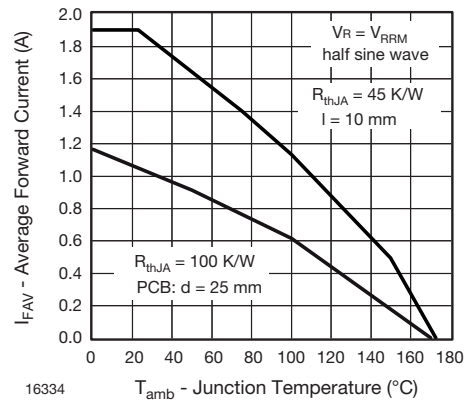


Fig. 3 - Max. Average Forward Current vs. Ambient Temperature

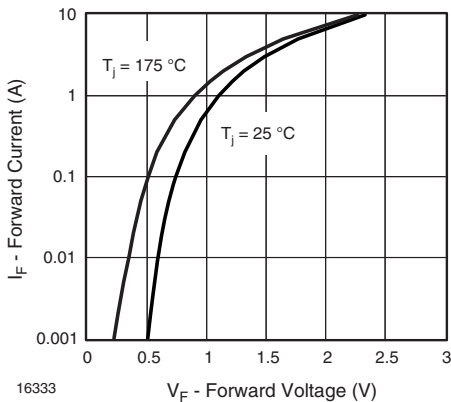


Fig. 2 - Max. Forward Current vs. Forward Voltage

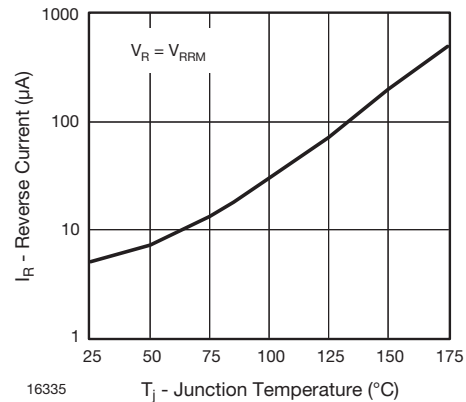


Fig. 4 - Max. Reverse Current vs. Junction Temperature

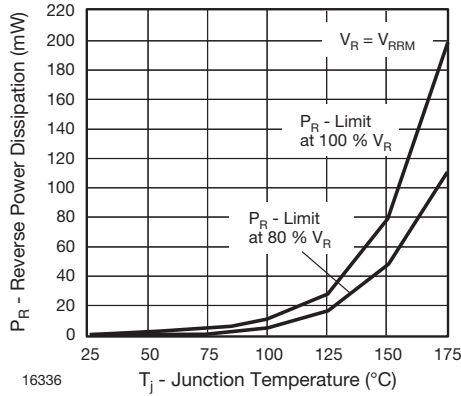


Fig. 5 - Max. Reverse Power Dissipation vs. Junction Temperature

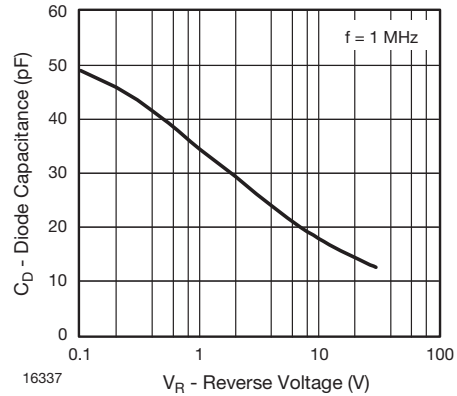
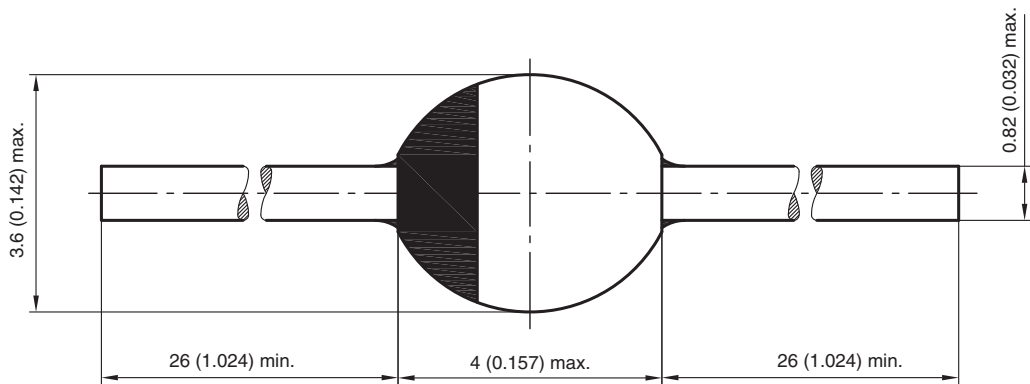


Fig. 6 - Diode Capacitance vs. Reverse Voltage

PACKAGE DIMENSIONS in millimeters (inches): SOD-57



20543
 Rev. 3 - Date: 09.February 2005
 Document no.:6.563-5006.3-4



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