

### **High Current, Through-Hole Inductor Edge-Wound Series**



- High temperature operation, up to 180 °C continuous with no aging
- Low DCR to minimize losses and reduce temperature rise
- Powdered iron alloy core technology provides inductance and saturation over stable operating temperature with satisfactory core losses



- Soft saturation gives predictable inductance decrease with increasing DC current independent of temperature
- · Standard terminal is stripped and tinned for through hole mounting but other terminal configurations such as bare copper, SMD, and press fit pin are available upon request
- Hot dipped Sn plating provides low risk of whisker growth
- Custom options are available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

#### **APPLICATIONS**

- · High current and high temperature applications
- DC/DC converters
- High current motor and switching noise suppression
- Inverters

STANDARD ELECTRICAL SPECIFICATIONS										
L <sub>0</sub> INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR AT 25 °C TYP. (mΩ)	DCR AT 25 °C MAX. (mΩ)	HEAT RATING CURRENT DC TYP. <sup>(1)</sup> (A)	SATURATION CURRENT DC TYP. <sup>(2)</sup> (A)	SATURATION CURRENT DC TYP. <sup>(3)</sup> (A)	SRF TYP. (MHZ)	DIMENSION A ± 0.016 [0.4]	DIMENSION B ± 0.020 [0.5]		
1.2	0.25	0.30	80	110	150	90	0.126 [3.2]	0.543 [13.8]		
2.2	0.35	0.40	70	75	110	45	0.098 [2.5]	0.571 [14.5]		
3.3	0.63	0.70	50	60	90	25	0.079 [2.0]	0.591 [15.0]		
4.7	0.86	0.95	45	50	70	15	0.071 [1.8]	0.598 [15.2]		
6.8	1.00	1.15	40	45	60	10	0.063 [1.6]	0.606 [15.4]		
8.2	1.35	1.50	35	35	50	9	0.055 [1.4]	0.614 [15.6]		
10	1.70	2.00	30	30	45	8	0.043 [1.1]	0.626 [15.9]		

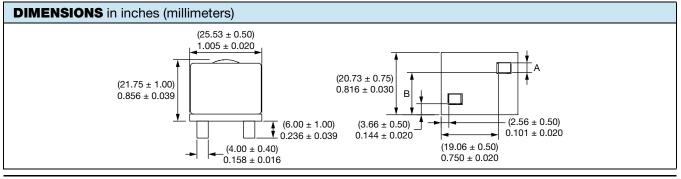
#### Notes

All test data is referenced to 25 °C ambient

Operating temperature range -40 °C to +180 °C The part temperature (ambient + temp. rise) should not exceed 180 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application

Isolation voltage, coil to core: 350 V<sub>DC</sub>, 60 s, 5 mA max.,

- (1) DC current (A) that will cause an approximate  $\Delta T$  of 40 °C (2)
- DC current (A) that will cause L0 to drop approximately 20 %
- (3) DC current (A) that will cause L<sub>0</sub> to drop approximately 30 %



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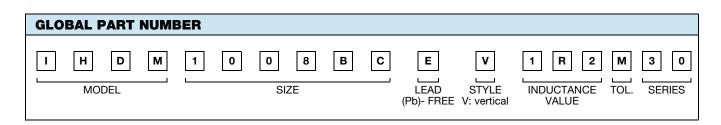


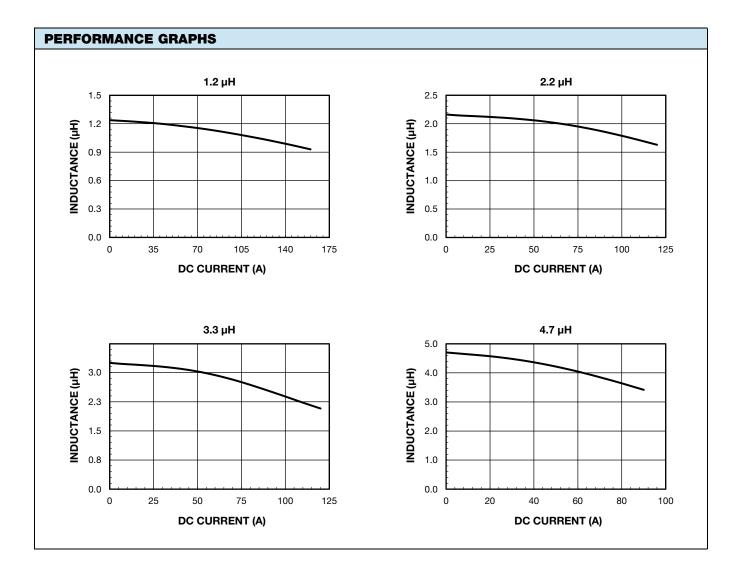
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# IHDM-1008BC-30

#### Vishay Custom Magnetics

DESCRIPTION				
IHDM-1008BC-30	1.2 μH	± 20 %	EV	e3
MODEL	INDUCTANCE	INDUCTANCE TOLERANCE	PACKAGE	JEDEC <sup>®</sup> LEAD (Pb)-FREE STANDARD





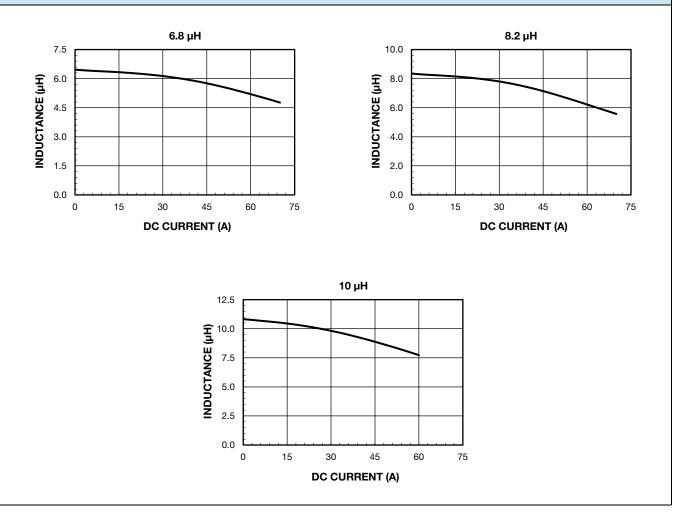
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## IHDM-1008BC-30

### Vishay Custom Magnetics

PERFORMANCE GRAPHS





Vishay

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