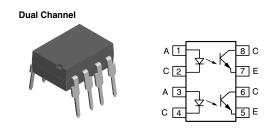
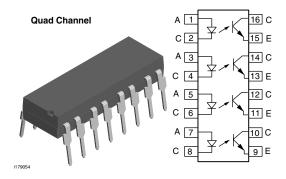


Vishay Semiconductors

# Optocoupler, Phototransistor Output (Dual, Quad Channel)





### **FEATURES**

- Alternate source to TLP621-2/-4 and TLP621GB-2/-4
- Pb-free
- High collector emitter voltage, BV<sub>CEO</sub> = 70 V
- Dual and guad packages feature:
  - Lower pin and parts count
  - Better channel to channel CTR match
  - Improved common mode rejection
- Isolation test voltage, 5300 V<sub>RMS</sub>
- · Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

### **AGENCY APPROVALS**

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending available with option 1
- BSI IEC 60950; IEC 60065
- FIMKO

#### **DESCRIPTION**

The ILD621/ILQ621 and ILD621GB/ILQ621GB are multi-channel phototransistor optocouplers that use GaAs IRLED emitters and high gain NPN silicon phototransistors. These devices are constructed using double molded insulation technology. This assembly process offers a withstand test voltage of 7500 VDC.

The ILD621/ILQ621GB is well suited for CMOS interfacing given the CTR<sub>CEsat</sub> of 30 % minimum at I<sub>F</sub> of 1.0 mA. High gain linear operation is guaranteed by a minimum CTR<sub>CE</sub> of 100 % at 5.0 mA. The ILD/Q621 has a guaranteed CTR<sub>CE</sub> 50 % minimum at 5.0 mA. The transparent ion shield insures stable DC gain in applications such as power supply feedback circuits, where constant DC  $V_{\rm IO}$  voltages are present.

ORDER INFORMATION	
PART	REMARKS
ILD621	CTR > 50 %, dual, DIP-8
ILD621GB	CTR > 100 %, dual, DIP-8
ILQ621	CTR > 50 %, quad, DIP-16
ILQ621GB	CTR > 100 %, quad, DIP-16
ILD621-X006	CTR > 50 %, dual, DIP-8 400 mil

Vishay Semiconductors

## Optocoupler, Phototransistor Output (Dual, Quad Channel)



ORDER INFORMATION	
PART	REMARKS
ILD621-X007	CTR > 50 %, dual, SMD-8 (option 7)
ILD621-X009	CTR > 50 %, dual, SMD-8 (option 9)
ILD621GB-X007	CTR > 100 %, dual, SMD-8 (option 7)
ILQ621-X006	CTR > 50 %, quad, DIP-16 400 mil
ILQ621-X007	CTR > 50 %, quad, SMD-16 (option 7)
ILQ621-X009	CTR > 50 %, quad, SMD-16 (option 9)
ILQ621GB-X006	CTR > 100 %, quad, DIP-16 400 mil
ILQ621GB-X007	CTR > 100 %, quad, SMD-16 (option 7)
ILQ621GB-X009	CTR > 100 %, quad, SMD-16 (option 9)

#### Note

For additional information on the available options refer to option information.

PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
INPUT	.25. 552	. , , , , ,	3552	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	J
Reverse voltage			V <sub>R</sub>	6.0	V
Forward current			I <sub>F</sub>	60	mA
Surge current			I <sub>FSM</sub>	1.5	Α
Power dissipation			P <sub>diss</sub>	100	mW
Derate from 25 °C			uiss	1.33	mW/°C
OUTPUT				l l	
Collector emitter reverse voltage			V <sub>ECO</sub>	70	V
Collector current			I <sub>C</sub>	50	mA
Collector current	t < 1.0 ms		I <sub>C</sub>	100	mA
Power dissipation			P <sub>diss</sub>	150	mW
Derate from 25 °C				- 2.0	mW/°C
COUPLER					
Isolation test voltage	t = 1.0 s		V <sub>ISO</sub>	5300	V <sub>RMS</sub>
Dealease dissination		ILD621		400	mW
Package dissipation		ILD621GB		400	mW
Derate from 25 °C				5.33	mW/°C
Package discinction		ILQ621		500	mW
Package dissipation		ILQ621GB		500	mW
Derate from 25 °C				6.67	mW/°C
Creepage distance				≥ 7.0	mm
Clearance distance				≥ 7.0	mm
Indiction registeres	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C		R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C		R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Storage temperature			T <sub>stg</sub>	- 55 to + 150	°C
Operating temperature			T <sub>amb</sub>	- 55 to + 100	°C
Junction temperature			Ti	100	°C
Soldering temperature (2)	2.0 mm from case bottom		T <sub>sld</sub>	260	°C

### Notes

 $<sup>^{(1)}</sup>$   $T_{amb} = 25$  °C, unless otherwise specified.

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(2)</sup> Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).



Optocoupler, Phototransistor Output (Dual, Quad Channel)

Vishay Semiconductors

ELECTRICAL CHARACTERISTICS								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT								
Forward voltage	I <sub>F</sub> = 10 mA		$V_{F}$	1.0	1.15	1.3	V	
Reverse current	V <sub>R</sub> = 6.0 V		I <sub>R</sub>		0.01	10	μΑ	
Capacitance	$V_R = 0 \text{ V}, f = 1.0 \text{ MHz}$		Co		40		pF	
Thermal resistance, junction to lead			$R_{THJL}$		750		K/W	
OUTPUT								
Collector emitter capacitance	$V_{CE} = 5.0 \text{ V}, f = 1.0 \text{ MHz}$		C <sub>CE</sub>		6.8		pF	
Collector amittar lookaga current	V <sub>CE</sub> = 24 V		I <sub>CEO</sub>		10	100	nA	
Collector emitter leakage current			I <sub>CEO</sub>		20	50	μΑ	
Thermal resistance, junction to lead			R <sub>THJL</sub>		500		K/W	
COUPLER								
Capacitance (input to output)	V <sub>IO</sub> = 0 V, f = 1.0 MHz		C <sub>IO</sub>	0.8			pF	
Insulation resistance	V <sub>IO</sub> = 500 V			10 <sup>12</sup>			Ω	
Channel to channel insulation				500			VAC	
Collector emitter saturation voltage	$I_F = 8.0 \text{ mA}, I_{CE} = 2.4 \text{ mA}$	ILD621 ILQ621	V <sub>CEsat</sub>			0.4	٧	
	I <sub>F</sub> = 1.0 mA, I <sub>CE</sub> = 0.2 mA	ILD621GB ILQ621GB	V <sub>CEsat</sub>			0.4	V	

### Note

 $T_{amb}$  = 25  $^{\circ}C,$  unless otherwise specified.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Channel/channel CTR match	$I_F = 5.0 \text{ mA}, V_{CE} = 5.0 \text{ V}$		CTRX/ CTRY	1 to 1		3 to 1	%
	I <sub>F</sub> = 1.0 mA, V <sub>CE</sub> = 0.4 V	ILD621	CTR <sub>CEsat</sub>		60		%
Current transfer ratio (collector emitter		ILQ621	CTR <sub>CEsat</sub>		60		%
saturated)		ILD621GB	CTR <sub>CEsat</sub>	30			%
cara. area,		ILQ621GB	CTR <sub>CEsat</sub>	30			%
		ILD621	CTR <sub>CE</sub>	50	80	600	%
Current transfer ratio (collector emitter)		ILQ621	CTR <sub>CE</sub>	50	80	600	%
		ILD621GB	CTR <sub>CE</sub>	100	200	600	%
		ILQ621GB	CTR <sub>CE</sub>	100	200	600	%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
NON-SATURATED							
On time	$I_F$ = $\pm$ 10 mA, $V_{CC}$ = 5.0 V, $R_L$ = 75 $\Omega$ , 50 % of $V_{PP}$	t <sub>on</sub>		3.0		μs	
Rise time	$I_F$ = $\pm$ 10 mA, $V_{CC}$ = 5.0 V, $R_L$ = 75 $\Omega$ , 50 % of $V_{PP}$	t <sub>r</sub>		2.0		μs	
Off time	$I_F$ = $\pm$ 10 mA, $V_{CC}$ = 5.0 V, $R_L$ = 75 $\Omega$ , 50 % of $V_{PP}$	t <sub>off</sub>		2.3		μs	
Fall time	$I_F$ = $\pm$ 10 mA, $V_{CC}$ = 5.0 V, $R_L$ = 75 $\Omega$ , 50 % of $V_{PP}$	t <sub>f</sub>		2.0		μs	
Propagation H to L	$I_F$ = $\pm$ 10 mA, $V_{CC}$ = 5.0 V, $R_L$ = 75 $\Omega$ , 50 % of $V_{PP}$	t <sub>PHL</sub>		1.1		μs	
Propagation L to H	$I_F$ = ± 10 mA, $V_{CC}$ = 5.0 V, $R_L$ = 75 $\Omega$ , 50 % of $V_{PP}$	t <sub>PLH</sub>		2.5		μs	

Document Number: 83654 Rev. 1.5, 20-Dec-07

Vishay Semiconductors

## Optocoupler, Phototransistor Output (Dual, Quad Channel)



SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
SATURATED							
On time	$I_F = \pm 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1 \text{ k}\Omega, V_{TH} = 1.5 \text{ V}$	t <sub>on</sub>		4.3		μs	
Rise time	$I_F = \pm 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1 \text{ k}\Omega, V_{TH} = 1.5 \text{ V}$	t <sub>r</sub>		2.8		μs	
Off time	$I_F = \pm 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1 \text{ k}\Omega, V_{TH} = 1.5 \text{ V}$	t <sub>off</sub>		2.5		μs	
Fall time	$I_F = \pm 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1 \text{ k}\Omega, V_{TH} = 1.5 \text{ V}$	t <sub>f</sub>		11		μs	
Propagation H to L	$I_F = \pm 10 \text{ mA}, V_{CC} = 5.0 \text{ V}, R_L = 1 \text{ k}\Omega, V_{TH} = 1.5 \text{ V}$	t <sub>PHL</sub>		2.6		μs	
Propagation L to H	$I_F = \pm 10$ mA, $V_{CC} = 5.0$ V, $R_L = 1$ k $\Omega$ , $V_{TH} = 1.5$ V	t <sub>PLH</sub>		7.2		μs	

COMMON MODE TRANSIENT IMMUNITY						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode rejection, output high	$V_{CM}$ = 50 $V_{P-P}$ , $R_L$ = 1.0 $k\Omega$ , $I_F$ = 0 mA	CM <sub>H</sub>		5000		V/µs
Common mode rejection, output low	$V_{CM}$ = 50 $V_{P-P}$ , $R_L$ = 1.0 $k\Omega$ , $I_F$ = 10 mA	CML		5000		V/µs

### **TYPICAL CHARACTERISTICS**

 $T_{amb}$  = 25 °C, unless otherwise specified

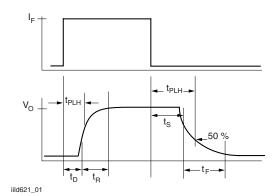


Fig. 1 - Non-Saturated Switching Timing

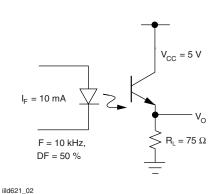


Fig. 2 - Non-Saturated Switching Timing

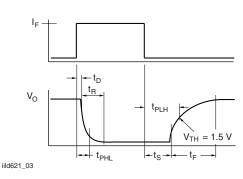


Fig. 3 - Saturated Switching Timing

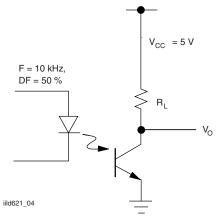


Fig. 4 - Saturated Switching Timing



## Optocoupler, Phototransistor Output (Dual, Quad Channel)

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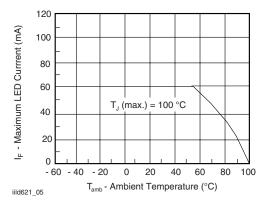


Fig. 5 - Maximum LED Current vs. Ambient Temperature

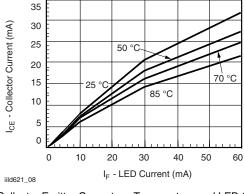


Fig. 8 - Collector Emitter Current vs. Temperature and LED Current

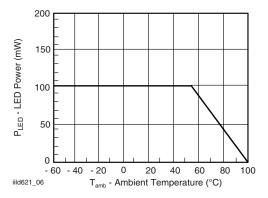


Fig. 6 - Maximum LED Power Dissipation

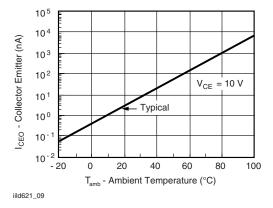


Fig. 9 - Collector Emitter Leakage vs. Temperature

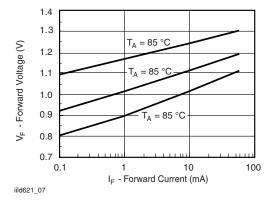


Fig. 7 - Forward Voltage vs. Forward Current

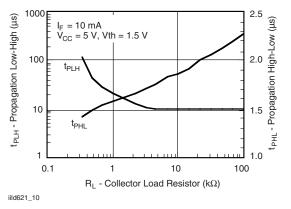


Fig. 10 - Propagation Delay vs. Collector Load Resistor

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## Optocoupler, Phototransistor Output (Dual, Quad Channel)

2.0

iild621\_15

Normalized to:



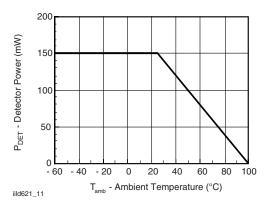
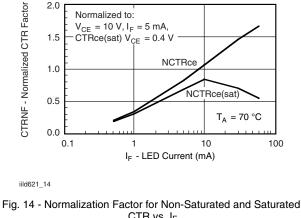


Fig. 11 - Maximum Detector Power Dissipation



CTR vs. I<sub>F</sub>

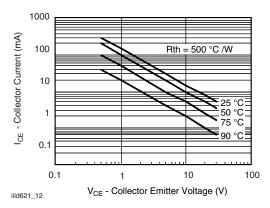


Fig. 12 - Maximum Collector Current vs. Collector Voltage

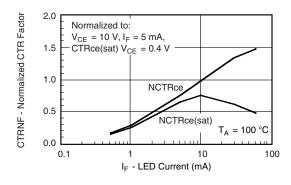


Fig. 15 - Normalization Factor for Non-Saturated and Saturated CTR vs. I<sub>F</sub>

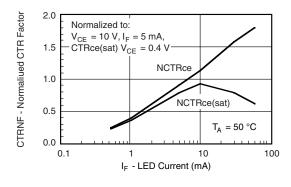


Fig. 13 - Normalization Factor for Non-Saturated and Saturated CTR vs. I<sub>F</sub>

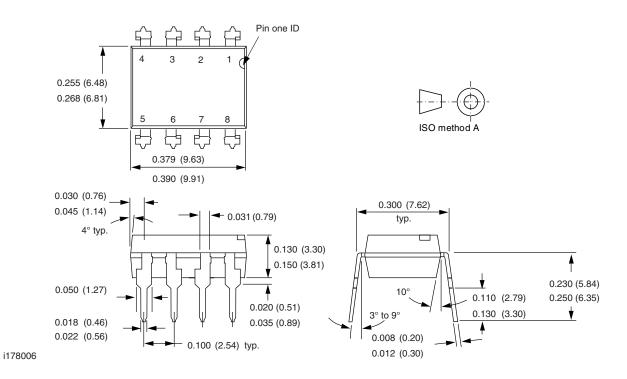
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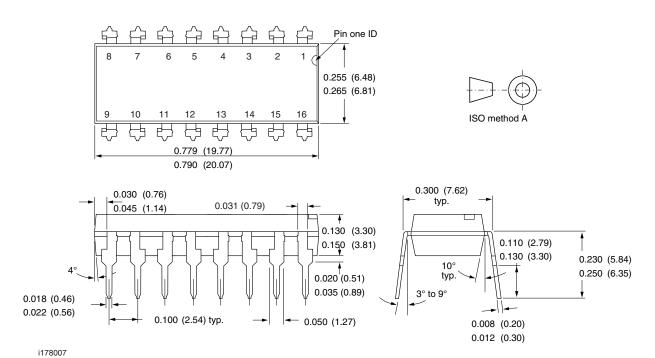


Optocoupler, Phototransistor Output (Dual, Quad Channel)

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### **PACKAGE DIMENSIONS** in inches (millimeters)

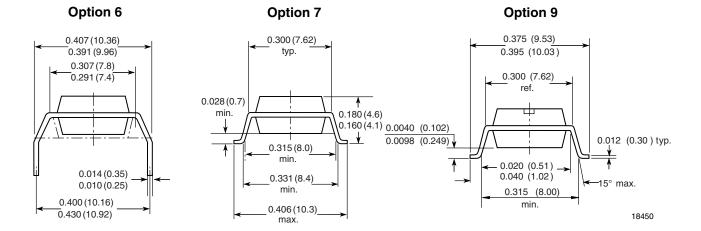




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Optocoupler, Phototransistor Output (Dual, Quad Channel)







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It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany

Document Number: 83654 Rev. 1.5, 20-Dec-07



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