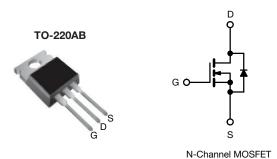


## **Power MOSFET**



PRODUCT SUMMAI	RY	
V <sub>DS</sub> (V)	80	00
$R_{DS(on)}(\Omega)$	V <sub>GS</sub> = 10 V	3.0
Q <sub>g</sub> max. (nC)	7	8
Q <sub>gs</sub> (nC)	9.	.6
Q <sub>gd</sub> (nC)	4	5
Configuration	Sin	gle

### **FEATURES**

- Dynamic dV/dt rating
- · Repetitive avalanche rated
- · Fast switching
- · Ease of paralleling
- Simple drive requirements
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### Note

\* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

## **DESCRIPTION**

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRFBE30PbF
Lead (Pb)-free and halogen-free	IRFBE30PbF-BE3

PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-source voltage		V <sub>DS</sub>	800	V		
Gate-source voltage		V <sub>GS</sub>	± 20	7 v		
Continuous drain current	V at 10 V	T <sub>C</sub> = 25 °C T <sub>C</sub> = 100 °C		4.1		
Continuous drain current	V <sub>GS</sub> at 10 V	T <sub>C</sub> = 100 °C	I <sub>D</sub>	2.6	A	
Pulsed drain current <sup>a</sup>			I <sub>DM</sub>	16		
Linear derating factor			1.0	W/°C		
ingle pulse avalanche energy <sup>b</sup>		E <sub>AS</sub>	260	mJ		
Repetitive avalanche current a		I <sub>AR</sub>	4.1	Α		
Repetitive avalanche energy <sup>a</sup>		E <sub>AR</sub>	13	mJ		
Maximum power dissipation $T_C = 25  ^{\circ}C$		25 °C	P <sub>D</sub>	125	W	
Peak diode recovery dV/dt <sup>c</sup>			dV/dt	2.0	V/ns	
Operating junction and storage temperature range			T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	
Soldering recommendations (peak temperature) <sup>d</sup>	For	10 s		300	°C	
Mounting torque	6-32 or M3 screw			10	lbf ⋅ in	
Mounting torque				1.1	N⋅m	

#### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b.  $V_{DD}$  = 50 V, starting  $T_J$  = 25 °C, L = 29 mH,  $R_g$  = 25  $\Omega$ ,  $I_{AS}$  = 4.1 A (see fig. 12)
- c.  $I_{SD} \le 4.1$  A,  $dI/dt \le 100$  A/ $\mu$ s,  $V_{DD} \le 600$ ,  $T_J \le 150$  °C
- d. 1.6 mm from case



# Vishay Siliconix

THERMAL RESISTANCE RAT	INGS			
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R <sub>thJA</sub>	-	62	
Case-to-sink, flat, greased surface	R <sub>thCS</sub>	0.50	-	°C/W
Maximum junction-to-case (drain)	R <sub>thJC</sub>	-	1.0	

PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static						•	
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		800	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I <sub>D</sub> = 1 mA	-	0.9	-	V/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.0	-	4.0	V
Gate-source leakage	I <sub>GSS</sub>	V	<sub>'GS</sub> = ± 20 V	-	-	± 100	nA
7		V <sub>DS</sub> = 800 V, V <sub>GS</sub> = 0 V		-	-	100	μΑ
Zero gate voltage drain current	I <sub>DSS</sub>	$V_{DS} = 640 \text{ V},$	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C		-	500	
Drain-source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 2.5 A <sup>b</sup>	-	-	3.0	Ω
Forward transconductance	9 <sub>fs</sub>	$V_{DS} = 1$	100 V, I <sub>D</sub> = 2.5 A <sup>b</sup>	2.5	-	-	S
Dynamic							
Input capacitance	C <sub>iss</sub>	$V_{GS} = 0 \text{ V},$		-	1300	-	pF
Output capacitance	C <sub>oss</sub>	,	$V_{DS} = 25 \text{ V},$		310	-	
Reverse transfer capacitance	C <sub>rss</sub>	f = 1.0 MHz, see fig. 5		-	190	-	
Total gate charge	Qg			-	-	78	
Gate-source charge	Q <sub>gs</sub>	V <sub>GS</sub> = 10 V	$I_D = 4.1 \text{ A}, V_{DS} = 400 \text{ V},$ see fig. 6 and 13 b	-	-	9.6	nC
Gate-drain charge	Q <sub>gd</sub>	7		-	-	45	
Turn-on delay time	t <sub>d(on)</sub>			-	12	-	
Rise time	t <sub>r</sub>	$V_{DD}$ = 400 V, $I_{D}$ = 4.1 A $R_{g}$ = 12 $\Omega$ , $R_{D}$ = 95 $\Omega$ , see fig. 10 b		-	33	-	ns
Turn-off delay time	t <sub>d(off)</sub>			-	82	-	
Fall time	t <sub>f</sub>			-	30	-	
Gate input resistance	R <sub>g</sub>	f = 1 MHz, open drain		0.6	-	1.6	Ω
Internal drain inductance	L <sub>D</sub>	6 mm (0.25")	Between lead, 6 mm (0.25") from		4.5	-	- nH
Internal source inductance	L <sub>S</sub>	package and center of die contact		-	7.5	-	''''
<b>Drain-Source Body Diode Characteristic</b>	es						
Continuous source-drain diode current	I <sub>S</sub>	showing t	MOSFET symbol showing the		-	4.1	- A
Pulsed diode forward current <sup>a</sup>	I <sub>SM</sub>	integral reverse p - n junction diode		-	-	16	_ ^
Body diode voltage	V <sub>SD</sub>	$T_J = 25  ^{\circ}\text{C},  I_S = 4.1  \text{A},  V_{GS} = 0  \text{V}^{ \text{b}}$		-	-	1.8	V
Body diode reverse recovery time	t <sub>rr</sub>	T 25 °C 1	- 4.1 A dl/dt - 100 A/us b	_	480	720	ns
Body diode reverse recovery charge	Q <sub>rr</sub>	$T_J = 25$ °C, $I_F = 4.1$ A, $dI/dt = 100$ A/ $\mu$ s b		-	1.8	2.7	μC
Forward turn-on time	t <sub>on</sub>	Intrinsic tur	n-on time is negligible (turn	on is do	minated b	y L <sub>S</sub> and	L <sub>D</sub> )

### Notes

- a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
- b. Pulse width  $\leq 300~\mu s;~duty~cycle \leq 2~\%$



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

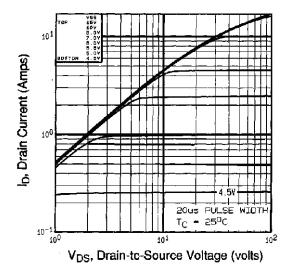


Fig. 1 - Typical Output Characteristics, T<sub>C</sub> = 25 °C

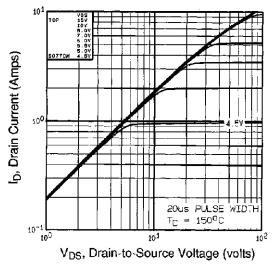
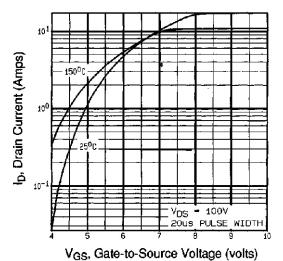


Fig. 2 - Typical Output Characteristics,  $T_C = 150$  °C



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Fig. 3 - Typical Transfer Characteristics

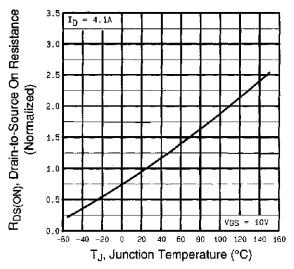


Fig. 4 - Normalized On-Resistance vs. Temperature



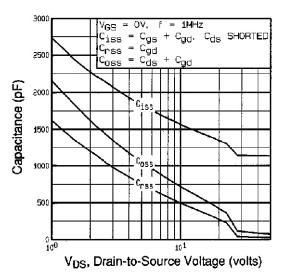


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

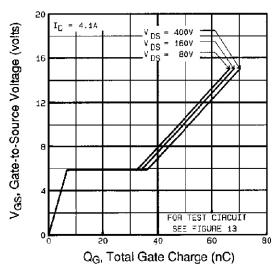


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

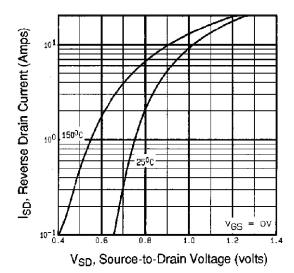


Fig. 7 - Typical Source-Drain Diode Forward Voltage

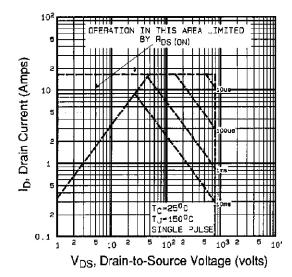


Fig. 8 - Maximum Safe Operating Area



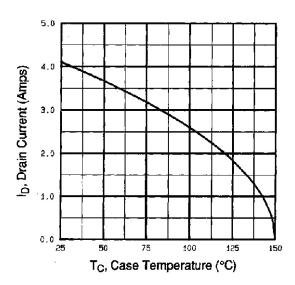


Fig. 9 - Maximum Drain Current vs. Case Temperature

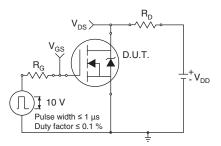


Fig. 10a - Switching Time Test Circuit

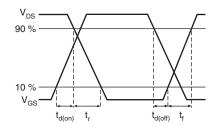


Fig. 10b - Switching Time Waveforms

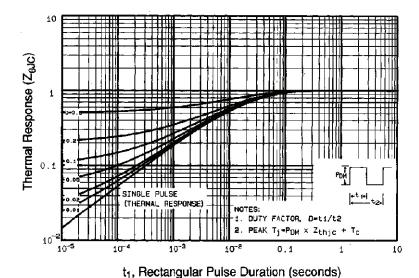


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

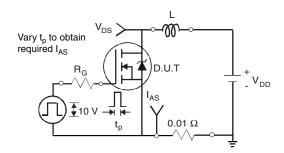


Fig. 12a - Unclamped Inductive Test Circuit

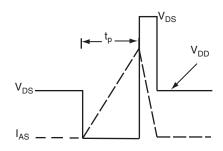


Fig. 12b - Unclamped Inductive Waveforms



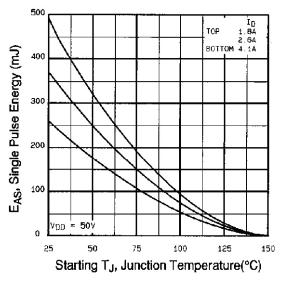


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

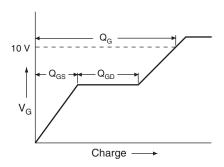


Fig. 13a - Basic Gate Charge Waveform

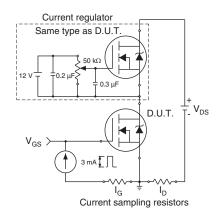
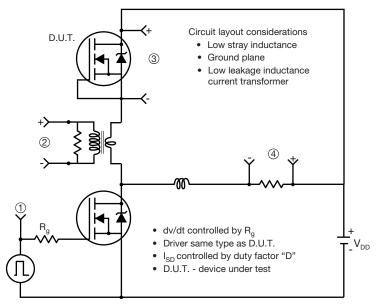


Fig. 13b - Gate Charge Test Circuit



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### Peak Diode Recovery dv/dt Test Circuit



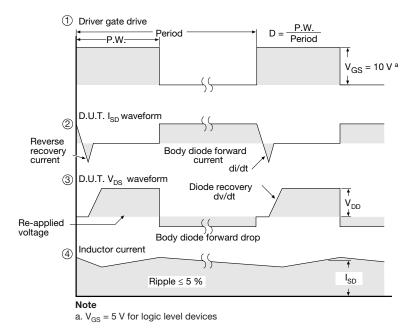


Fig. 14 - For N-Channel

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## TO-220-1



DIM.	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
Α	4.24	4.65	0.167	0.183
b	0.69	1.02	0.027	0.040
b(1)	1.14	1.78	0.045	0.070
С	0.36	0.61	0.014	0.024
D	14.33	15.85	0.564	0.624
Е	9.96	10.52	0.392	0.414
е	2.41	2.67	0.095	0.105
e(1)	4.88	5.28	0.192	0.208
F	1.14	1.40	0.045	0.055
H(1)	6.10	6.71	0.240	0.264
J(1)	2.41	2.92	0.095	0.115
L	13.36	14.40	0.526	0.567
L(1)	3.33	4.04	0.131	0.159
ØΡ	3.53	3.94	0.139	0.155
Q	2.54	3.00	0.100	0.118

### Note

DWG: 6031

•  $M^* = 0.052$  inches to 0.064 inches (dimension including protrusion), heatsink hole for HVM



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