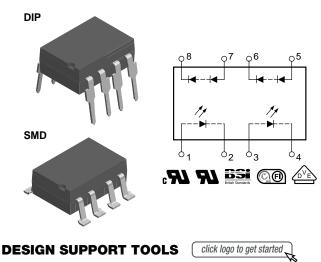


## LH1262CAC, LH1262CACTR, LH1262CB

Vishay Semiconductors

## **Dual Photovoltaic MOSFET Driver Solid-State Relay**





DESCRIPTION

The LH1262CB, LH1262CAC photovoltaic MOSFET driver consists of two LEDs optically coupled to two photodiode arrays. The photodiode array provides a floating source with adequate voltage and current to drive high-power MOSFET transistors. Optical coupling provides a high I/O isolation voltage. In order to turn the MOSFET off, an external resistance (gate-to-source) is required for gate discharge.

### FEATURES

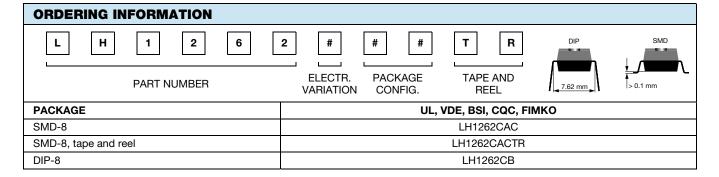
- · High open circuit voltage
- High short circuit current
- Isolation test voltage 5300 V<sub>RMS</sub>
- Logic compatible input
- High reliability
- Material categorization: for definitions of 
   <sup>COMPLIANT</sup>
   compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- High-side driver
- · Solid-state relays
- Floating power supply
- Power control
- Data acquisition
- ATE
- · Isolated switching

### AGENCY APPROVALS

- <u>UL1577</u>
- <u>DIN EN</u>
- <u>BSI</u>
- <u>CQC</u>
- FIMKO



<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
SSR							
LED input ratings continuous forward current		I <sub>F</sub>	50	mA			
LED input ratings reverse voltage	I <sub>R</sub> ≤ 10 μA	V <sub>R</sub>	5.0	V			
Ambient operating temperature range		T <sub>amb</sub>	-40 to +85	°C			
Storage temperature range		T <sub>stg</sub>	-40 to +150	°C			
Pin soldering time	t = 7.0 s max.	Τ <sub>S</sub>	270	°C			

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_{amb}$ = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
LED forward voltage	I <sub>F</sub> = 10 mA	V <sub>F</sub>	1.15	1.26	1.45	V
Detector forward voltage	I <sub>F</sub> = 10 μA	V <sub>F(PDA)</sub>	-	14	-	V
Detector reverse voltage	I <sub>R</sub> = 2.0 μA	V <sub>R(PDA)</sub>	-	200	-	V
Open circuit voltage (pins 5, 6 or 7, 8)	I <sub>F</sub> = 5.0 mA	V <sub>OC</sub>	10	12.95	15	V
	I <sub>F</sub> = 10 mA	V <sub>OC</sub>	-	13.45	-	V
	I <sub>F</sub> = 20 mA	V <sub>OC</sub>	-	13.92	-	V
Short circuit current (pins 5, 6 or 7, 8)	I <sub>F</sub> = 5.0 mA	I <sub>SC</sub>	1.0	1.6	6.5	μA
	I <sub>F</sub> = 10 mA	I <sub>SC</sub>	2.6	3.4	14	μA
	I <sub>F</sub> = 20 mA	I <sub>SC</sub>	-	6.9	-	μA

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Turn-on time	I <sub>F</sub> = 20 mA <sup>(1)</sup>	t <sub>on</sub>	-	35	-	μs
Turn-off time	$I_F = 20 \text{ mA}^{(1)}$	t <sub>off</sub>	-	90	-	μs

#### Note

(1) f = 1.0 kHz, pulse width = 100 µs, load (R<sub>L</sub>) = 1.0 MΩ, 15 pF; measured at 90 % rated voltage (t<sub>on</sub>), 10 % rated voltage (t<sub>off</sub>). Actuation speed depends upon the external t<sub>on</sub> and t<sub>off</sub> circuitry and the capacitance of the MOSFET

SAFETY AND INSULATION RATINGS					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Climatic classification	According to IEC 68 part 1		40 / 85 / 21		
Pollution degree	According to DIN VDE 0109		2		
Comparative tracking index	Insulation group IIIa	CTI	175		
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	5300	V <sub>RMS</sub>	
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V <sub>ISO</sub>	4420	V <sub>RMS</sub>	
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>	
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V <sub>IORM</sub>	890	V <sub>peak</sub>	
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω	
	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = \text{T}_{S}$	R <sub>IO</sub>	≥ 10 <sup>9</sup>	Ω	
Output safety power		P <sub>SO</sub>	700	mW	
Input safety current		I <sub>SI</sub>	300	mA	
Safety temperature		T <sub>S</sub>	175	°C	
Creepage distance (DIP)	DIP-8		≥ 7	mm	
Clearance distance (DIP)	DIF-6		≥7	mm	
Creepage distance (SMD)	SMD-8		≥ 8	mm	
Clearance distance (SMD)	0-0INIC		≥ 8	mm	
Insulation thickness		DTI	≥ 0.4	mm	

#### Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits



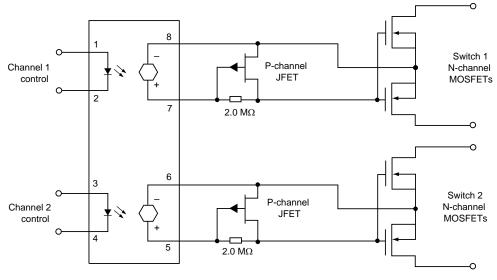


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### FUNCTIONAL DESCRIPTION

Figure 1 outlines the IV characteristics of the illuminated photodiode array (PDA). For operation at voltages below V<sub>OC</sub>, the PDA acts as a nearly constant current source. The actual region of operation depends upon the load.

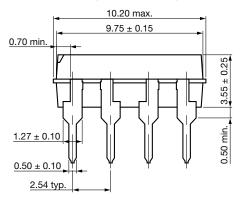
The amount of current applied to the LED (pins 1 and 2 or 3 and 4) determines the amount of light produced for the PDA. For high temperature operation, more LED current may be required.

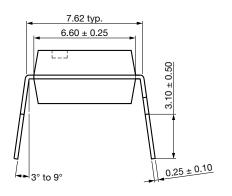


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Fig. 1 - Typical Dual Form A Solid-State Relay Application

#### **PACKAGE DIMENSIONS** (in millimeters)





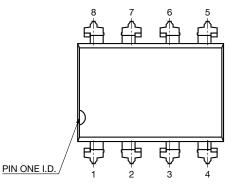


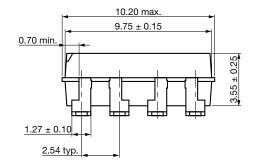
Fig. 2 - DIP-8 Package Drawing

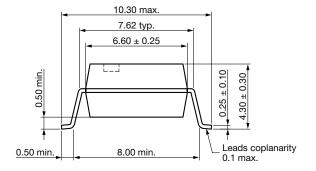
3

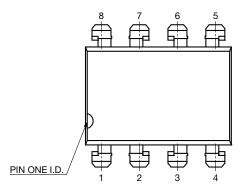


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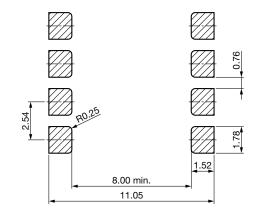
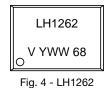


Fig. 3 - SMD-8 Package Drawing

#### **PACKAGE MARKING** (example)



#### Notes

- VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking

4



# LH1262CAC, LH1262CACTR, LH1262CB

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### **SOLDER PROFILES**

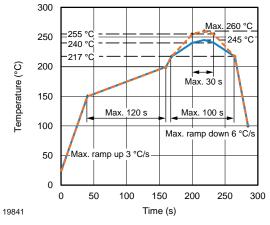
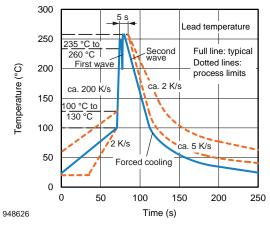
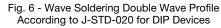


Fig. 5 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices







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