LPE-4841



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Vishay Dale

RoHS

FREE

Surface Mount Transformers/Inductors, Gapped and Ungapped, **Custom Configurations Available**

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FEATURES

 Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

ELECTRICAL SPECIFICATIONS

Inductance Range: 10 μH to 47 000 $\mu H,$ measured at 0.10 V_{RMS} at 10 kHz without DC current, using an HP 4263A or HP 4284A COMPLIANT HALOGEN impedance analyzer

DC Resistance Range: 0.03 Ω to 19.1 Ω , measured at +25 °C ± 5 °C

Rated Current Range: 2.00 A to 0.09 A

Dielectric Withstanding Voltage: 500 V_{RMS}, 60 Hz, 5 s

	IND.			DCR MAX.	MAX. RATED DC CURRENT	SATURATING CURRENT
MODEL	(μH)	IND. TOL.	SCHEMATIC LETTER		(A) $^{(1)}$	(A) ⁽²⁾
LPE4841ER101NU	100	± 30 %	А	0.17	0.88	N/A
LPE4841ER151NU	150	± 30 %	A	0.21	0.79	N/A
LPE4841ER221NU	220	± 30 %	A	0.25	0.721	NI/A
LPE4841ER331NU	330	± 30 %	A	0.30	0.65	N/A N/A
LPE4841ER471NU	470	± 30 %	A	0.36	0.60	
LPE4841ER681NU	680	± 30 %	A	0.44	0.54	N/A
LPE4841ER102NU	1000	± 30 %	A	0.53	0.49	N/A N/A N/A N/A
LPE4841ER152NU	1500	± 30 %	A	0.65	0.45	N/A
LPE4841ER222NU	2200	± 30 %	Â	0.79	0.40	
LPE4841ER332NU	3300	± 30 %	A	1.55	0.29	N/A
LPE4841ER472NU	4700	± 30 %	Â	1.85	0.26	N/A
LPE4841ER682NU	6800	± 30 %	Â	4.36	0.17	N/A
LPE4841ER103NU	10 000	± 30 %	A	5.29	0.16	N/A N/A N/A N/A N/A N/A
LPE4841ER153NU	15 000	± 30 %	A	6.48	0.14	N/A
LPE4841ER223NU	22 000	± 30 %	A	13.1	0.10	N/A
LPE4841ER333NU	33 000	± 30 %	A	16.0	0.09	N/A
LPE4841ER473NU	47 000	± 30 %	A	19.1	0.08	N/A
LPE4841ER100MG	10	± 20 %	В	0.03	2.03	2.320
LPE4841ER150MG	15	± 20 %		0.04	1.84	1.925
LPE4841ER220MG	22	± 20 %	С	0.07	1.32	1.610
LPE4841ER330MG	33	± 20 %	B C C	0.09	1.20	1 000
LPE4841ER470MG	47	± 20 %	D	0.13	0.98	1,125
LPE4841ER680MG	68	± 20 %	D	0.21	0.79	0.941 0.781 0.641 0.532
LPE4841ER101MG	100	± 20 %	E	0.35	0.58	0.781
LPE4841ER151MG	150	± 20 %	E	0.48	0.52	0.641
LPE4841ER221MG	220	± 20 %	E	0.73	0.42	0.532
LPE4841ER331MG	330	± 20 %	E	1.14	0.34	
LPE4841ER471MG	470	± 20 %		1.36	0.31	0.366
LPE4841ER681MG	680	± 20 %	E	2.07	0.25	0.436 0.366 0.305
LPE4841ER102MG	1000	± 20 %		3.15	0.20	0.252
LPE4841ER152MG	1500	± 20 %		4.76	0.16	0.206
LPE4841ER222MG	2200	± 20 %	E	7.29	0.13	0.170
LPE4841ER332MG	3300	± 20 %	E	11.7	0.11	0.139
LPE4841ER472MG	4700	± 20 %	F	17.7	0.09	0.117

(1) DC current that will create a maximum temperature rise of 30 °C when applied at +25 °C ambient
 (2) DC current that will typically reduce the initial inductance by 20 %
 UNGAPPED MODELS: Highest possible inductance with the lowest DCR and highest Q capability. Beneficial in filter, impedance matching and

Ine coupling devices GAPPED MODELS: Capable of handling large amounts of DC current, tighter inductance tolerance with better temperature stability than ungapped models. Beneficial in DC/DC converters or other circuits carrying DC currents or requiring inductance stability over a temperature range

DES	CRIPT	ION						
LPE	4841	1000 μH	± 30 %	Α	ER		e2	
MODE	L SIZE	INDUCTANCE VALUE	INDUCTANCE TOLE	RANCE CORE	PACKAGE CODE	JEDEC LEA	D (Pb)-FRE	E STANDARD
GLO	BAL P	ART NUMBER						
	L	P E 4	8 4 1	EF	1 0	2	Ν	U
	PRODUC	CT FAMILY	SIZE	PACKAGE C	ODE INDUCTAN	CE VALUE	TOL.	CORE
Note	e ie aleo a	vailable with SnPh termin	ations by using package	code BV for tan	and real (in place of	EB) or SM for	bulk (in plac	e of EB)

Revision: 30-Apr-2019

1 For technical questions, contact: magnetics@vishay.com Document Number: 34064

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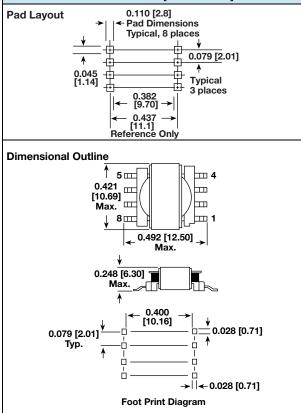
LPE-4841

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DIMENSIONS in inches [millimeters]



Notes

- Pad layout guidelines per MIL-STD-275E (printed wiring for electronic equipment)
- Tolerances: $xx \pm 0.01^{"}$ [± 0.25 mm]; $xxx \pm 0.005^{"}$ [± 0.12 mm]
- The underside of these components contains metal and thus should not come in contact with active circuit traces

PACKAGING

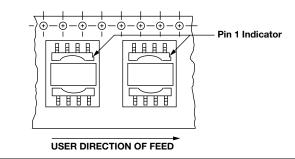
TAPE SPECIFICATIONS:

Carrier tape type: conductive Cover tape type: anti-static Cover tape adhesion to carrier: $40 \text{ g} \pm 30 \text{ g}$

REEL SPECIFICATIONS:

Diameter (flange): 13" [330.2 mm] Maximum width (over flanges): 1.197" [30.4 mm]

Tape and Reel Orientation



Ν	ote
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Top view shown with cover tape rem

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	Label Area	Embossed Cavity
noved		

SCHEMATIC (top view)					
Schema	atic A	Scher	natic B	Schematic C	
5 ©	_	5 @	<u> </u>	50 04	
6 ©	ξ © 3	6 @	∽∽_ீം 3	6 o	
7 ©	S ⊗ 2	7 @	ാപം 5 മ	7 @	
8 ©	ື່ ¹ ອ 1	8 @	∽∽® 1	8 @® 1	
Schematic D			Schematic E		
5 ©		© 4	5 0	• 4	
6 ©		© 3	6 ©	• 3	
7 œ	·····	- ©2	7 0	°© 2	
8 @	~~~~~	└ _ @ 1	8 ©—	° 1	
Note					

· Schematic A is for ungapped LPE series

ENVIRONMENTAL PERFORMANCE TEST CONDITIONS Thermal cycling Withstands -55 °C to +125 °C -55 °C to +125 °C (1) **Operating temperature High humidity** 85 % Soldering heat Tested to +230 °C Mechanical shock Per MIL-STD-202, method 213 (100G) Per MIL-STD-202, method 204 (20G) Vibration Per industry standards Solderability

Note

PART MARKING

- Vishay Dale
- Date code
- Marking code (suffix of model #)
- Pin 1 indicator

STANDARDS: All embossed carrier tape packaging will be accomplished in compliance with latest revision of EIA-481 "Taping of Surface Mount Components for Automatic Placement"

Cover Tape

Carrier Tape

MODEL	TAPE WIDTH	COMPONENT PITCH	UNITS PER 13" REEL
LPE-4841	24 mm	16 mm	600
\bigcirc			

⁽¹⁾ Must be checked in end use application



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